

Features

- Wide 8V to 36V Input Voltage Range
- Output Adjustable from 1.25V to 32V
- Maximum Duty Cycle 100%
- Minimum Drop Out 0.3V
- Fixed 180KHz Switching Frequency
- 4A Constant Output Current Capability
- Internal Optimize Power MOSFET
- High efficiency
- Excellent line and load regulation
- Built in thermal shutdown function
- Built in current limit function
- Built in output short protection function
- Available in TO-252 package

Applications

- LCD Monitor and LCD TV
- Portable instrument power supply
- Telecom / Networking Equipment

General Description

The HM3103 is a 180 KHz fixed frequency PWM buck (step-down) DC/DC converter, capable of driving a 4A load with high efficiency, low ripple and excellent line and load regulation. Requiring a minimum number of external components, the regulator is simple to use and include internal frequency compensation and a fixed-frequency oscillator.

The PWM control circuit is able to adjust the duty ratio linearly from 0 to 100%. An over current protection function is built inside. When short protection function happens, the operation frequency will be reduced from 180KHz to 48KHz. An internal compensation block is built in to minimize external component count.

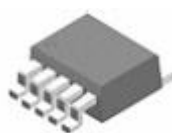


Figure1. Package Type of HM3103

Pin Configurations

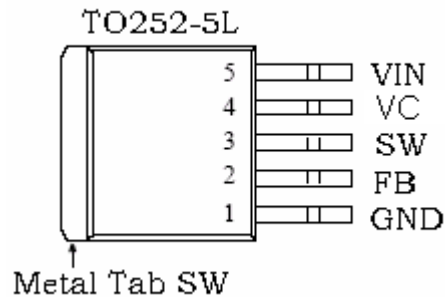


Figure2. Pin Configuration of HM3103 (Top View)

Table 1 Pin Description

Pin Number	Pin Name	Description
1	GND	Ground Pin. Care must be taken in layout. This pin should be placed outside of the Schottky Diode to output capacitor ground path to prevent switching current spikes from inducing voltage noise into HM3103.
2	FB	Feedback Pin (FB). Through an external resistor divider network, FB senses the output voltage and regulates it. The feedback threshold voltage is 1.25V.
3	SW	Power Switch Output Pin (SW). SW is the switch node that supplies power to the output.
4	VC	Internal Voltage Regulator Bypass Capacity. In typical system application, The VC pin connect a 1uf capacity to VIN.
5	VIN	Supply Voltage Input Pin. HM3103 operates from a 8V to 36V DC voltage. Bypass Vin to GND with a suitably large capacitor to eliminate noise on the input.

Function Block

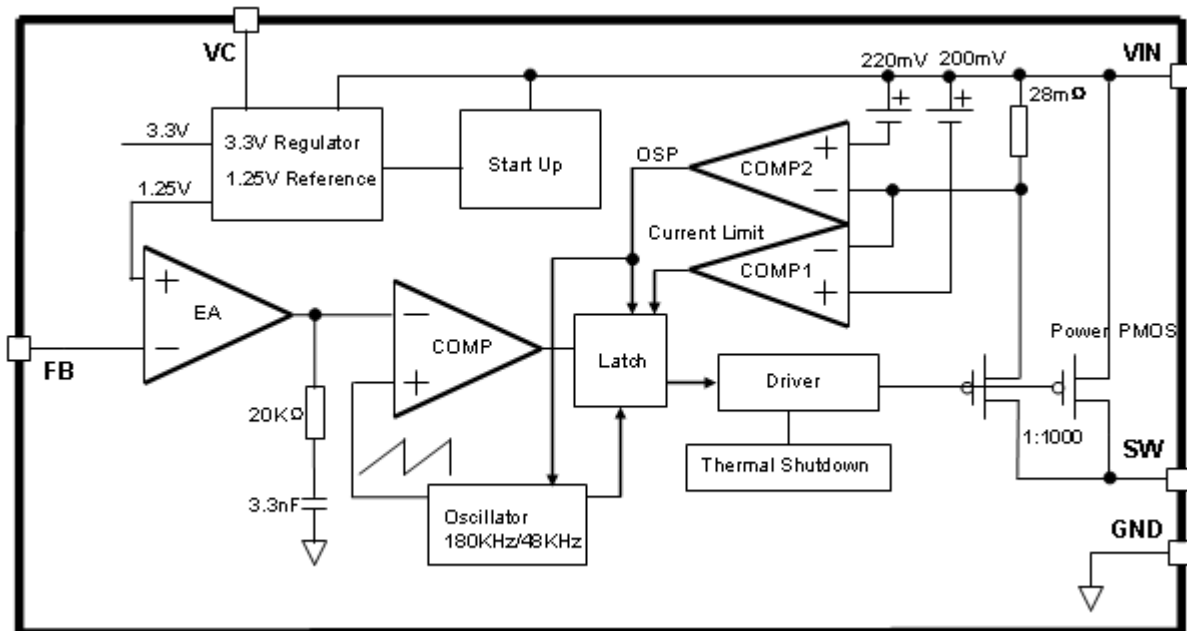


Figure3. Function Block Diagram of HM3103

Typical Application Circuit

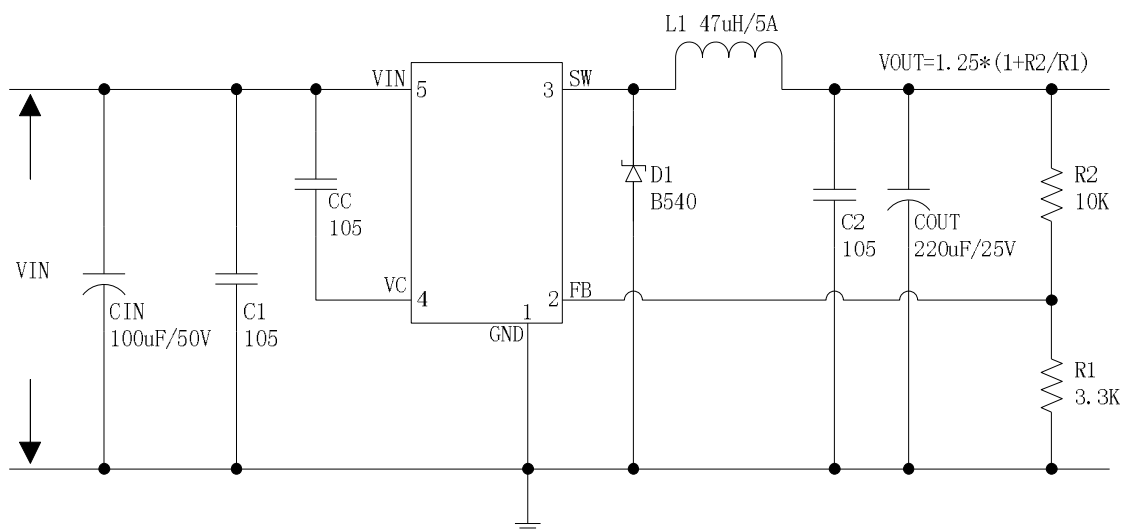


Figure4. HM3103 Typical Application Circuit (VIN=8V~36V, VOUT=5V/4A)

Ordering Information

Package	Temperature Range	Part Number	Marking ID	Packing Type
		Lead Free	Lead Free	
		HM3103	HM3103	

Absolute Maximum Ratings (Note1)

Parameter	Symbol	Value	Unit
Input Voltage	V_{in}	-0.3 to 40	V
Feedback Pin Voltage	V_{FB}	-0.3 to V_{in}	V
Output Switch Pin Voltage	V_{Output}	-0.3 to V_{in}	V
Power Dissipation	P_D	Internally limited	mW
Thermal Resistance (TO252) (Junction to Ambient, No Heatsink, Free Air)	R_{JA}	50	°C/W
Operating Junction Temperature	T_J	-40 to 125	°C
Storage Temperature	T_{STG}	-65 to 150	°C
Lead Temperature (Soldering, 10 sec)	T_{LEAD}	260	°C
ESD (HBM)		>2000	V

Note1: Stresses greater than those listed under Maximum Ratings may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operation is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

HM3103 Electrical Characteristics

$T_a = 25^\circ\text{C}$; unless otherwise specified.

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
<i>System parameters test circuit figure4</i>						
VFB	Feedback Voltage	$V_{in} = 8\text{V to } 36\text{V}, V_{out}=5\text{V}$ $I_{load}=0.5\text{A to } 4\text{A}$	1.225	1.25	1.275	V
Efficiency	η	$V_{in}=12\text{V}, V_{out}=5\text{V}$ $I_{out}=4\text{A}$	-	86	-	%
Efficiency	η	$V_{in}=24\text{V}, V_{out}=12\text{V}$ $I_{out}=3\text{A}$	-	94	-	%

Electrical Characteristics (DC Parameters)

$V_{in} = 12\text{V}$, $GND=0\text{V}$, V_{in} & GND parallel connect a $100\mu\text{f}/50\text{V}$ capacitor; $I_{out}=500\text{mA}$, $T_a = 25^\circ\text{C}$; the others floating unless otherwise specified.

Parameters	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Input operation voltage	V_{in}		8		36	V
Quiescent Supply Current	I_q	$V_{FB} = V_{in}$		2.1	5	mA
Oscillator Frequency	F_{osc}		144	180	216	KHz
Output Short Frequency	F_{osp}			48		KHz
Switch Current Limit	I_L	$V_{FB} = 0$		7		A
Max. Duty Cycle	D_{MAX}	$V_{FB}=0\text{V}$		100		%
Output Power PMOS	R_{dson}	$V_{FB}=0\text{V}, V_{in}=12\text{V},$ $I_{SW}=4\text{A}$		60	80	mohm

Typical System Application (VOUT=5V/4A)

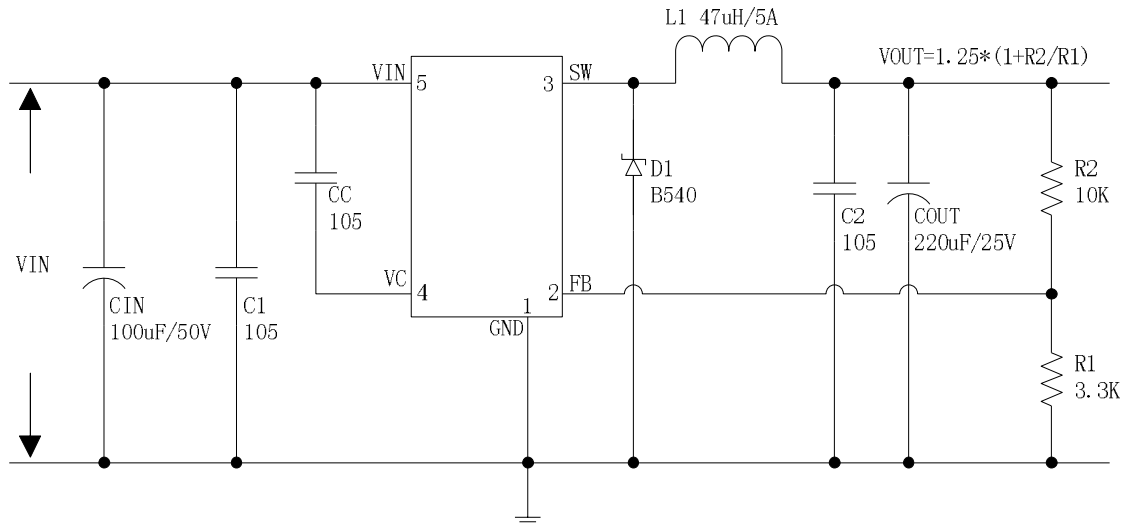


Figure5. HM3103 System Parameters Test Circuit (VIN=8V~36V, VOUT=5V/4A)

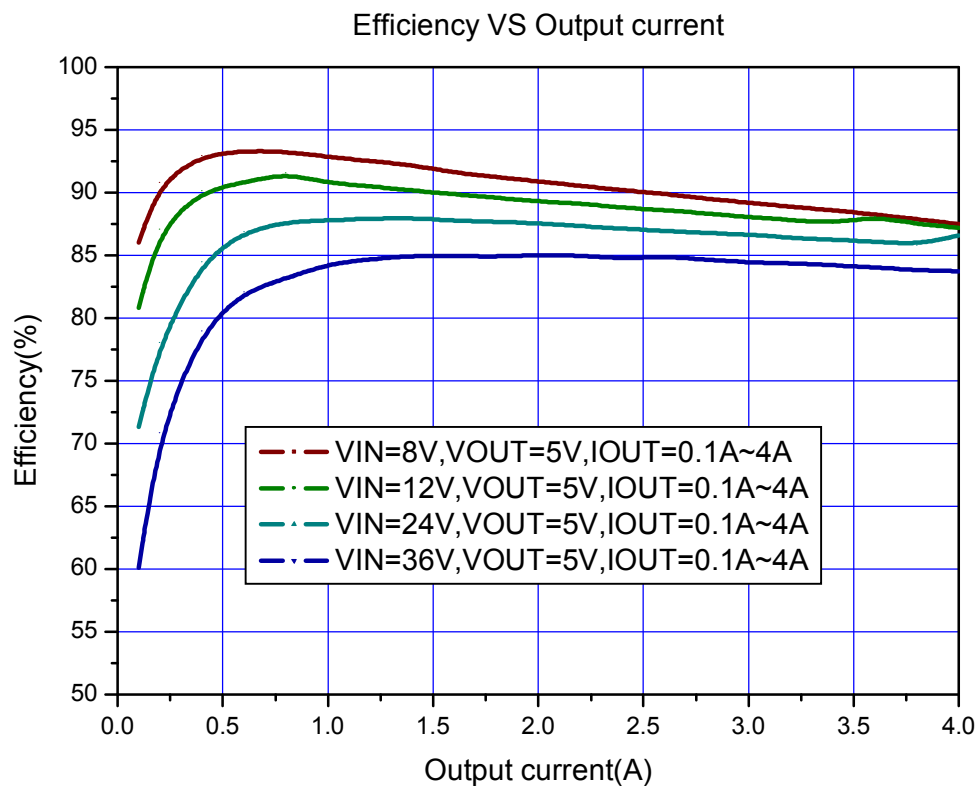


Figure6. HM3103 System Efficiency Curve

Typical System Application (VOUT=12V/3A)

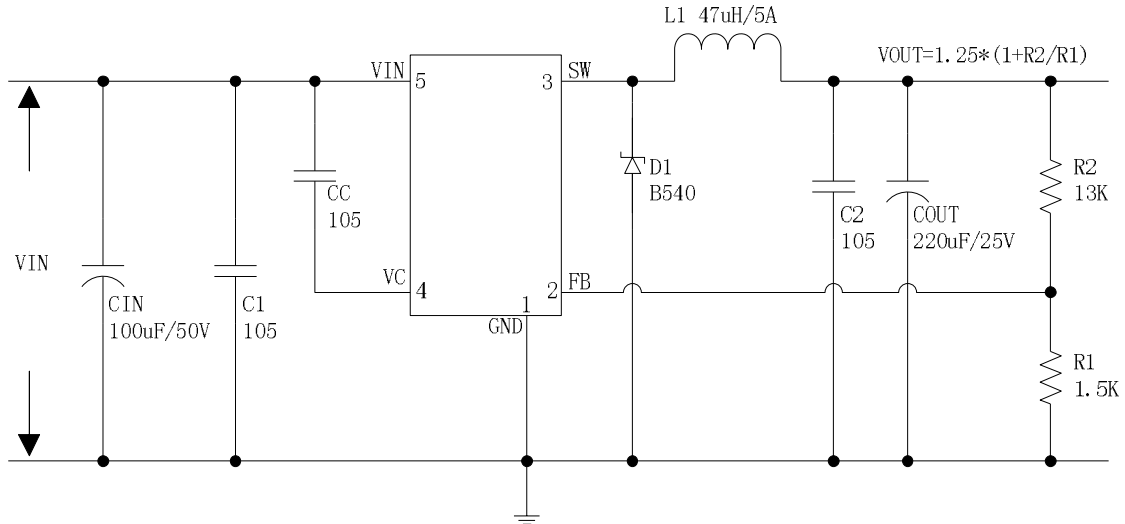


Figure7. HM3103 System Parameters Test Circuit (VIN=15V~36V, VOUT=12V/3A)

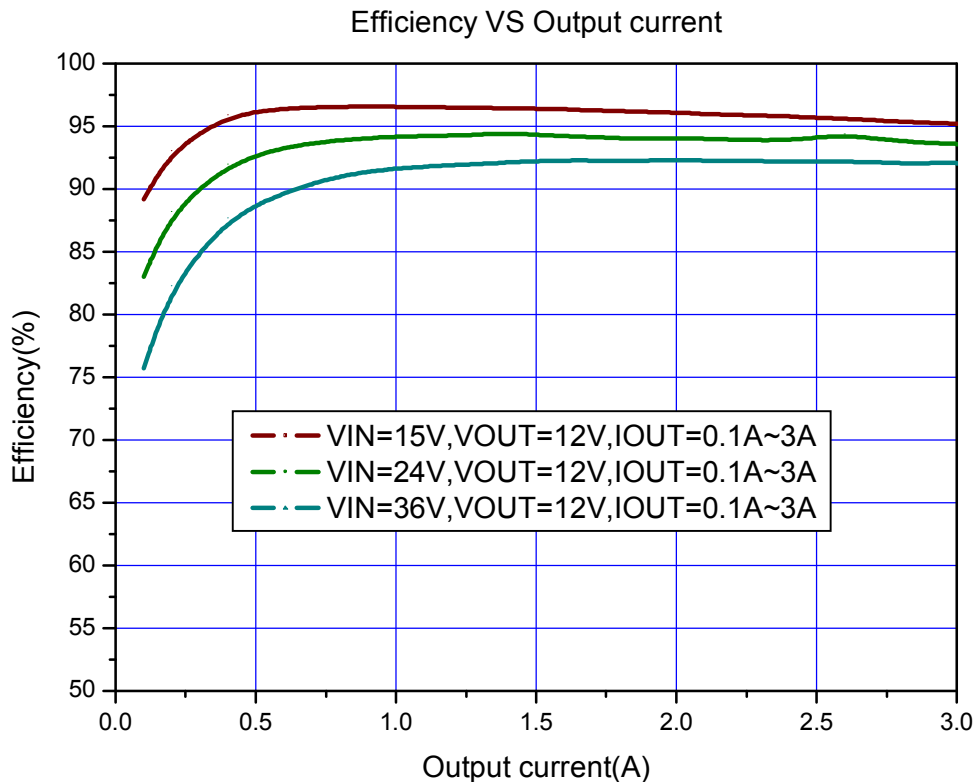


Figure8. HM3103 System Efficiency Curve

Typical System Application (With Enable function)

Logic level signal shutdown function can be used in typical system application with external components. When the TTL high voltage above 3.3V(referenced to ground, lower than VIN), the converter will shutdown, input current less than 3mA; when the TTL Low voltage below 0.8V(referenced to ground), the converter will turn on.

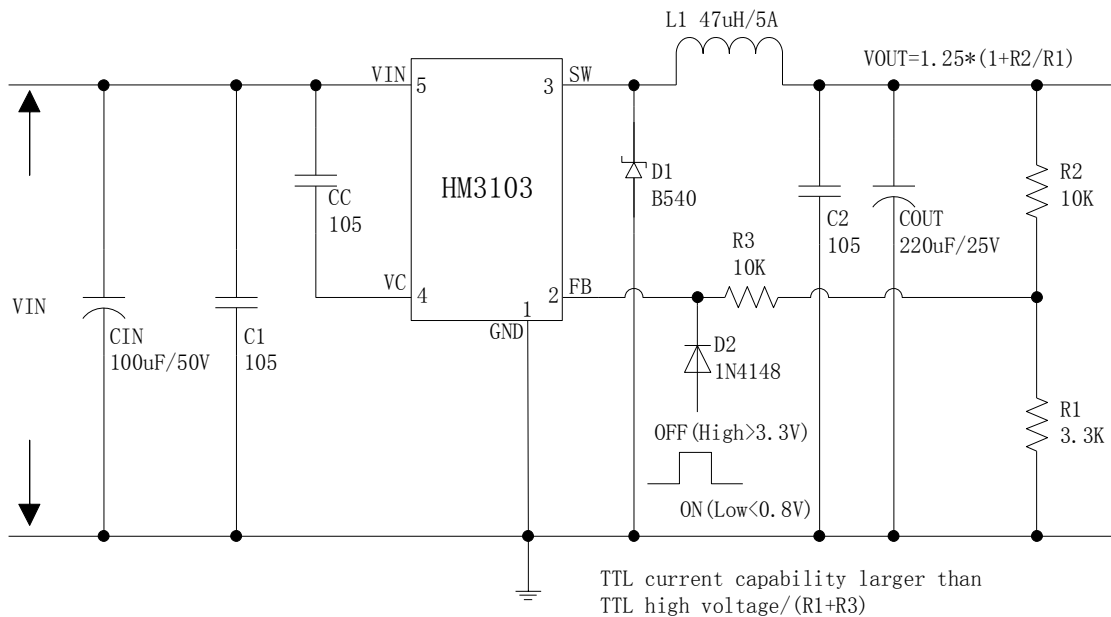
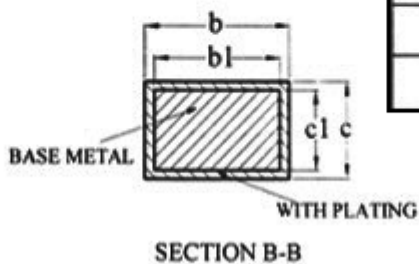
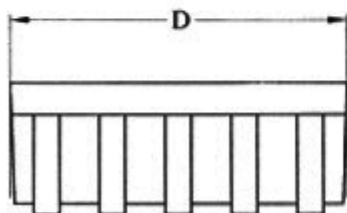
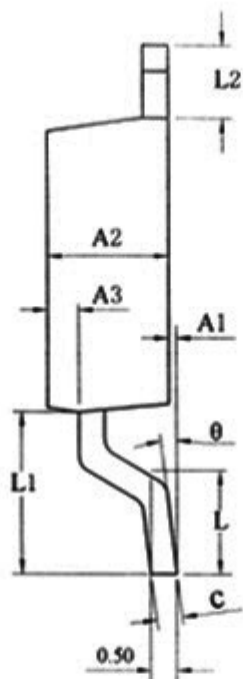
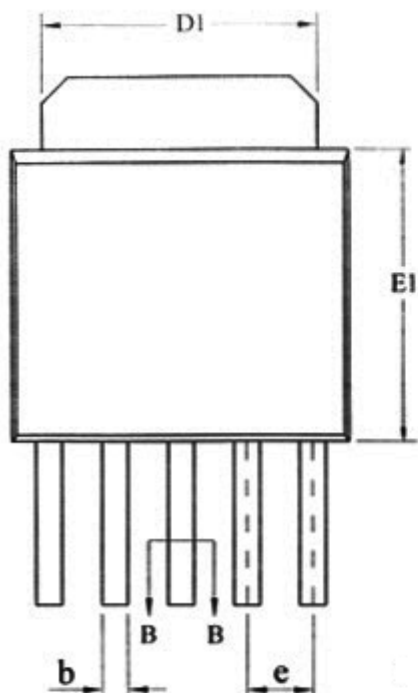


Figure9. HM3103 Typical Application Circuit with Enable Function

Package Information

TO252-5L



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A1	0.05	0.15	0.25
A2	2.10	2.30	2.50
A3	0.50	0.60	0.70
b	0.46	—	0.60
bl	0.45	0.50	0.55
c	0.49	—	0.56
c1	0.48	0.50	0.52
D	6.30	6.50	6.70
D1	5.30REF		
E1	5.30	5.50	5.70
e	1.27BSC		
L	1.40	1.50	1.60
L1	3.00	3.10	3.30
L2	1.40BSC		
θ	0	—	8°