

Features

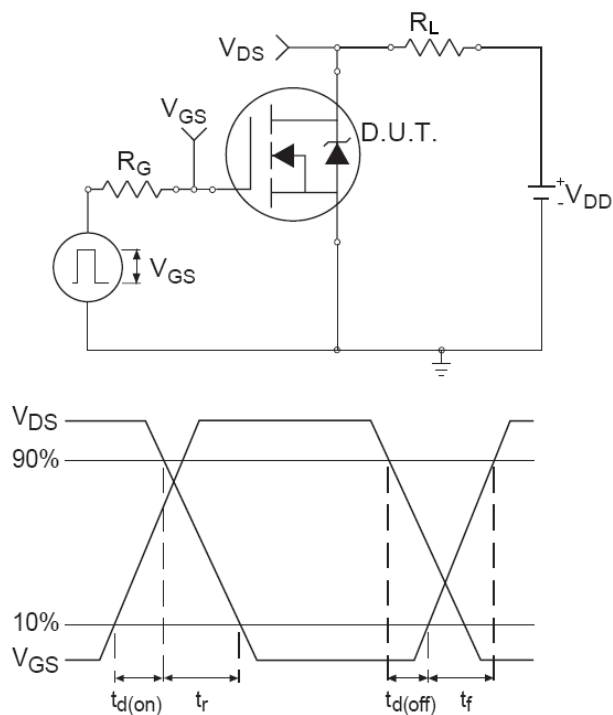
- $V_{DS}=100V/V_{GS}=\pm 20V/I_D=25A$
 $R_{DS(ON)}=37m\Omega(max.)@V_{GS}=10V$
- Reliable and Rugged
- Advanced trench process technology
- High Density Cell Design For Low On-Resistance

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Applications

- Power Management in Inverter System
- Boost for LED Backlight

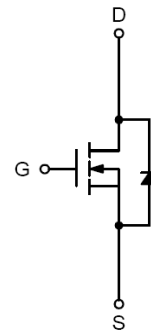
Switching Time Test Circuit and Waveforms



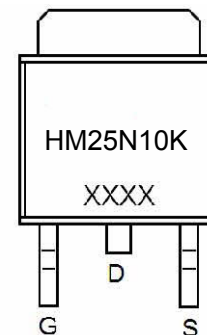
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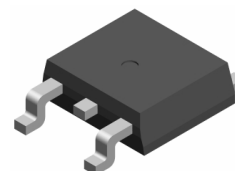
Pin Description



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Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM25N10K	HM25N10K	TO-252-2L		-	-

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter		Typical	Unit
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate –Source Voltage		±20	V
I _D	Continuous Drain Current	T _C =100°C	25	A
		T _C =25°C	16	A
I _{DP}	300us Pulsed Drain Current Tested		75	A
I _S	Diode Continuous Forward Current		25	A
T _J	Operating Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 ~ 150	°C

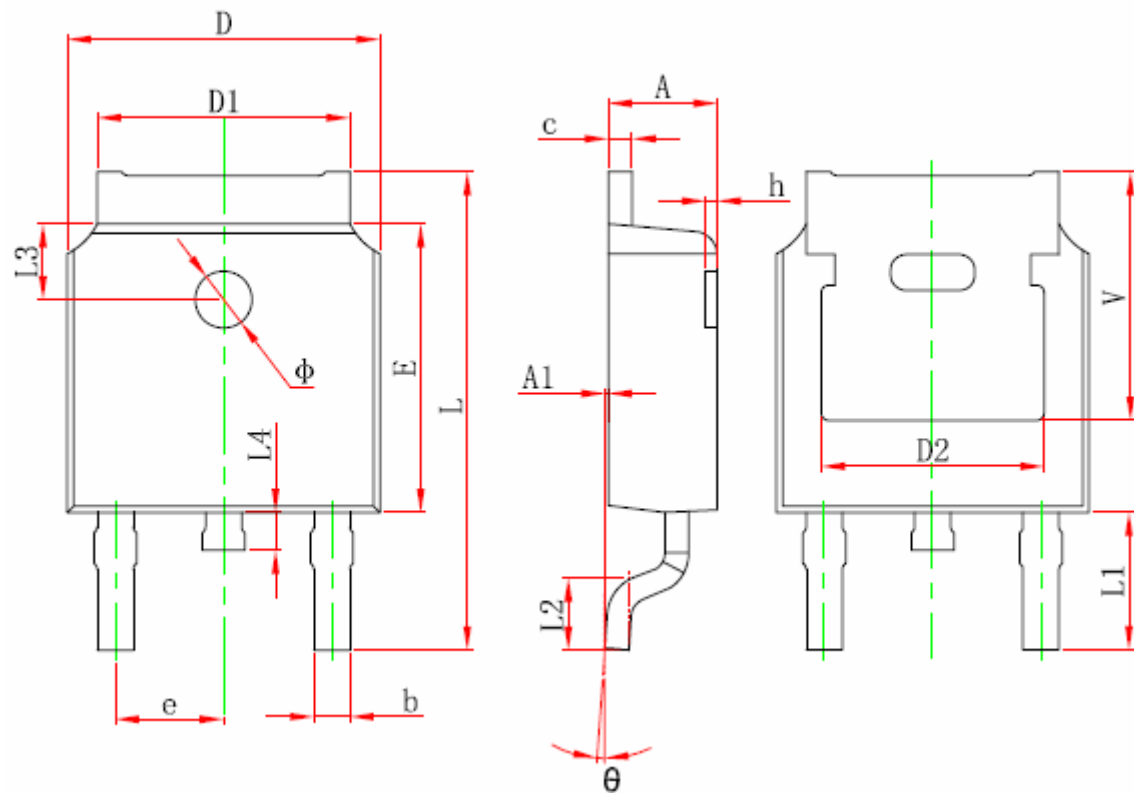
Electrical Characteristics (T_A=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D =250uA	100			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V,V _{GS} =0V			1	uA
		T _J =125°C			100	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =250uA	2	3.3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
R _{DS(on)} ¹	Drain-Source On-Resistance	V _{GS} =10V, I _D =12A		33	37	mΩ
Diode Characteristics						
V _{SD} ¹	Diode Forward Voltage	I _{SD} =12A,V _{GS} =0V			1.1	V
t _{rr}	Reverse Recovery Time	I _{SD} =12A,		60		ns
Q _{rr}	Reverse Recovery Charge	diF/dt=100A/us		90		nC
Dynamic Characteristics ²						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Frequency=1MHz		1.4		Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V Frequency=1MHz		2000		pF
C _{oss}	Output Capacitance			450		
C _{rss}	Reverse Transfer Capacitance			260		
t _{d(on)}	Turn-On Delay Time	V _{DD} =50V, R _L =30Ω I _D =1.0A, V _{GEN} =10V R _G =6Ω		25		ns
t _r	Turn-On Rise Time			18		
t _{d(off)}	Turn-Off Delay Time			60		
t _f	Turn-Off Fall Time			78		
Gate Charge Characteristics ²						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V I _D =12A		50		nC
Q _{gs}	Gate-Source Charge			13.5		
Q _{gd}	Gate-Drain Charge			11		

Note:

- 1: Pulse test ; pulse width ≤ 300ns, duty cycle ≤ 2%.
- 2: Guaranteed by design, not subject to production testing.

TO-252-2L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 REF.		0.211 REF.	

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