### N and P-Channel Enhancement Mode Power MOSFET

## **Description**

The PT I  $\hat{I}$   $\hat{F}$  Œuses advanced trench technology to provide excellent  $R_{DS(ON)}$  and low gate charge . The SOP-8 package is universally preferred for all commercial industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

#### **General Features**

N-Channel

 $V_{DS} = 30V, I_{D} = 1\dot{I}$  A

 $R_{DS(ON)}$  < 7.5m $\Omega$  @ VGS=10V

 $R_{DS(ON)}$  < 10m $\Omega$  @ VGS=5V

P-Channel

 $V_{DS} = -30V, I_{D} = -FGA$ 

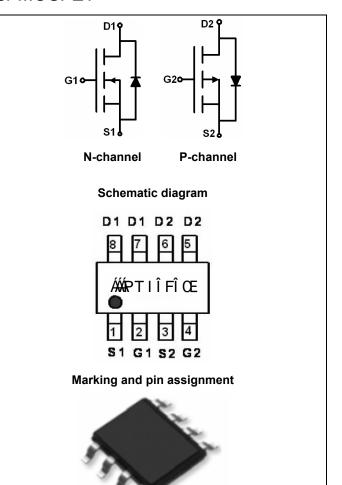
R<sub>DS(ON)</sub> < GÍ { ô ÁO ÁX ÕÙMË LĚ XÁ

R<sub>DS(ON)</sub> < FÎ { ô ÁO Á XÕÙM HE</sub>€X

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

### **Application**

- Battery protection
- Load switch
- Power management



SOP-8 top view

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
PT I Î FÎ Œ	PT I Î FÎ Œ	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

Paramete	Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	-30	V
Gate-Source Voltage		V <sub>GS</sub>	±20	±20	V
Continuous Drain Current	T <sub>A</sub> =25℃	- I <sub>D</sub>	18	-12	Α
Continuous Drain Current	T <sub>A</sub> =70℃		13	-9	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	50	-48	Α
Maximum Power Dissipation T <sub>A</sub> =25 ℃		P <sub>D</sub>	3	3	W
Operating Junction and Storage Temperature Range		$T_{J}$ , $T_{STG}$	-55 To 150	-55 To 150	$^{\circ}$ C



## **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note2)	P	N-Ch	50	°C/W	
Thermal Nesistance, Junction-to-Ambient (Note2)	K <sub>⊕JA</sub>	P-Ch	50	CIVV	

# N-CH Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA	30	33	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μA	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.6	3	V	
Drain-Source On-State Resistance	В	V <sub>GS</sub> =10V, I <sub>D</sub> =9A	-	5.5	7.5	mΩ	
Dialii-Source Oil-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =5V, I <sub>D</sub> =9A	-	7.5	10		
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =18A	5	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C <sub>lss</sub>	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V,	-	2100	-	PF	
Output Capacitance	Coss	V <sub>DS</sub> =15V,V <sub>GS</sub> =0V, F=1.0MHz	-	460	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	r-1.0WInz	-	230	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t <sub>d(on)</sub>		-	20	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =10 $V$ , $I_D$ =9 $A$	-	15	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =2.7 $\Omega$	-	60	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	10	-	nS	
Total Gate Charge	Qg	)/ 40\/ L 40A	-	41	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}=10V,I_{D}=10A,$	-	14	-	nC	
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	11	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =9A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	18	Α	

**/** 

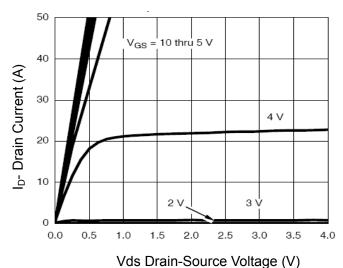
# P-CH Electrical Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA		-33	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =-250μA	-1	-1.5	-3	V
Duit On the Original Provides	-	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A	-	11.5	15	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-7A	-	18	25	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =-10V,I <sub>D</sub> =-10A	20	-	-	S
Dynamic Characteristics (Note4)	1	1	ı	l.		
Input Capacitance	C <sub>lss</sub>	15/1/ 01/	-	1750	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-15V, $V_{GS}$ =0V,	-	215	-	PF
Reverse Transfer Capacitance	$C_{rss}$	F=1.0MHz	-	180	-	PF
Switching Characteristics (Note 4)		-	ı	l		
Turn-on Delay Time	$t_{d(on)}$		_	9	-	nS
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =-15V, ID=-10A,	-	8	-	nS
Turn-Off Delay Time	$t_{\sf d(off)}$	$V_{GS}$ =-10V, $R_{GEN}$ =1 $\Omega$	_	28	-	nS
Turn-Off Fall Time	t <sub>f</sub>	1	_	10	-	nS
Total Gate Charge	Qg		-	24	-	nC
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =-15V,I <sub>D</sub> =-10A,V <sub>GS</sub> =-10V	-	3.5	-	nC
Gate-Drain Charge	$Q_{gd}$	1	-	6	-	nC
Drain-Source Diode Characteristics	1		ı	ı		1
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =-2A	-	-	-1.2	V

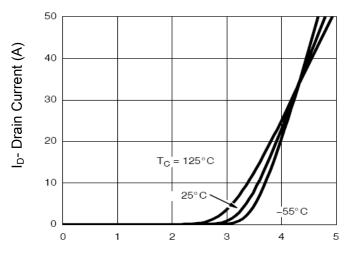
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

## N- Channel Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)

**Figure 2 Transfer Characteristics** 

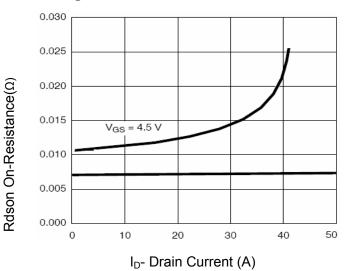


Figure 3 Rdson- Drain Current

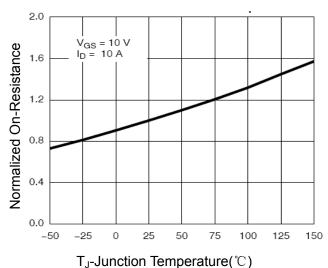


Figure 4 Rdson- Junction Temperature

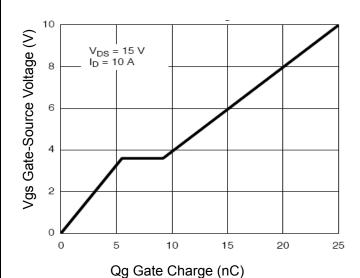


Figure 5 Gate Charge

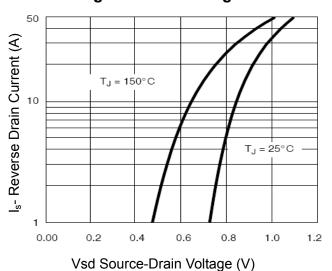


Figure 6 Source- Drain Diode Forward

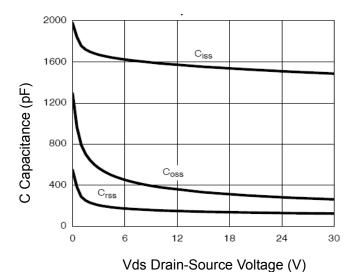


Figure 7 Capacitance vs Vds

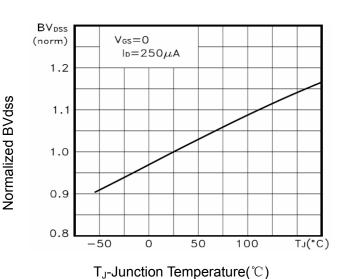
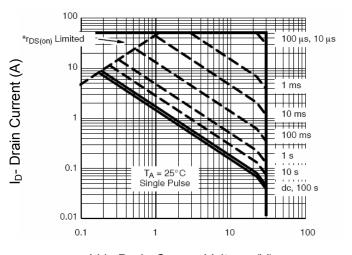


Figure 9 BV<sub>DSS</sub> vs Junction Temperature



Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area

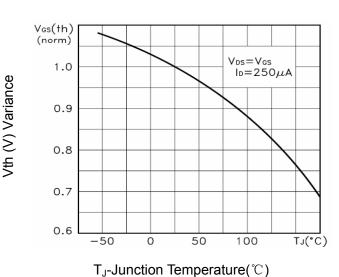


Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

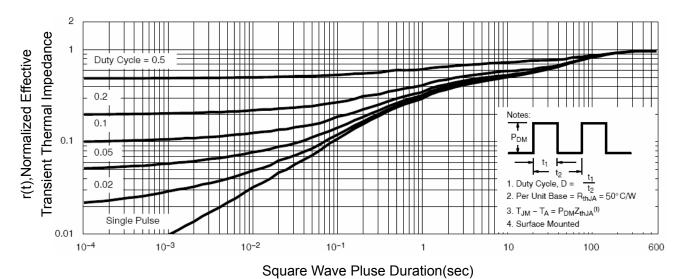
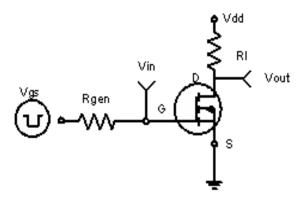


Figure 11 Normalized Maximum Transient Thermal Impedance

## P-Channel Typical Electrical and Thermal Characteristics



**Figure 1:Switching Test Circuit** 

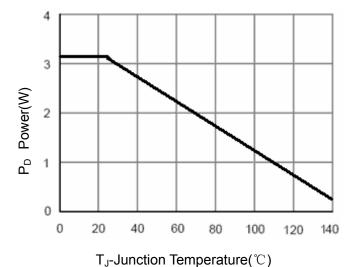
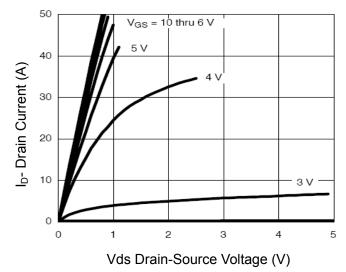


Figure 3 Power Dissipation



**Figure 5 Output Characteristics** 

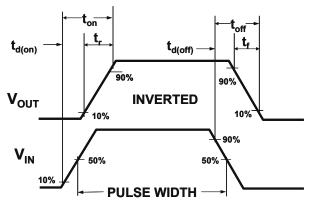


Figure 2:Switching Waveforms

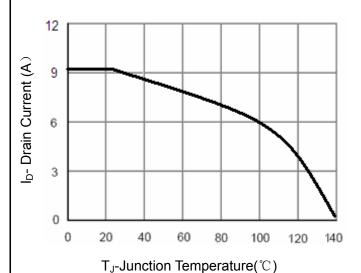


Figure 4 Drain Current

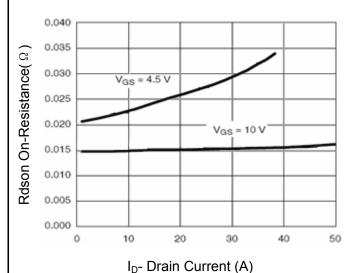
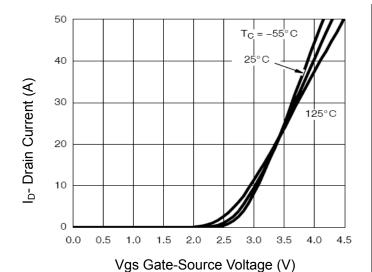
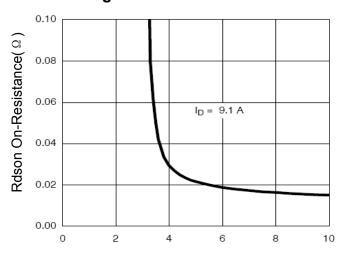


Figure 6 Drain-Source On-Resistance



**Figure 7 Transfer Characteristics** 



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

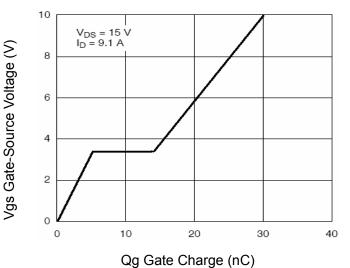


Figure 11 Gate Charge

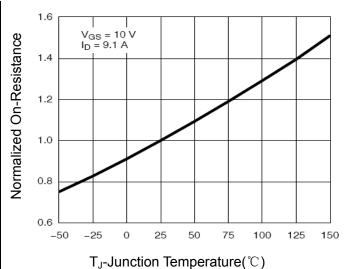
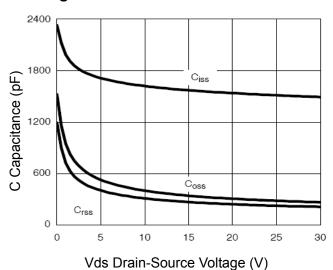


Figure 8 Drain-Source On-Resistance



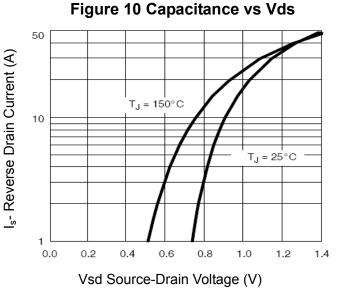
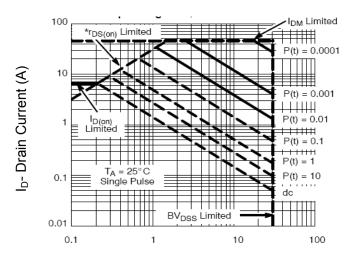
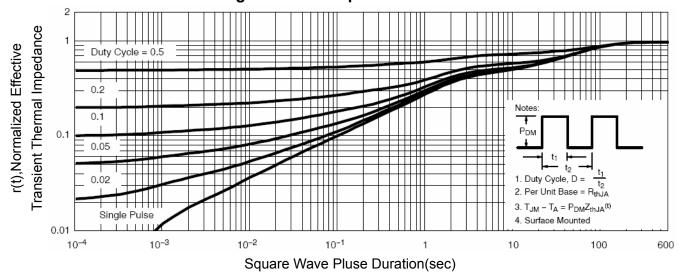


Figure 12 Source- Drain Diode Forward



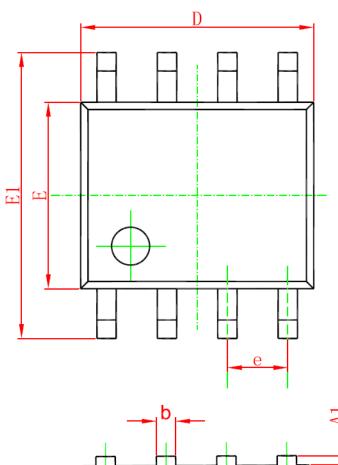
Vds Drain-Source Voltage (V)

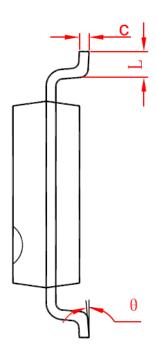
Figure 13 Safe Operation Area

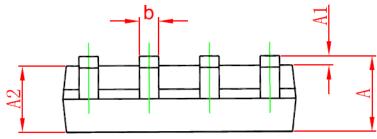


**Figure 14 Normalized Maximum Transient Thermal Impedance** 

# **SOP-8 Package Information**







C. mh a l	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1. 350	1. 550	0. 053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
Е	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0.050	(BSC)	
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	

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