

N and P-Channel Enhancement Mode Power MOSFET

Description

The PT I \hat{I} F \hat{I} uses advanced trench technology to provide excellent R_{DS(ON)} and low gate charge . The SOP-8 package is universally preferred for all commercial industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

General Features

N-Channel

 $V_{DS} = 30V, I_{D} = 10A$

 $R_{DS(ON)}$ < 20m Ω @ V_{GS} =4.5V

 $R_{DS(ON)} < 13.5 \text{m}\Omega$ @ $V_{GS} = 10V$

P-Channel

 $V_{DS} = -30V, I_{D} = -9.1A$

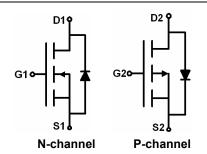
 $R_{DS(ON)} < 35 m\Omega$ @ V_{GS} =-4.5V

 $R_{DS(ON)}$ < 20m Ω @ V_{GS} =-10V

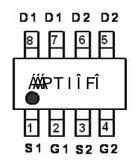
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

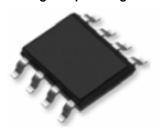
- Battery protection
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOP-8 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
PTTÎFÎ	PT I Î FÎ	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Paramete	Symbol	N-Channel	P-Channel	Unit		
Drain-Source Voltage		V _{DS}	30	-30	V	
Gate-Source Voltage	V _{GS}	±20	±20	V		
Continuous Drain Current	T _A =25°C	1	10	-9.1	^	
Continuous Drain Current	T _A =70℃	I _D	7.9	-7.2	Α	
Pulsed Drain Current (Note 1)		I _{DM}	30	-30	Α	
Maximum Power Dissipation T _A =25 ℃		P _D	2.5	2.5	W	
Operating Junction and Storage Ten	T_{J} , T_{STG}	-55 To 150	-55 To 150	$^{\circ}$		





Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note2)	D	N-Ch	50	°C/W
Thermal Nesistance, surfiction-to-Ambient (Note2)	$R_{ heta JA}$	P-Ch	50	CIVV

N-CH Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u> </u>		•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	30	33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS},I_{D}=250\mu A$	1	1.6	3	V
Drain Course On Chata Decistores	Б	V _{GS} =10V, I _D =10A	-	7.5	13.5	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =5A	-	11	20	mΩ
Forward Transconductance	g _{FS}	V_{DS} =5 V , I_{D} =10 A	15	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss})/ 45)/)/ O)/	-	1550	-	PF
Output Capacitance	C _{oss}	V_{DS} =15 V , V_{GS} =0 V ,	-	300	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	180	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	30	-	nS
Turn-on Rise Time	t _r	V_{DD} =25 V , I_{D} =1 A	-	20	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =6 Ω	-	100	-	nS
Turn-Off Fall Time	t _f		-	80	-	nS
Total Gate Charge	Qg)/ 45\/\ 40A	-	13	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =10A,	-	5.5	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V	-	3.5	-	nC
Drain-Source Diode Characteristics	1		L			
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =6A	-	0.8	1.2	V





P-CH Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage BV _{DSS} V _{GS} =0		V _{GS} =0V I _D =-250μA	-30	-33	-	٧	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μΑ	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)	•						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS} , I_D =-250 μ A	-1	-1.5	-3	٧	
Drain Course On State Registeres	Б	V _{GS} =-10V, I _D =-9.1A	-	15	20	mΩ	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-5A	-	21	35	mΩ	
Forward Transconductance	g FS	V _{DS} =-15V,I _D =-9.1A	10	-	-	S	
Dynamic Characteristics (Note4)	•						
Input Capacitance	C _{lss}	\/ - 15\/\/ -0\/	-	1600	-	PF	
Output Capacitance	Coss	V _{DS} =-15V,V _{GS} =0V, F=1.0MHz	-	350	-	PF	
Reverse Transfer Capacitance	C _{rss}	r-1.0ivinz	-	300	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	10	-	nS	
Turn-on Rise Time	t _r	V _{DD} =-15V, ID=-1A,	-	15	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{GEN} =6 Ω	-	110	-	nS	
Turn-Off Fall Time	t _f		-	70	-	nS	
Total Gate Charge	Qg	V 45VI 04A	-	30	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =-15V, I_{D} =-9.1A V_{GS} =-10V	-	5.5	-	nC	
Gate-Drain Charge	Q_{gd}	VGS10V	-	8	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =-6A	-	-	-1.2	V	

Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

N- Channel Typical Electrical and Thermal Characteristics (Curves)

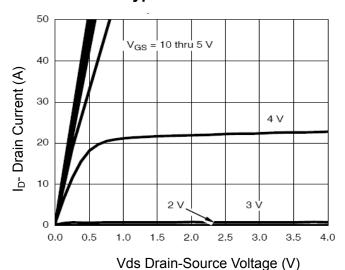
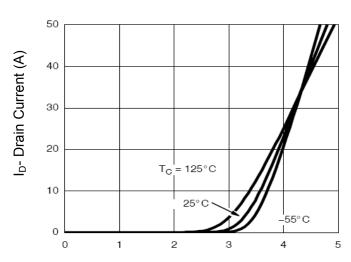


Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)
Figure 2 Transfer Characteristics

0.025

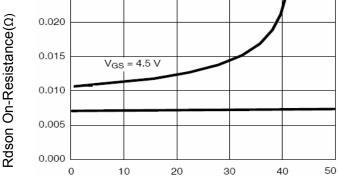


Figure 3 Rdson- Drain Current

I_D- Drain Current (A)

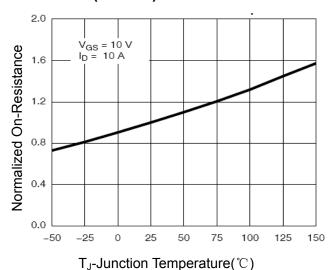


Figure 4 Rdson- Junction Temperature

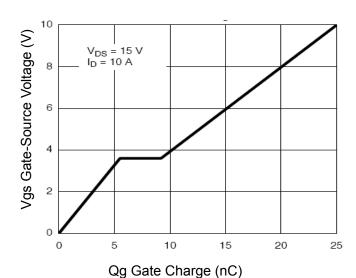


Figure 5 Gate Charge

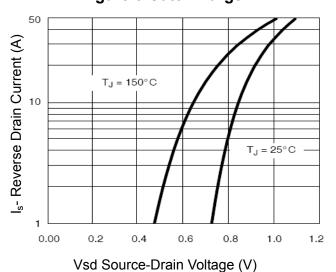
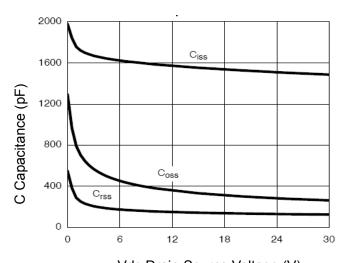
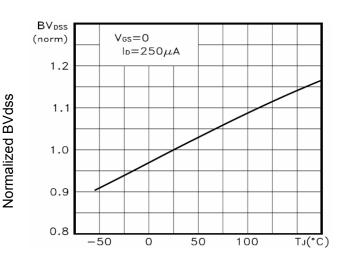


Figure 6 Source- Drain Diode Forward

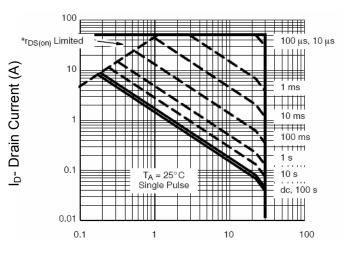




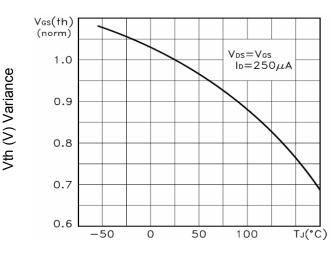
Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds



 T_J -Junction Temperature (°C) Figure 9 BV_{DSS} vs Junction Temperature



Vds Drain-Source Voltage (V)



T_J-Junction Temperature(℃)

Figure 8 Safe Operation Area

Figure 10 V_{GS(th)} vs Junction Temperature

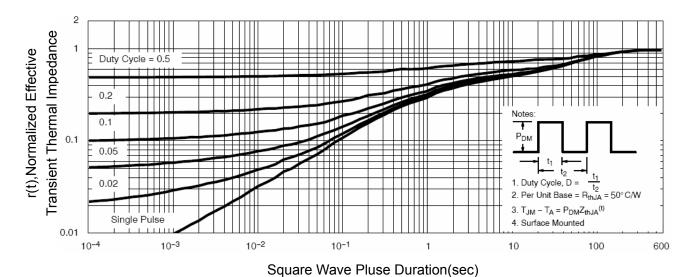


Figure 11 Normalized Maximum Transient Thermal Impedance



P-Channel Typical Electrical and Thermal Characteristics

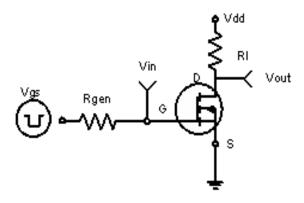


Figure 1:Switching Test Circuit

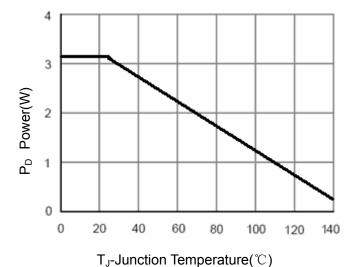


Figure 3 Power Dissipation

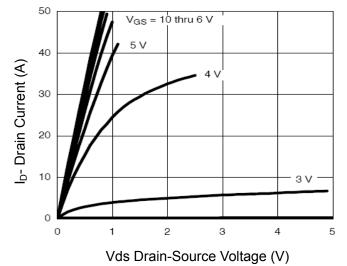


Figure 5 Output Characteristics

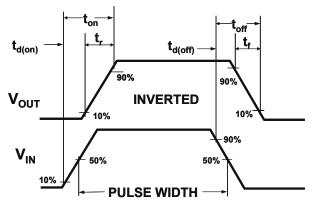


Figure 2:Switching Waveforms

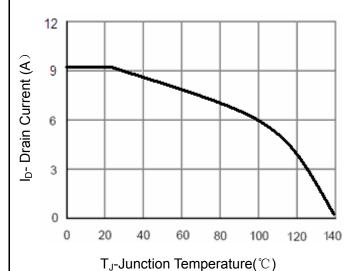


Figure 4 Drain Current

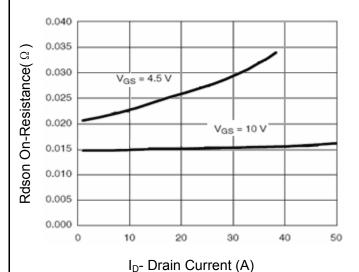
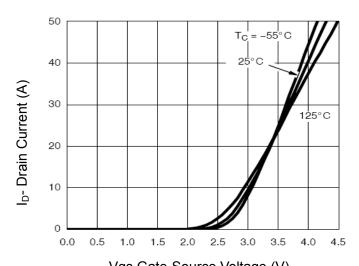
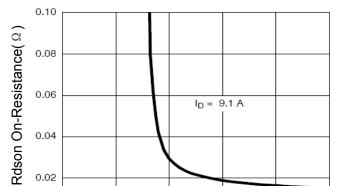


Figure 6 Drain-Source On-Resistance



Vgs Gate-Source Voltage (V)
Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

0.00

0

Figure 9 Rdson vs Vgs

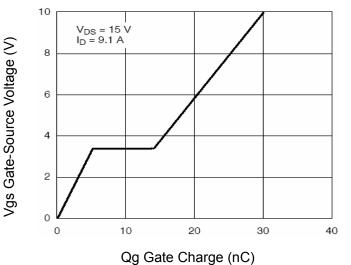


Figure 11 Gate Charge

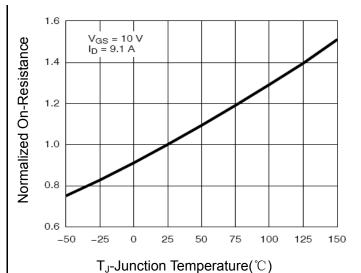


Figure 8 Drain-Source On-Resistance

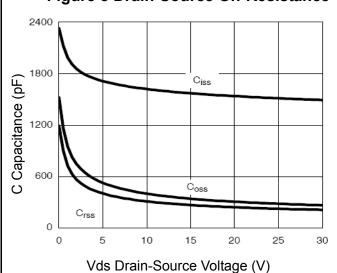


Figure 10 Capacitance vs Vds

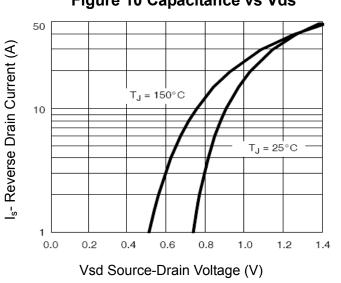
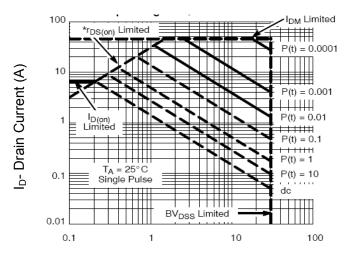


Figure 12 Source- Drain Diode Forward

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Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

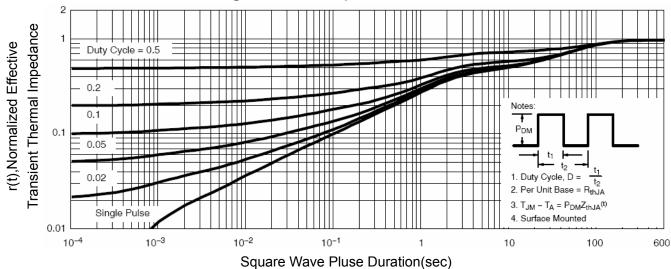
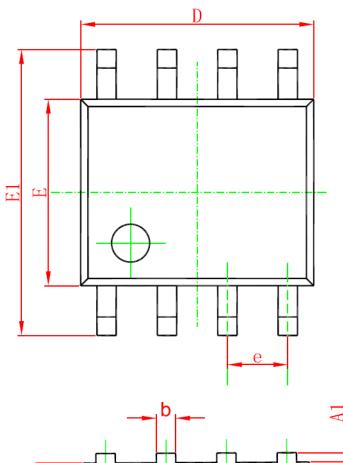
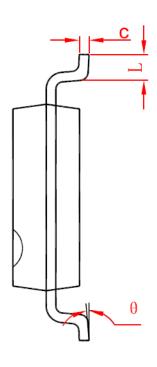


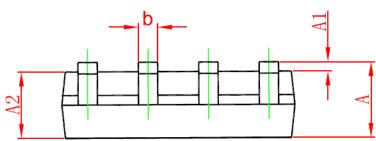
Figure 14 Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information







Comb a l	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1. 350	1. 550	0. 053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
Е	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	





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