N-Channel Enhancement Mode Power MOSFET

Description

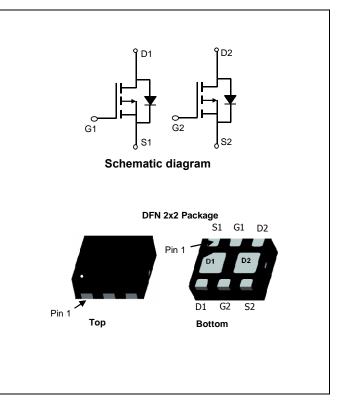
The HM2800D uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a battery protection or in other switching application.

General Features

- $V_{DS} = 20V, I_D = 8.0A$ $R_{DS(ON)} < 40m\Omega @ V_{GS} = 2.5V$ $R_{DS(ON)} < 33m\Omega @ V_{GS} = 4.5V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management



Package Marking and Ordering Information

U	0	0			
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM2800D	HM2800D	DFN2X2-6L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		Vds	20	V
Gate-Source Voltage		Vgs	±12	V
Continuous Drain Current	T _A =25℃	I	8.0	Α
	T _A =70℃	- I _D	6.4	
Drain Current-Pulsed (Note 1)	·	I _{DM}	32	А
Maximum Power Dissipation		PD	6.8	W
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ extsf{ heta}JA}$	100	°C/W

Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	20	22	-	V	

Shenzhen H&M Semiconductor Co.Ltd http: //www.hmsemi.com

H&M 华之美半导体 SEMI www.hmsemi.com

HM2800D

Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	0.5	0.65	1.2	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =2.5V, I _D =4.0 A	-	33	40	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _D =4.5A	-	22	33	mΩ
Forward Transconductance	g fs	V _{DS} =10V,I _D =4A	-	10	-	S
Dynamic Characteristics (Note4)	·			•		
Input Capacitance	Clss		-	500	-	PF
Output Capacitance	C _{oss}	- V _{DS} =8V,V _{GS} =0V, - F=1.0MHz	-	300	-	PF
Reverse Transfer Capacitance	Crss		-	140	-	PF
Switching Characteristics (Note 4)	·			•		
Turn-on Delay Time	t _{d(on)}		-	20	40	nS
Turn-on Rise Time	tr	V _{DD} =10V,I _D =1A	-	18	40	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5V, R_{GEN} =6 Ω	-	60	108	nS
Turn-Off Fall Time	t _f		-	28	56	nS
Total Gate Charge	Qg		-	10	15	nC
Gate-Source Charge	Q _{gs}	V _{DS} =10V,I _D =3A,V _{GS} =4.5V	-	2.3	-	nC
Gate-Drain Charge	Q _{gd}		-	2.9	-	nC
Drain-Source Diode Characteristics		•			•	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =1A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	1	А

Notes:

- 1. Repetitive rating: pulse width limited by maximum junction temperature.
- **2.** Surface mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse test: pulse width \leq 300µs, duty cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

Shenzhen H&M Semiconductor Co.Ltd http://www.hmsemi.com

Typical Electrical and Thermal Characteristics

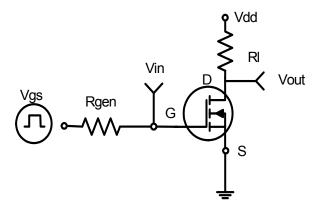
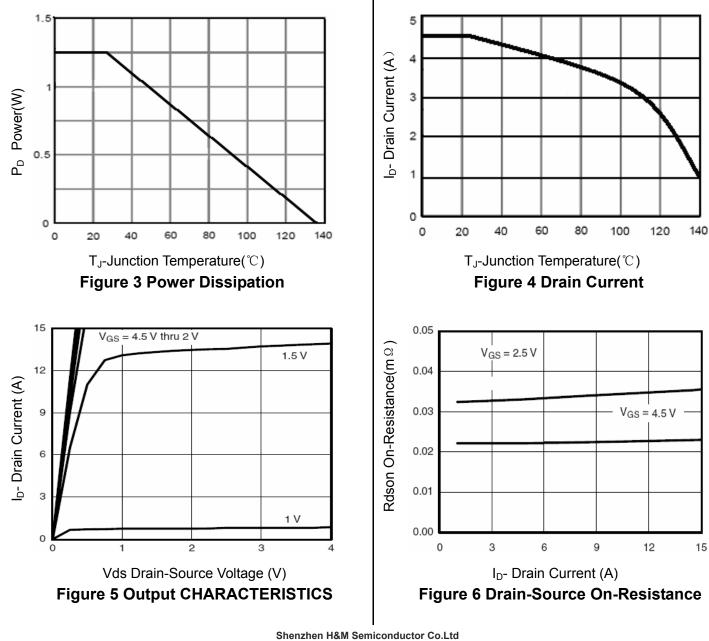
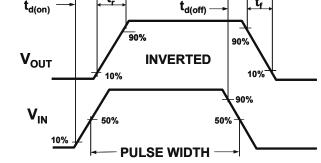


Figure 1:Switching Test Circuit





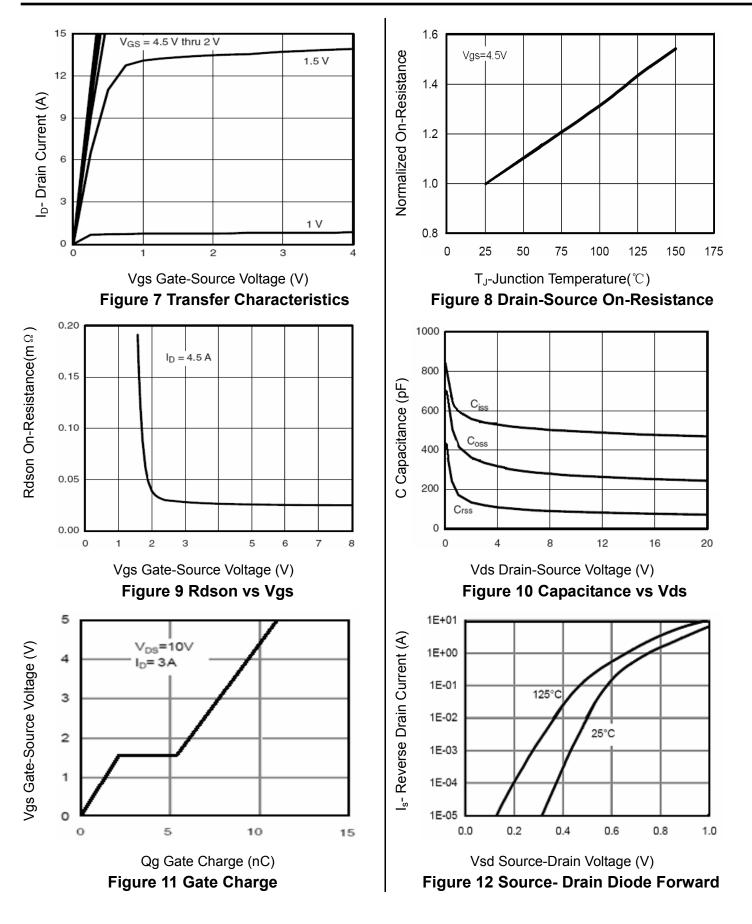
C_{on} t

Figure 2:Switching Waveforms

15

H&M 华之美半导体 SEMI www.hmsemi.com

HM2800D



Shenzhen H&M Semiconductor Co.Ltd http: //www.hmsemi.com

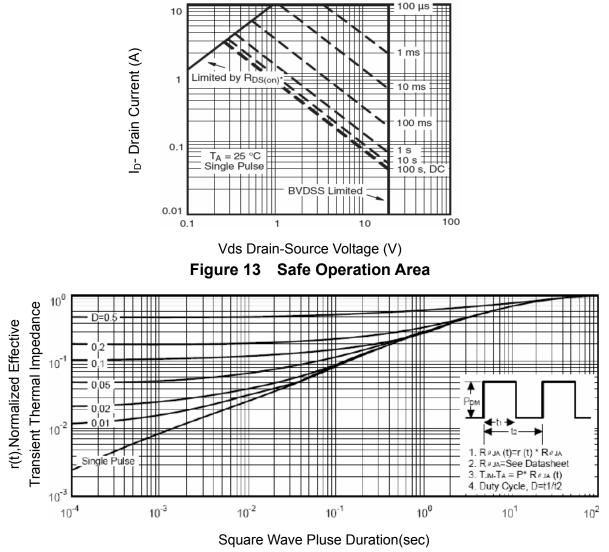
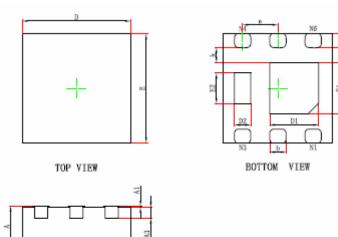


Figure 14 Normalized Maximum Transient Thermal Impedance

Shenzhen H&M Semiconductor Co.Ltd http://www.hmsemi.com

DFN2X2-6L Package Information



SIDE VIEW

Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
Cyrribol	Min.	Max.	Min.	Max.	
A	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A3	0.203	REF.	0.008	REF.	
D	1.924	2.076	0.076	0.082	
E	1.924	2.076	0.076	0.082	
D1	0.800	1.000	0.031	0.039	
E1	0.850	1.050	0.033	0.041	
D2	0.200	0.400	0.008	0.016	
E2	0.460	0.660	0.018	0.026	
k	0.200MIN.		0.008MIN.		
b	0.250	0.350	0.010	0.014	
е	0.650TYP.		0.026	STYP.	
L	0.174	0.326	0.007	0.013	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

Shenzhen H&M Semiconductor Co.Ltd http://www.hmsemi.com

ATTENTION:

- Any and all H&M SEMI products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your H&M SEMI representative nearest you before using any H&M SEMI products described or contained herein in such applications.
- H&M SEMI assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all H&M SEMI products described or contained herein.
- Specifications of any and all H&M SEMI products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- H&M Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all H&M SEMI products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of H&M Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. H&M SEMI believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the H&M SEMI product that you intend to use.
- This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.