Dual N-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The HM4828 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

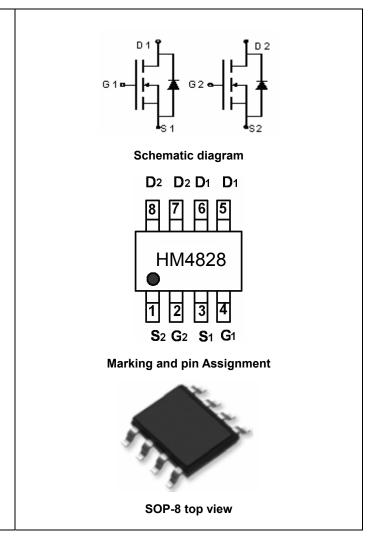
GENERAL FEATURES

• $V_{DS} = 60V, I_D = 4.5A$ $R_{DS(ON)} < 45m\Omega @ V_{GS} = 10V$ (Typ: $38m\Omega$) $R_{DS(ON)} < 77m\Omega @ V_{GS} = 4.5V$ (Typ: $55m\Omega$)

- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current
- Low Gate to Drain Charge to Reduce Switching Losses

Application

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



100% ΔVds TESTED!

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4828	HM4828	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	60	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous	Ι _D	4.5	А	
Drain Current-Continuous(Tc=100°C)	I _D (100℃)	3.0	А	
Pulsed Drain Current	I _{DM}	20	А	
Maximum Power Dissipation	PD	2	W	
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient(Note 2)	$R_{ extsf{ heta}JA}$	62.5	°C/W

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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	60	69	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	· ·		·	•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1	2	3	V
Drain Source On State Desistance		V_{GS} =10V, I _D =4.5A		38	45	
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =4.5V, I _D =3A	-	55	77	mΩ
Forward Transconductance	g fs	V _{DS} =5V,I _D =4.5A	11	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}			450		PF
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V, F=1.0MHz		60		PF
Reverse Transfer Capacitance	C _{rss}			25		PF
Switching Characteristics (Note 4)	· ·		·	•		
Turn-on Delay Time	t _{d(on)}		-	4.7	-	nS
Turn-on Rise Time	tr	V _{Ds} =30V,I _D =4.5A	-	2.3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =3 Ω	-	15.7	-	nS
Turn-Off Fall Time	t _f		-	1.9	-	nS
Total Gate Charge	Qg		-	8.5	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =30V,I _D =4.5A,	-	1.6	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	2.2	-	nC
Drain-Source Diode Characteristics			·			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =3.7A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	4	Α

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

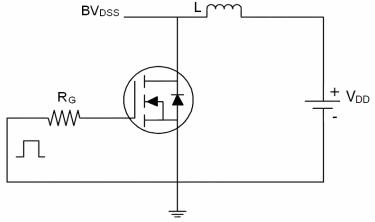
2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

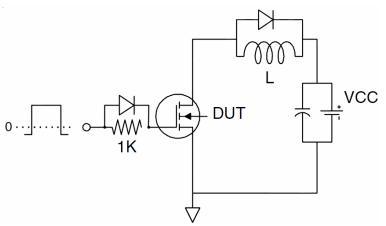
4. Guaranteed by design, not subject to production

Test circuit

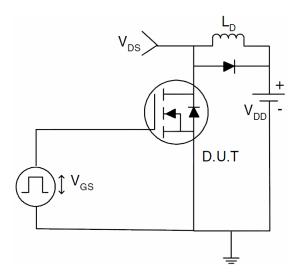
1) E_{AS} test Circuits



2) Gate charge test Circuit:

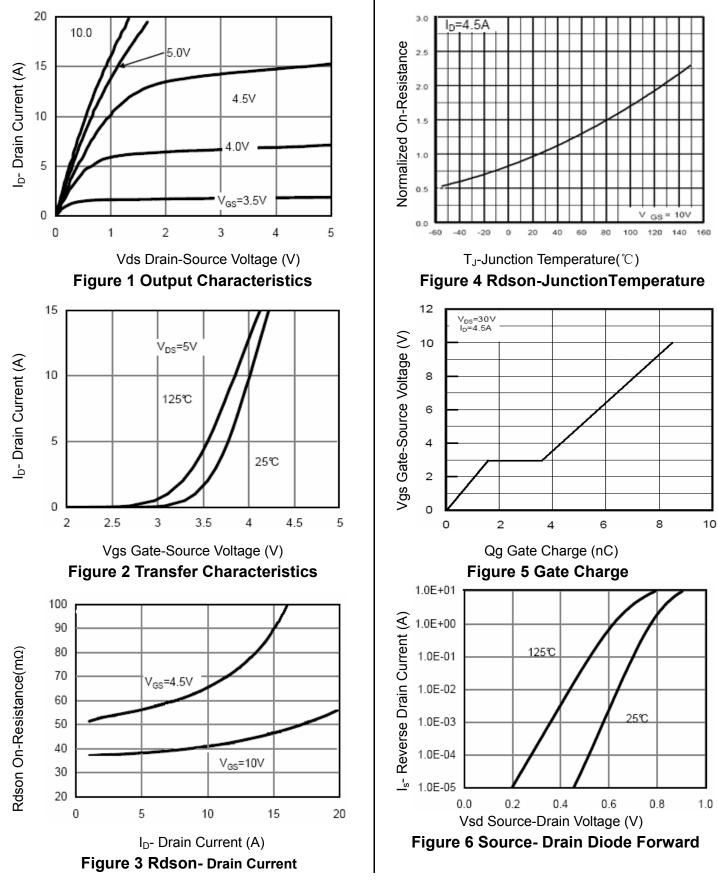


3) Switch Time Test Circuit:

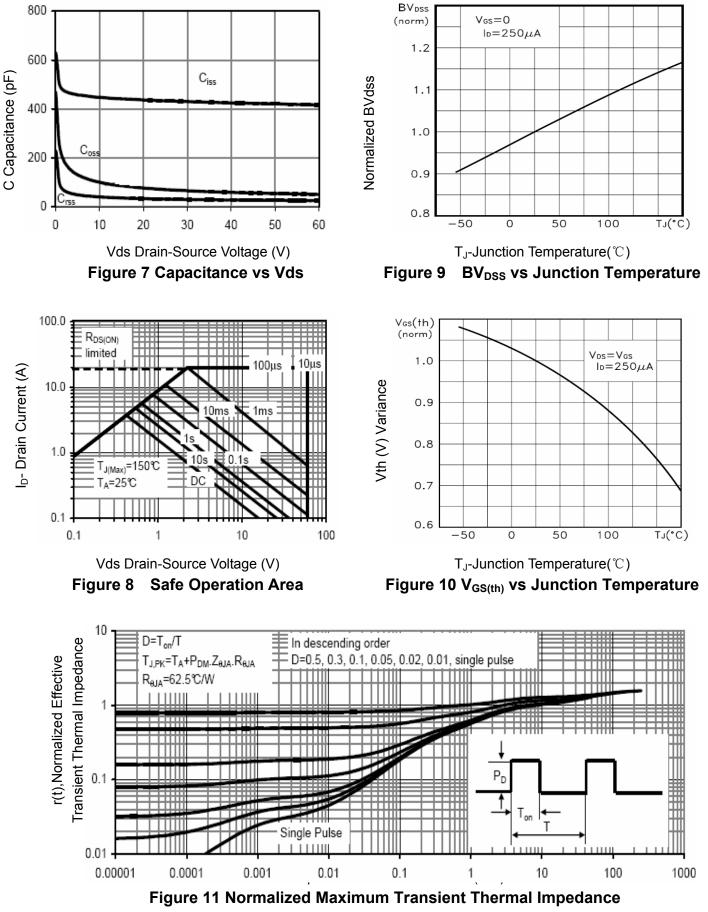


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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)



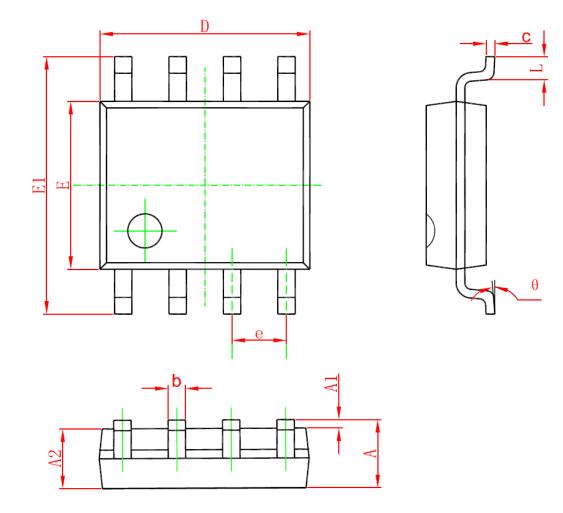
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Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1. 350	1. 550	0. 053	0. 061	
b	0. 330	0. 510	0.013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
E	3.800	4.000	0. 150	0. 157	
E1	5.800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
L	0. 400	1. 270	0.016	0. 050	
θ	0°	8°	0°	8°	

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