

## N-Channel Enhancement Mode Power MOSFET

### **DESCRIPTION**

The HM4410 uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### **GENERAL FEATURES**

V<sub>DS</sub> =30V,I<sub>D</sub> =10A

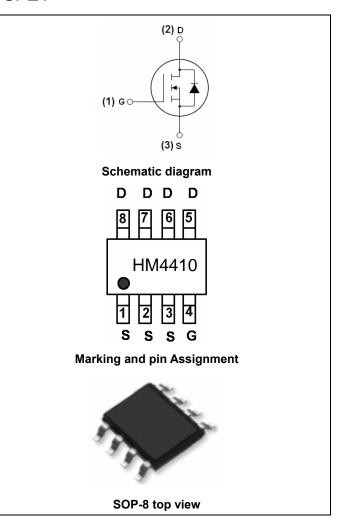
 $R_{DS(ON)} < 13.5 m\Omega$  @  $V_{GS} \text{=} 10 V$ 

 $R_{DS(ON)}$  < 20m $\Omega$  @  $V_{GS}$ =4.5V

- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current

### **Application**

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



### **Package Marking And Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4410	HM4410	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (TA=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Drain Current-Continuous	I <sub>D</sub>	10	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	6	А
Pulsed Drain Current	I <sub>DM</sub>	50	А
Maximum Power Dissipation	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}\mathbb{C}$

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case(Note 2)	R <sub>eJC</sub>	50	°C/W
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Shenzhen H&M Semiconductor Co.Ltd http://www.hmsemi.com Electrical Characteristics (TA=25°C unless otherwise noted)

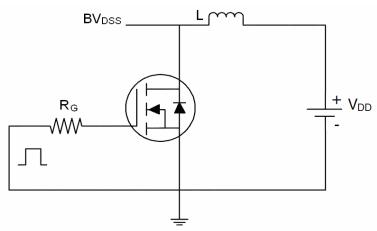
Parameter	Symbol	Symbol Condition		Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	33	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.6	3	V	
Danier Courses Our Otata Banistana	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	7.5	13.5	- mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	11	20		
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =10A	15	-	-	S	
Dynamic Characteristics (Note4)				ı		•	
Input Capacitance	C <sub>lss</sub>	\/ 45\/\\ 0\/	-	1550	-	PF	
Output Capacitance	Coss	$V_{DS}$ =15V, $V_{GS}$ =0V, F=1.0MHz	-	300	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	r=1.0Winz	-	180	-	PF	
Switching Characteristics (Note 4)	·						
Turn-on Delay Time	t <sub>d(on)</sub>		-	30	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =25 $V$ , $I_D$ =1 $A$	-	20	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =6 $\Omega$	-	100	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	80	-	nS	
Total Gate Charge	Qg	\/ -45\/  -400	-	13	-	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =15V, $I_{D}$ =10A, $V_{GS}$ =5V	-	5.5	-	nC	
Gate-Drain Charge	$Q_{gd}$	v <sub>GS</sub> -ov	-	3.5	-	nC	
<b>Drain-Source Diode Characteristics</b>	· .						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	10	А	

### Notes:

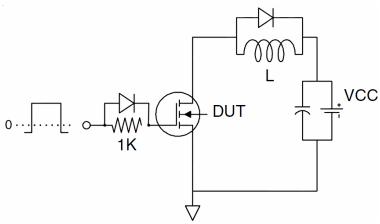
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- **4.** Guaranteed by design, not subject to production

## **Test circuit**

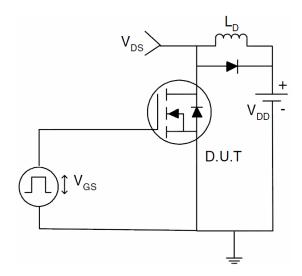
## 1) E<sub>AS</sub> test Circuits



## 2) Gate charge test Circuit:



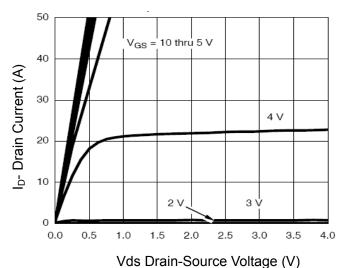
### 3) Switch Time Test Circuit:



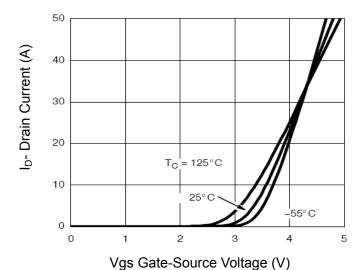
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## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

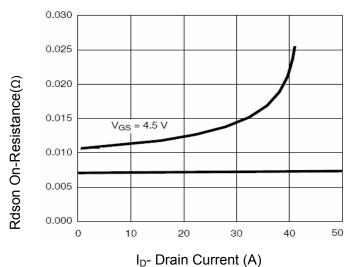


Figure 3 Rdson- Drain Current

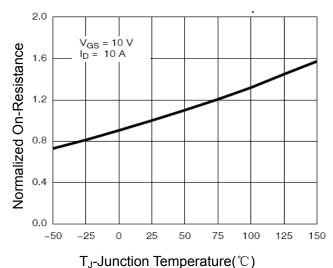


Figure 4 Rdson-JunctionTemperature

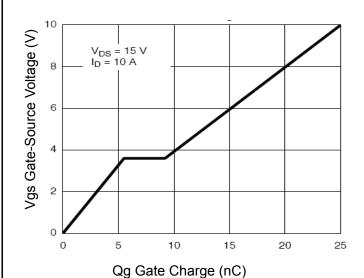


Figure 5 Gate Charge

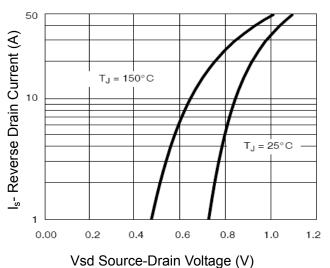


Figure 6 Source- Drain Diode Forward

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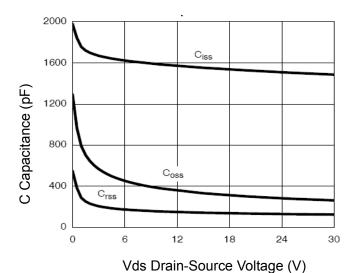
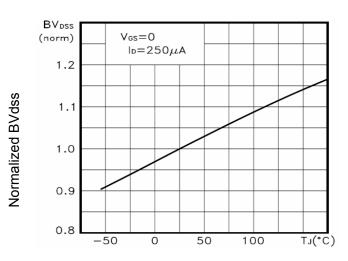


Figure 7 Capacitance vs Vds



T<sub>J</sub>-Junction Temperature(°C)

Figure 9 BV<sub>DSS</sub> vs Junction Temperature

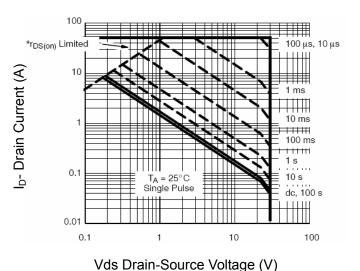
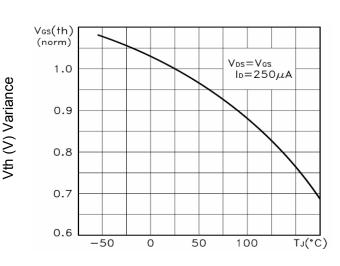


Figure 8 Safe Operation Area



 $T_J$ -Junction Temperature( ${}^{\circ}\!\mathbb{C}$ )

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature

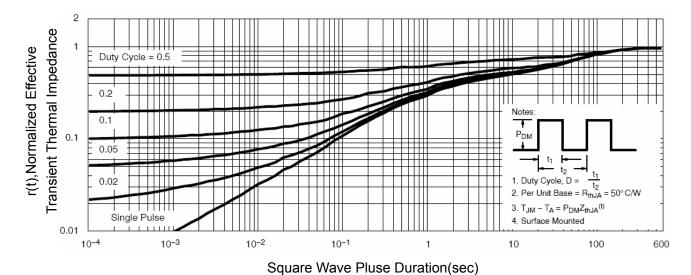
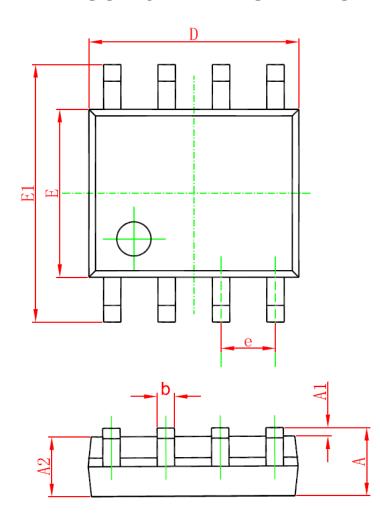


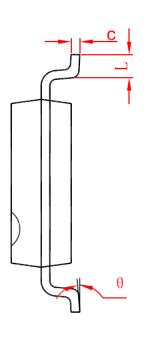
Figure 11 Normalized Maximum Transient Thermal Impedance

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# **SOP-8 PACKAGE IN FORMATION**





Ch a l	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0. 010	
A2	1. 350	1. 550	0.053	0. 061	
b	0. 330	0. 510	0. 013	0. 020	
С	0. 170	0. 250	0.006	0. 010	
D	4. 700	5. 100	0. 185	0. 200	
Е	3. 800	4. 000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270 (BSC)		0. 050 (BSC)		
L	0. 400	1. 270	0. 016	0. 050	
θ	0°	8°	0°	8°	

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