

HK431特别声明：

- 1：航顺芯片 铁脚封装 精度0.5-0.7% A档（中低端消费类产品建议使用）
- 2：航顺芯片 铁脚封装 精度0.7-1.2% B档（不建议使用）
- 3：航顺大芯片 铁脚封装 精度0.5%（中低端产品建议使用）
- 4：航顺大芯片 铜脚封装 精度0.5%（中高端产品建议使用，抗静电 抗干扰强 精度高）



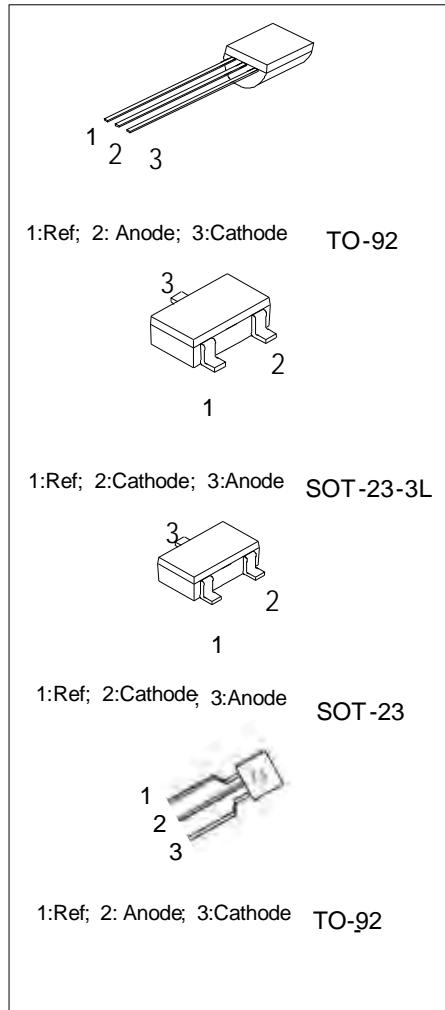
## PROGRAMMABLE PRECISION REFERENCE

### DESCRIPTION

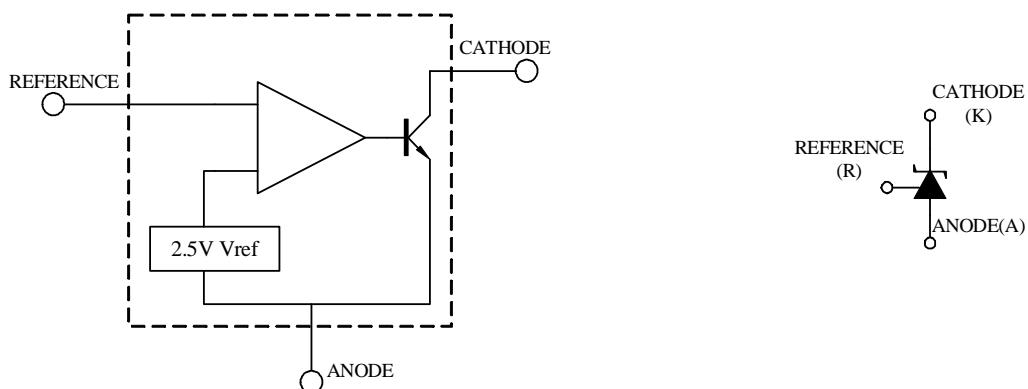
The HK431 is three-terminal adjustable regulator with a guaranteed thermal stability over applicable temperature ranges. The output Voltage may be set to any value between Vref(approximately 2.5V) and 36 V with two external resistors. These devices have provides a very sharp turn-on characteristic, making these devices excellent replacement for zener diodes in many applications.

### FEATURE

- \*Programmable output Voltage to 36V
- \*Low dynamic output impedance 0.2Ω
- \*Sink current capability of 0.5 to 100mA
- \*Equivalent full-range temperature coefficient of 50ppm/°C typical
- \*Temperature compensated for operation over full rated operating temperature range
- \*Low output noise voltage
- \*Fast turn on response



BLOCK DIAGRAM



**ABSOLUTE MAXIMUM RATINGS** (Operating temperature range applies unless otherwise specified)

CHARACTERISTICS	SYMBOL	VALUE		UNITS
Cathode Voltage	V <sub>KA</sub>	37		V
Cathode Current Range(Continuous)	I <sub>KA</sub>	-100~+150		mA
Reference Input Current Range	I <sub>ref</sub>	-0.05~+10		mA
Power Dissipation	P <sub>D</sub>	TO-92	770	mW
		SOT-23-3	370	
Operating temperature	T <sub>opr</sub>	-40~+85		°C
Storage temperature Temperature	T <sub>stg</sub>	-65~+150		°C

**RECOMMENDED OPERATING CONDITIONS**

Characteristic	Symbol	Min	Typ	Max	Unit
Cathode Voltage	V <sub>KA</sub>	V <sub>REF</sub>		36	V
Cathode Current	I <sub>KA</sub>	0.5		100	mA



深圳市航顺芯片技术研发有限公司  
上海航顺微电子有限公司 HK431

ELECTRICAL CHARACTERISTICS( $T_a=25^\circ C$ ,unless otherwise specified)

Characteristic		Symbol	Test conditions		MIN	TYP	MAX	UNIT
Reference Input Voltage 1	0.5%	Vref	VKA=VREF,IKA=10mA		2.488	2.50	2.512	V
	1%				2.475	2.50	2.525	
	2%				2.450	2.50	2.550	
Reference Input Voltage 2*	0.5%	Vref	VKA=VREF,IKA=10mA		2.483	2.495	2.507	V
	1%				2.470	2.495	2.520	
	2%				2.445	2.495	2.545	
Deviation of reference Input Voltage Over temperature	$\Delta V_{ref}$	VKA=VREF,IKA=10mA $T_{MIN} \leq T_a \leq T_{MAX}$			4.5	25	mV	
Ratio of Change in Reference Input Voltage to the Change in Cathode Voltage	$\Delta V_{ref}/\Delta V_{KA}$	IKA=10mA	$\Delta V_{KA}=10V \sim V_{REF}$			-1.0	-2.7	mV/V
			$\Delta V_{KA}=36V \sim 10V$			-0.5	-2.0	
Reference Input Current	Iref	IKA=10mA,R1=10kΩ,R2=∞			1	2	μA	
Deviation of Reference Input Current Over Full Temperature Range	$\Delta I_{ref}/\Delta T$	IKA=10mA,R1=10kΩ,R2=∞, TA=full Temperature			0.2	0.4	μA	
Minimum cathode current for regulation	IKA(min)	VKA=VREF			0.3	0.5	mA	
Off-state cathode Current	IKA(OFF)	VKA=36V,VREF=0			0.05	0.5	μA	
Dynamic Impedance	ZKA	VKA=VREF,IKA=1 to 100mA $f \leq 1.0kHz$			0.15	0.5	Ω	

### TEST CIRCUITS

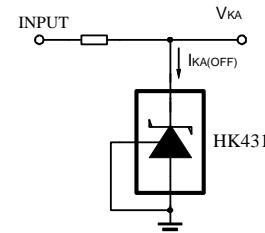
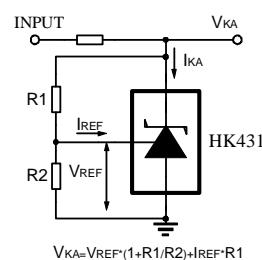
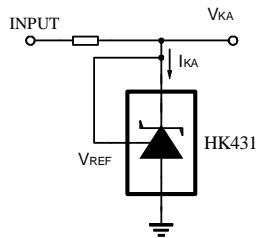


Fig 7 Test Circuit For V<sub>KA</sub>=V<sub>REF</sub>

Fig 8 Test Circuit for V<sub>KA</sub>≥V<sub>REF</sub>

Fig 9 Test Circuit For I<sub>KA</sub>(OFF)

### TYPICAL APPLICATION

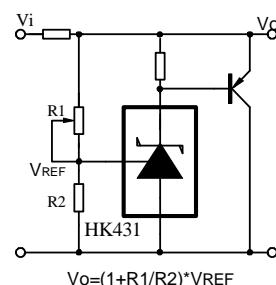
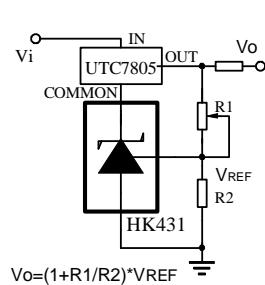
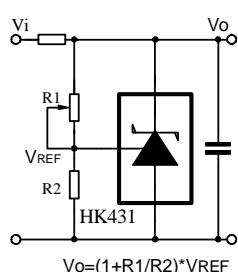


Fig 10 Shutdown Regulator

Fig 11 Output Control of a Three-Terminal Fixed Regulator

Fig 12 Higher-current Shunt Regulator

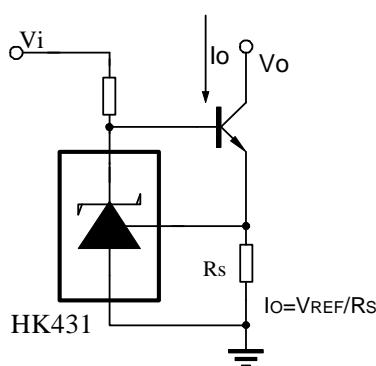


Fig 13 Constant-current Sink

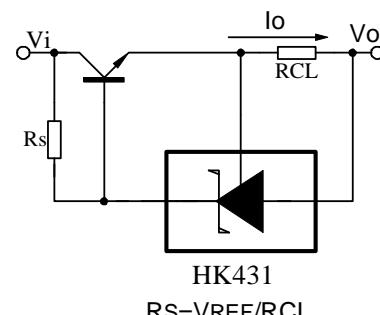


Fig 14 Current Limiting or Current Source

### TYPICAL PERFORMANCE CHARACTERISTICS

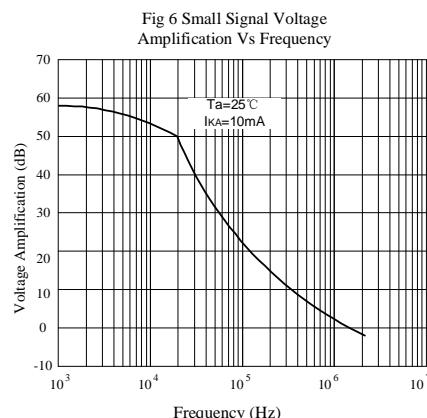
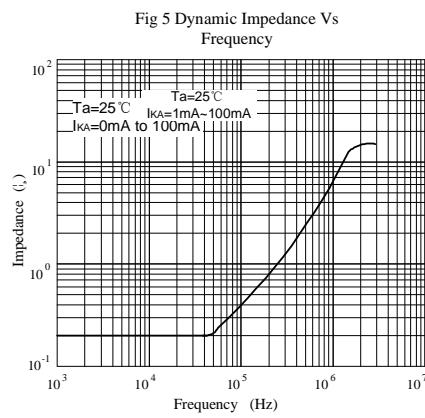
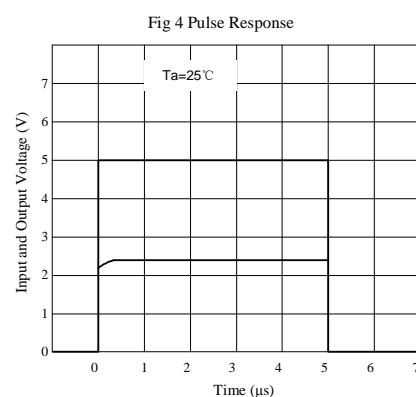
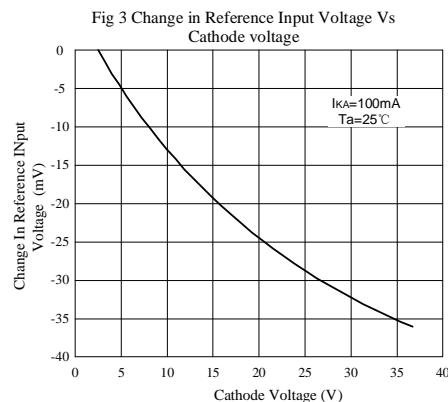
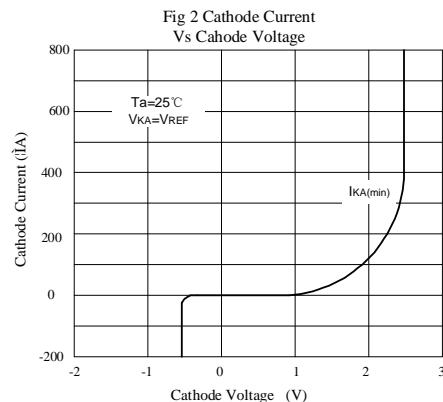
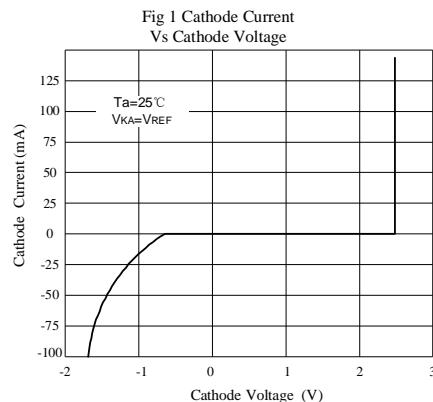
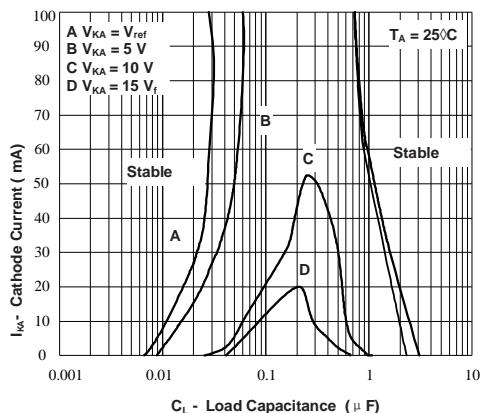
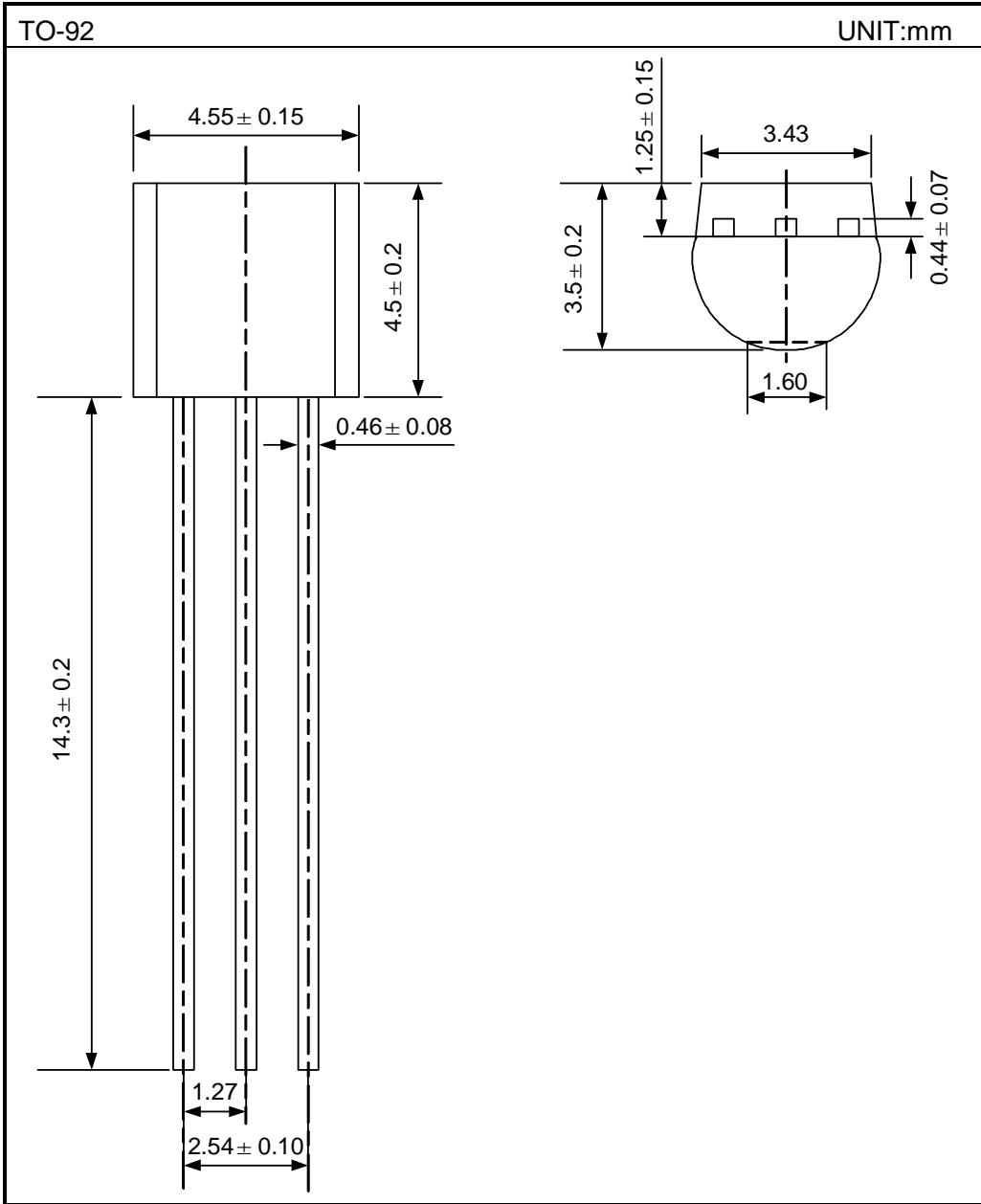
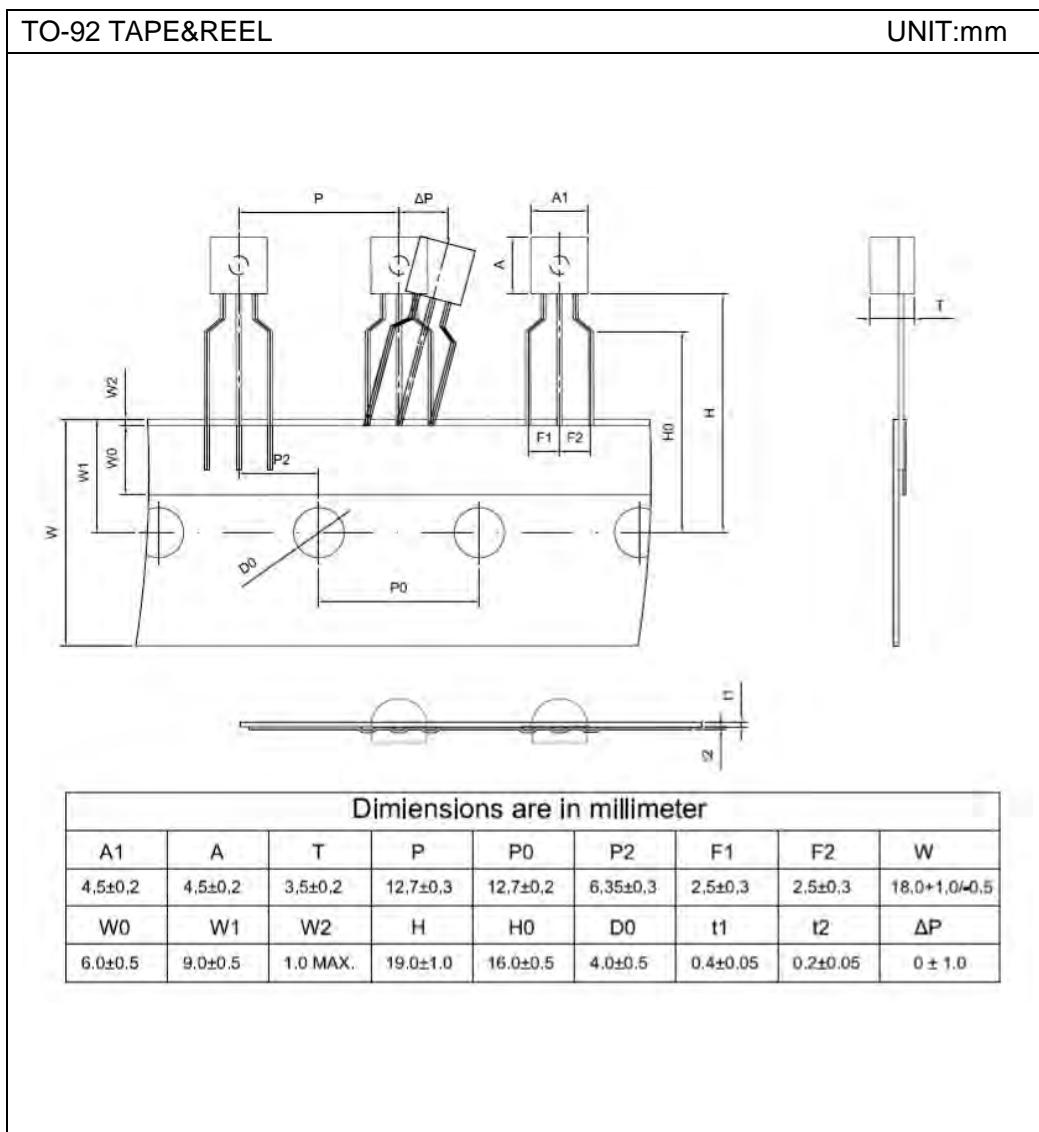


Fig 7 Cathode Current Vs Load Capacitance

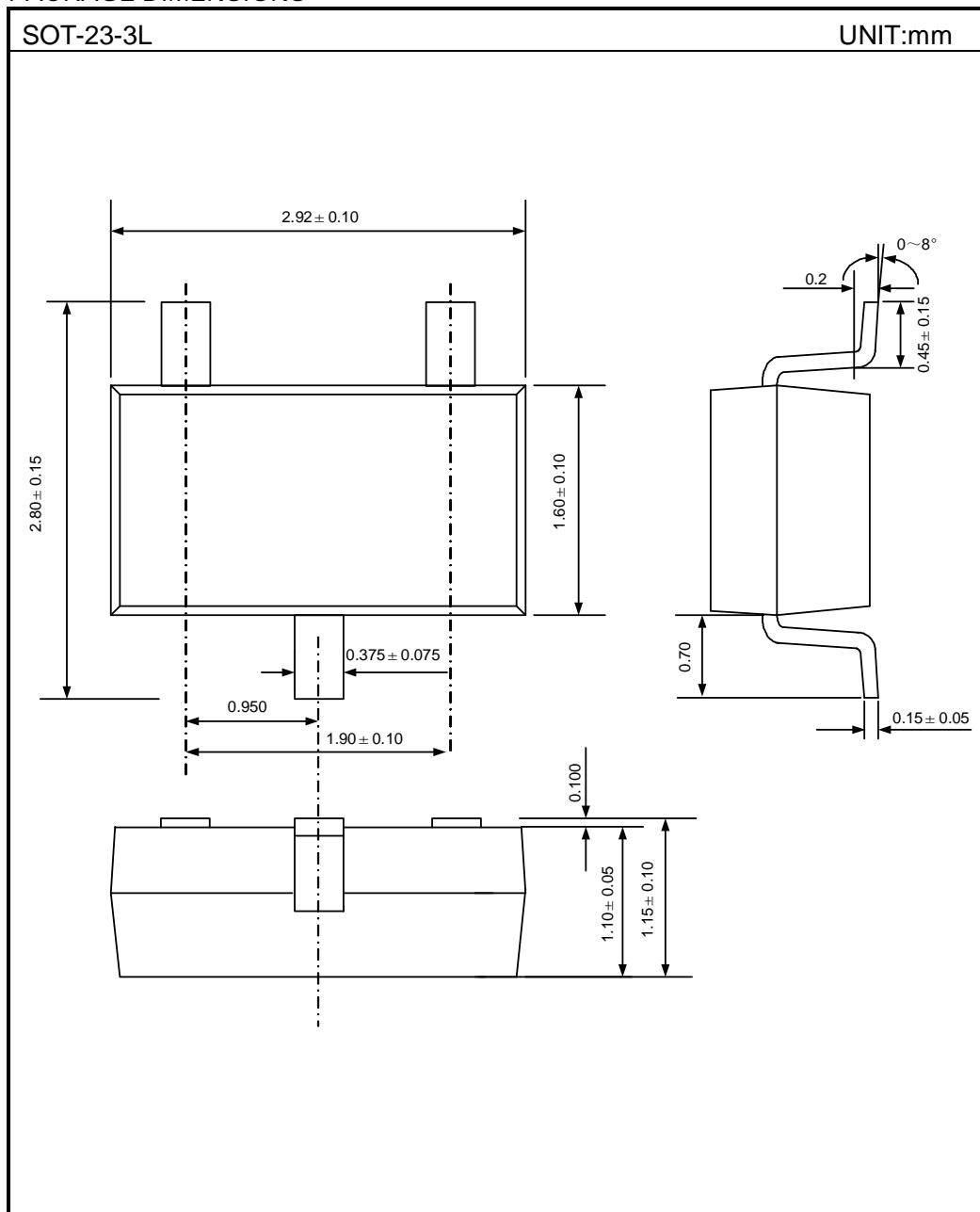


PACKAGE DIMENSIONS

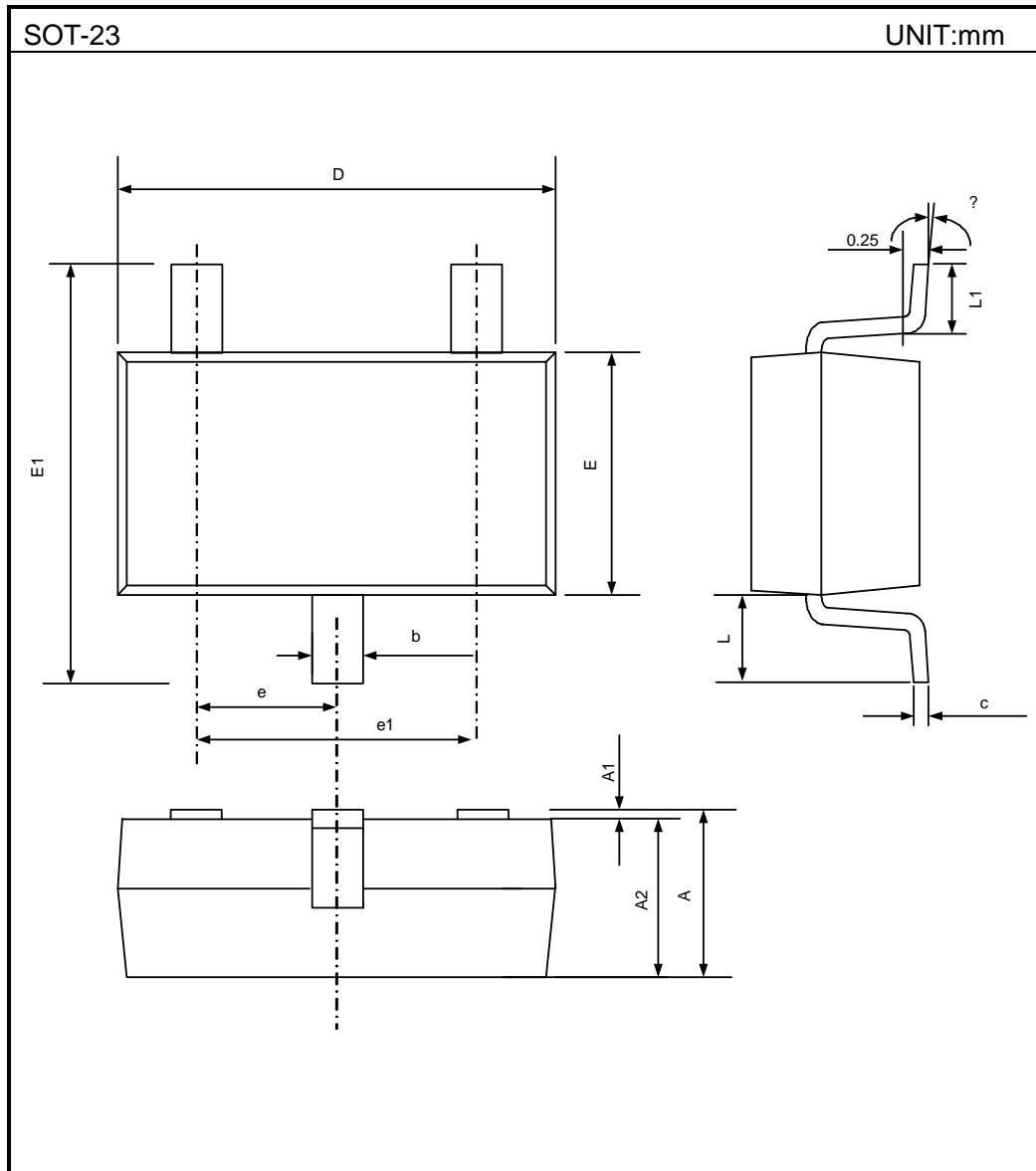




PACKAGE DIMENSIONS



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SOT-23		UNIT:mm		
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.2550	0.089	0.100
e	0.950TYP		0.037TYP	
E1	1.800	2.000	0.071	0.079
L	0.550REF		0.022REF	
L1	0.300	0.500	0.012	0.020
$\theta$	0°	8°	0°	8°



Attach

Revision History

Data	REV	Description	Page
	1.0	Original	
2006.11.16	1.2	Revise "PACKAGE OUTLINE SOT-23-3"	5
2007.08.01	1.3	Revise Pin	1
2010.05.17	1.4	Increase encapsulation	6
2011.08.31	1.5	Increase TYPICAL PERFORMANCE CHARACTERISTICS Fig.7	5
2012.09.06	1.6	Increase PACKAGE	8