

## **UF2N30Z**

# 2A, 300V N-CHANNEL POWER MOSFET

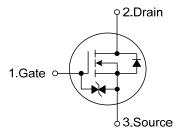
### DESCRIPTION

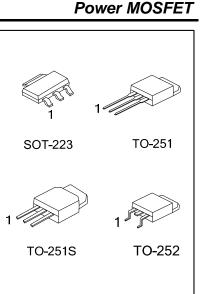
The UTC **UF2N30Z** is an N-channel enhancement mode Power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low gate charge and superior switching performance.

#### FEATURES

- \* R<sub>DS(ON)</sub> < 2.5Ω @ V<sub>GS</sub>=10V, I<sub>D</sub>=1A
- \* High switching speed
- \* Typically 4nC low gate charge
- \* 100% avalanche tested

#### SYMBOL



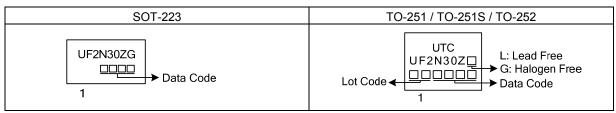


#### ORDERING INFORMATION

Ordering Number		Deekage	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
_	UF2N30ZG-AA3-R	SOT-223	G	D	S	Tape Reel	
UF2N30ZL-TM3-T	UF2N30ZG-TM3-T	TO-251	G	D	S	Tube	
UF2N30ZL-TMS-T	UF2N30ZG-TMS-T	TO-251S	G	D	S	Tube	
UF2N30ZL-TN3-T	UF2N30ZG-TN3-T	TO-252	G	D	S	Tube	
Note: Pin Assignment: G: Gate D: Drain S: Source							

UF2N30ZG-AA3-R (1)Packing Type (2)Package Type (3)Green Package (1) R: Tape Reel, T: Tube (2) AA3: SOT-223, TM3: TO-251, TMS: TO-251S TN3: TO-252 (3) L: Lead Free, G: Halogen Free and Lead Free

#### MARKING



#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V <sub>DSS</sub>	300	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Continuous Drain Current	Continuous	I <sub>D</sub>	2	А	
	Pulsed	I <sub>DM</sub>	8	А	
Avalanche Energy		E <sub>AS</sub>	52	mJ	
Power Dissipation (T <sub>C</sub> =25°C)	SOT-223		0.8	w	
	TO-251/TO-251S TO-252	P <sub>D</sub>	1.13		
Junction Temperature		ТJ	+150	°C	
Storage Temperature Range		T <sub>STG</sub>	-55~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

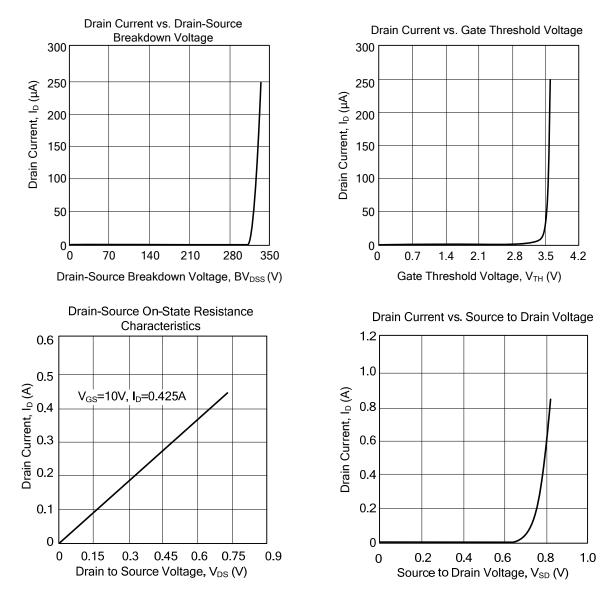
#### **ELECTRICAL CHARACTERISTICS**

PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V				V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =300V			1	μA
Gate-Source Leakage Current	Forward		V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			10	μA
	Reverse	I <sub>GSS</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-10	μA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	I <sub>D</sub> =250μA			4	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =1A			2.5	Ω
DYNAMIC PARAMETERS							
Input Capacitance		CISS			200		рF
Output Capacitance		C <sub>OSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		90		рF
Reverse Transfer Capacitance		C <sub>RSS</sub>			30		pF
SWITCHING PARAMETERS					•		
Total Gate Charge		$Q_{G}$	Vpp=50V. lp=1.3A.		4	6	nC
Gate to Source Charge		$Q_{GS}$	Ig=100µA. Vgs=10V		0.64		nC
Gate to Drain Charge		$Q_{GD}$			1.6		nC
Turn-ON Delay Time		t <sub>D(ON)</sub>			29	35	ns
Rise Time		t <sub>R</sub>	V <sub>DD</sub> =30V, I <sub>D</sub> =0.5A,		110	125	ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω, V <sub>GS</sub> =0~10V		50	56	ns
Fall-Time		t <sub>F</sub>			99	120	ns
SOURCE- DRAIN DIODE RATI	NGS AND	CHARACTER	ISTICS				
Maximum Body-Diode Continuous Current		ls				2	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				8	Α
Drain-Source Diode Forward Voltage		$V_{SD}$	I <sub>S</sub> =2A			1.3	V



# UF2N30Z

## TYPICAL CHARACTERISTICS



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