# **UTC** UNISONIC TECHNOLOGIES CO., LTD

# UTT120N04

Preliminary

## 120A, 40V N-CHANNEL POWER MOSFET

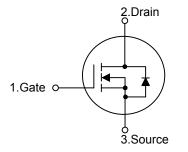
### DESCRIPTION

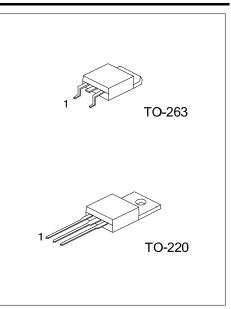
The UTC **UTT120N04** is an N-channel enhancement mode Power FET, it uses UTC's advanced technology to provide customers a minimum on-state resistance and high switching speed.

#### FEATURES

- \*  $R_{DS(ON)}$ < 3.8m $\Omega$  @  $V_{GS}$ =10V,  $I_D$ =60A
- \* High switching speed
- \* Improved dv/dt capability

#### SYMBOL





#### ORDERING INFORMATION

Ordering Number		Deskare	Pin Assignment			Deeking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTT120N04L-TA3-T	UTT120N04G-TA3-T	TO-220	G	D	S	Tube	
UTT120N04L-TQ2-T	UTT120N04G-TQ2-T	TO-263	G	D	S	Tube	
UTT120N04L-TQ2-R	UTT120N04G-TQ2-R	TO-263	G	D	S	Tape Reel	
Noto: Dia Aggianment: C: Cata D: Drain S: Source							

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT120N04L-TA3-T	(1) T: Tube, R: Tape Reel
(2) Package Type	(2) TA3: TO-220, TQ2: TO-263
(3) Lead Free	(3) L: Lead Free, G: Halogen Free

## ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	40	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Drain Current	Continuous	ID	120	А
	Pulsed	I <sub>DM</sub>	480	Α
Avalanche Energy	Single Pulsed	E <sub>AS</sub>	541.5	mJ
Power Dissipation		PD	100	W
Junction Temperature		TJ	+150	°C
Storage Temperature Ra	ange	T <sub>STG</sub>	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62.5	°C/W
Junction to Case	θ <sub>JC</sub>	1.5	°C/W

#### ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS	•		•	•			
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μΑ, V <sub>GS</sub> =0V	40			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =32V			10	μA
Gate-Source Leakage Current	Forward	- I <sub>GSS</sub>	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
	Reverse		V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	I <sub>D</sub> =250μA	1		3	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =60A			3.8	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		2890		рF
Output Capacitance Reverse Transfer Capacitance		Coss			575		рF
		C <sub>RSS</sub>			310		рF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_{G}$	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =1A, I <sub>G</sub> =100µA		160	200	nC
Gate to Source Charge		$Q_{GS}$			35		nC
Gate to Drain Charge		$Q_{GD}$	IG-100μΑ		42	60	nC
Turn-ON Delay Time		t <sub>D(ON)</sub>			17		ns
Rise Time		t <sub>R</sub>	$V_{DD}$ =30V, $I_{D}$ =0.5A, $R_{G}$ =25 $\Omega$ ,		140		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	V <sub>GS</sub> =0~10V		72		ns
Fall-Time	all-Time				26		ns
SOURCE- DRAIN DIODE RATII	NGS AND C	CHARACTERI	STICS				
Maximum Body-Diode Continuous Current		ls				120	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				480	Α
Drain-Source Diode Forward Voltage		$V_{SD}$	I <sub>S</sub> =120A			1.28	V



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