UTC UNISONIC TECHNOLOGIES CO., LTD

UTT120N04

Preliminary

120A, 40V N-CHANNEL POWER MOSFET

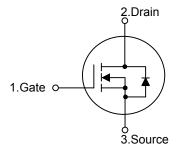
DESCRIPTION

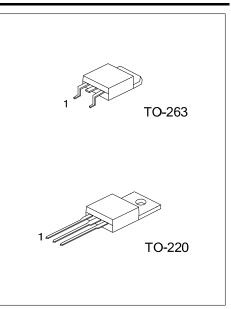
The UTC **UTT120N04** is an N-channel enhancement mode Power FET, it uses UTC's advanced technology to provide customers a minimum on-state resistance and high switching speed.

FEATURES

- * $R_{DS(ON)}$ < 3.8m Ω @ V_{GS} =10V, I_D =60A
- * High switching speed
- * Improved dv/dt capability

SYMBOL





ORDERING INFORMATION

Ordering Number		Deskare	Pin Assignment			Deeking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTT120N04L-TA3-T	UTT120N04G-TA3-T	TO-220	G	D	S	Tube	
UTT120N04L-TQ2-T	UTT120N04G-TQ2-T	TO-263	G	D	S	Tube	
UTT120N04L-TQ2-R	UTT120N04G-TQ2-R	TO-263	G	D	S	Tape Reel	
Noto: Dia Aggianment: C: Cata D: Drain S: Source							

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT120N04L-TA3-T	(1) T: Tube, R: Tape Reel
(2) Package Type	(2) TA3: TO-220, TQ2: TO-263
(3) Lead Free	(3) L: Lead Free, G: Halogen Free

ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V _{DSS}	40	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous	ID	120	А
	Pulsed	I _{DM}	480	Α
Avalanche Energy	Single Pulsed	E _{AS}	541.5	mJ
Power Dissipation		PD	100	W
Junction Temperature		TJ	+150	°C
Storage Temperature Ra	ange	T _{STG}	-55~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	62.5	°C/W
Junction to Case	θ _{JC}	1.5	°C/W

ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS	•		•	•			
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μΑ, V _{GS} =0V	40			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =32V			10	μA
Gate-Source Leakage Current	Forward	- I _{GSS}	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	I _D =250μA	1		3	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =60A			3.8	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz		2890		рF
Output Capacitance Reverse Transfer Capacitance		Coss			575		рF
		C _{RSS}			310		рF
SWITCHING PARAMETERS							
Total Gate Charge		Q_{G}	V _{DD} =30V, V _{GS} =10V, I _D =1A, I _G =100µA		160	200	nC
Gate to Source Charge		Q_{GS}			35		nC
Gate to Drain Charge		Q_{GD}	IG-100μΑ		42	60	nC
Turn-ON Delay Time		t _{D(ON)}			17		ns
Rise Time		t _R	V_{DD} =30V, I_{D} =0.5A, R_{G} =25 Ω ,		140		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	V _{GS} =0~10V		72		ns
Fall-Time	all-Time				26		ns
SOURCE- DRAIN DIODE RATII	NGS AND C	CHARACTERI	STICS				
Maximum Body-Diode Continuous Current		ls				120	Α
Maximum Body-Diode Pulsed Current		I _{SM}				480	Α
Drain-Source Diode Forward Voltage		V_{SD}	I _S =120A			1.28	V



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