

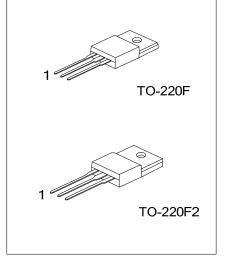
UNISONIC TECHNOLOGIES CO., LTD

UFP254

23A, 250V N-CHANNEL POWER MOSFET

DESCRIPTION

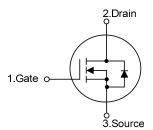
The UTC **UFP254** is an N-channel mode Power FET, it uses UTC's advanced technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.



FEATURES

- * R_{DS(ON)}<140mΩ @ V_{GS}=10V,I_D=14A
- * Low Gate Charge (Maximum 140nC)
- * High Switching Speed

SYMBOL



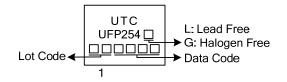
ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UFP254L-TF2-T	UFP254G-TF2-T	TO-220F2	G	D	S	Tube	
UFP254L-TF3-T	UFP254G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source

UFP254L- <u>TF2-T</u> T T (1)Packing Type	(1) T: Tube
(2)Package Type	(2) TF2: TO-220F2
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

MARKING



ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	250	V	
Gate-Source Voltage		V _{GSS}	±20		
Drain Current	Continuous	I _D	23	А	
	Pulsed	I _{DM}	92	А	
Avalanche Current		I _{AR}	23	А	
Avalanche Energy	Single Pulsed	E _{AS}	1780	mJ	
Peak Diode Recovery dv/dt		dv/dt	9	V/ns	
Power Dissipation		PD	42	W	
Junction Temperature		TJ	+150	°C	
Storage Temperature Range		T _{STG}	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		_				_	
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μΑ, V _{GS} =0V	250			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =250V			25	μA
Gate-Source Leakage Current	Forward	lass	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse	I _{GSS}	V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	I _D =250μΑ	2.0		4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =14A			140	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}			2800		рF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1MHz		380		pF
Reverse Transfer Capacitance		C _{RSS}			23		рF
SWITCHING PARAMETERS							
Total Gate Charge		Q_{G}	V _{DS} =50V, V _{GS} =10V, I _D =1.3A ,		120		nC
Gate to Source Charge		Q_{GS}	ν _{DS} =50ν, ν _{GS} =10ν, ι _D =1.3Α, I _G =100μΑ		19		nC
Gate to Drain Charge		Q_{GD}			21		nC
Turn-ON Delay Time		t _{D(ON)}			85		ns
Rise Time		t _R	V _{DD} =30V, V _{GS} =10V, I _D =0.5A,		115		ns
Turn-OFF Delay Time		t _{D(OFF)}	R _G =25Ω		780		ns
Fall-Time		t _F			170		ns
SOURCE- DRAIN DIODE RATING	GS AND CH	HARACTERIS	TICS				
Maximum Body-Diode Continuous Current		ls				23	Α
Maximum Body-Diode Pulsed Current		I _{SM}				92	Α
Drain-Source Diode Forward Voltage		V _{SD}	I _S =23A, V _{GS} =0V			1.8	V
Reverse Recovery Time		t _{rr}	V _{GS} = 0 V, I _S = 10A,		212		ns
Reverse Recovery Charge		Q_{RR}	dI _F / dt = 100 A/µs (Note 1)		1.73		μC



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