

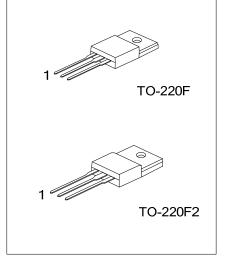
# UNISONIC TECHNOLOGIES CO., LTD

### **UFP254**

## 23A, 250V N-CHANNEL POWER MOSFET

#### DESCRIPTION

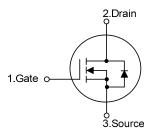
The UTC **UFP254** is an N-channel mode Power FET, it uses UTC's advanced technology. This technology allows a minimum on-state resistance, superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.



#### FEATURES

- \* R<sub>DS(ON)</sub><140mΩ @ V<sub>GS</sub>=10V,I<sub>D</sub>=14A
- \* Low Gate Charge (Maximum 140nC)
- \* High Switching Speed

#### SYMBOL



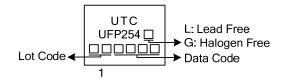
#### ORDERING INFORMATION

Ordering Number		Daakaga	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
UFP254L-TF2-T	UFP254G-TF2-T	TO-220F2	G	D	S	Tube	
UFP254L-TF3-T	UFP254G-TF3-T	TO-220F	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source

UFP254L- <u>TF2-T</u> T T (1)Packing Type	(1) T: Tube
(2)Package Type	(2) TF2: TO-220F2
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

#### MARKING



#### ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V <sub>DSS</sub>	250	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20		
Drain Current	Continuous	I <sub>D</sub>	23	А	
	Pulsed	I <sub>DM</sub>	92	А	
Avalanche Current		I <sub>AR</sub>	23	А	
Avalanche Energy	Single Pulsed	E <sub>AS</sub>	1780	mJ	
Peak Diode Recovery dv/dt		dv/dt	9	V/ns	
Power Dissipation		PD	42	W	
Junction Temperature		TJ	+150	°C	
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		_				_	
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μΑ, V <sub>GS</sub> =0V	250			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =250V			25	μA
Gate-Source Leakage Current	Forward	lass	V <sub>GS</sub> =+20V, V <sub>DS</sub> =0V			+100	nA
	Reverse	I <sub>GSS</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	I <sub>D</sub> =250μΑ	2.0		4.0	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =14A			140	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>			2800		рF
Output Capacitance		Coss	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		380		pF
Reverse Transfer Capacitance		C <sub>RSS</sub>			23		рF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_{G}$	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =1.3A ,		120		nC
Gate to Source Charge		$Q_{GS}$	ν <sub>DS</sub> =50ν, ν <sub>GS</sub> =10ν, ι <sub>D</sub> =1.3Α, I <sub>G</sub> =100μΑ		19		nC
Gate to Drain Charge		$Q_{GD}$			21		nC
Turn-ON Delay Time		t <sub>D(ON)</sub>			85		ns
Rise Time		t <sub>R</sub>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A,		115		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω		780		ns
Fall-Time		t <sub>F</sub>			170		ns
SOURCE- DRAIN DIODE RATING	GS AND CH	HARACTERIS	TICS				
Maximum Body-Diode Continuous Current		ls				23	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				92	Α
Drain-Source Diode Forward Voltage		V <sub>SD</sub>	I <sub>S</sub> =23A, V <sub>GS</sub> =0V			1.8	V
Reverse Recovery Time		t <sub>rr</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 10A,		212		ns
Reverse Recovery Charge		$Q_{RR}$	dI <sub>F</sub> / dt = 100 A/µs (Note 1)		1.73		μC



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