UNISONIC TECHNOLOGIES CO., LTD

2N60Z **Power MOSFET**

2A, 600V N-CHANNEL **POWER MOSFET**

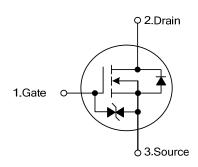
DESCRIPTION

The UTC 2N60Z is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)} = 5\Omega@V_{GS} = 10V$
- * Ultra Low gate charge (typical 9.0nC)
- * Low reverse transfer capacitance (C_{RSS} = typical 5.0 pF)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

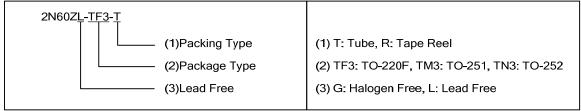
SYMBOL



ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Doolsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
2N60ZL-TF3-T	2N60ZG-TF3-T	TO-220F	G	D	S	Tube	
2N60ZL-TM3-T	2N60ZG-TM3-T	TO-251	G	D	S	Tube	
2N60ZL-TN3-T	2N60ZG-TN3-T	TO-252	G	D	S	Tube	
2N60ZL-TN3-R	2N60ZG-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



TO-251 TO-220F TO-252

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■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	±20	V
Avalanche Current (Note 2)		I _{AR}	2.0	Α
Drain Current	Continuous	I_D	2.0	Α
	Pulsed (Note 2)	I _{DM}	8.0	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	140	mJ
	Repetitive (Note 2)	E _{AR}	4.5	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220F	D	23	W
	TO-251/TO-252	P _D	44	W
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ + 150	°C
Storage Temperature		T _{STG}	-55 ~ + 150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by T_{J}
- 3. L=64mH, I_{AS} =2.0A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 2.4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	PACKAGE	SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F	0	62.5	°C/W
	TO-251/TO-252	θ_{JA}	100	°C/W
Junction to Case	TO-220F	0	5.5	°C/W
	TO-251/TO-252	θ_{Jc}	2.87	°C/W

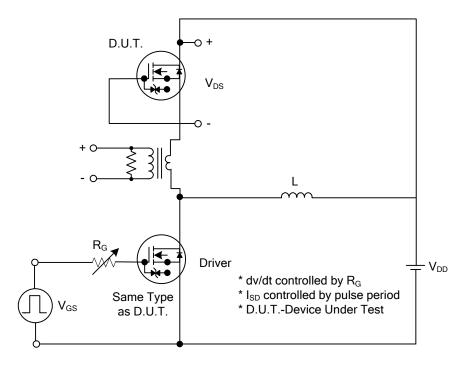
■ ELECTRICAL CHARACTERISTICS (T_J =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV_{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V	
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ	
Cata Sauraa Laakaga Current	Forward	1000	$V_{GS} = 20V, V_{DS} = 0V$			5	μΑ	
Gate-Source Leakage Current	Reverse		$V_{GS} = -20V, V_{DS} = 0V$			-5	μΑ	
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_{J}$	I _D =250μA, Referenced to 25°C		0.4		V/°C	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_{D} = 1A$		3.6	5	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance	nput Capacitance		., -25,/ ,/ -0,/		270	350	pF	
Output Capacitance		C _{ISS} C _{OSS}	V _{DS} =25V, V _{GS} =0V, f =1MHz		40	50	pF	
Reverse Transfer Capacitance		C_{RSS}	I = IIVIHZ		5	7	pF	
SWITCHING CHARACTERISTICS	SWITCHING CHARACTERISTICS							
Turn-On Delay Time		t _{D (ON)}			10	30	ns	
Turn-On Rise Time		t _R	$V_{DD} = 300V, I_D = 2.4A,$		25	60	ns	
Turn-Off Delay Time		$t_{D(OFF)}$	R _G =25Ω (Note 1, 2)		20	50	ns	
Turn-Off Fall Time		t _F			25	60	ns	
Total Gate Charge	otal Gate Charge Q _G		\/ -480\/ \/ -10\/		9.0	11	nC	
Gate-Source Charge Gate-Drain Charge		Q_GS	V _{DS} =480V, V _{GS} =10V, I _D =2.4A (Note 1, 2)		1.6		nC	
		Q_GD	1D-2.4A (Note 1, 2)		4.3		nC	
DRAIN-SOURCE DIODE CHARACTERISTICS								
Drain-Source Diode Forward Voltage	ge	V_{SD}	$V_{GS} = 0 \text{ V}, I_{SD} = 2.0 \text{ A}$			1.4	V	
Continuous Drain-Source Current		I_{SD}				2.0	Α	
Pulsed Drain-Source Current		I _{SM}				8.0	Α	
Reverse Recovery Time		t _{rr}	$V_{GS} = 0 \text{ V}, I_{SD} = 2.4\text{A},$		180		ns	
Reverse Recovery Charge		Q_{RR}	di/dt = 100 A/µs (Note 1)		0.72		μC	

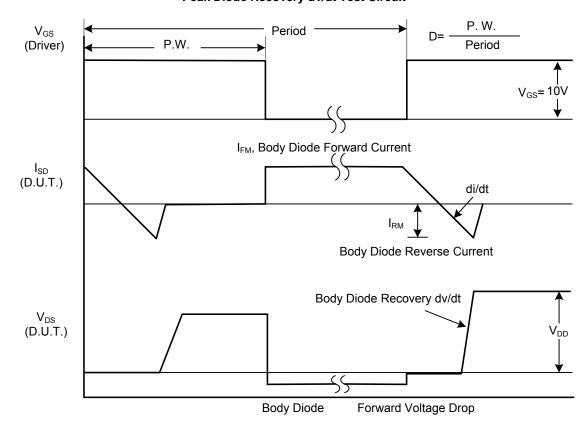
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle≤2%

^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

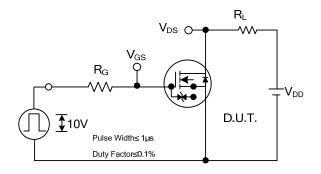


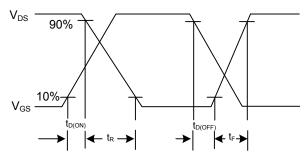
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

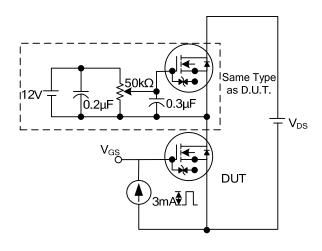
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

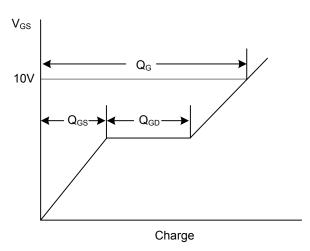




Switching Test Circuit

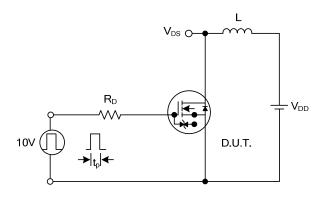
Switching Waveforms

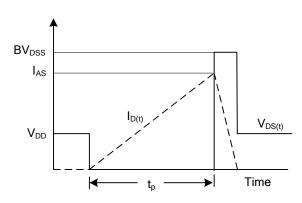




Gate Charge Test Circuit

Gate Charge Waveform

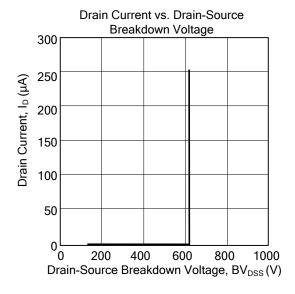


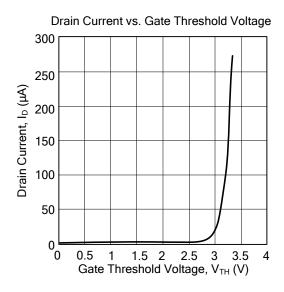


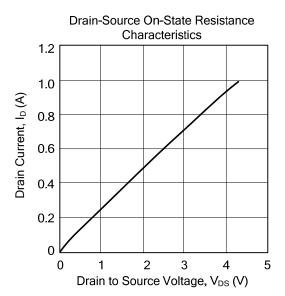
Unclamped Inductive Switching Test Circuit

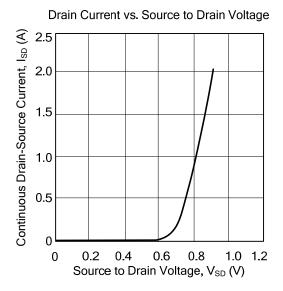
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS









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