UNISONIC TECHNOLOGIES CO., LTD

4N60-C Power MOSFET

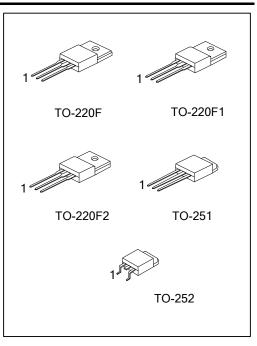
4A, 600V N-CHANNEL **POWER MOSFET**

DESCRIPTION

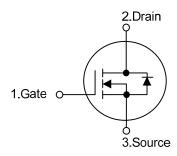
The UTC 4N60-C is a high voltage power MOSFET and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristics. This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

- * $R_{DS(ON)}$ < 2.5 Ω @ V_{GS} = 10 V
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, high Ruggedness



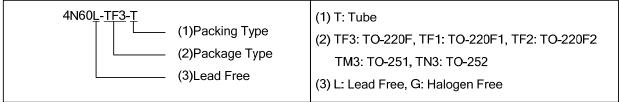
SYMBOL



ORDERING INFORMATION

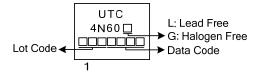
Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
4N60L-TF3-T	4N60G-TF3-T	TO-220F	G	D	S	Tube	
4N60L-TF1-T	4N60G-TF1-T	TO-220F1	G	D	S	Tube	
4N60L-TF2-T	4N60G-TF2-T	TO-220F2	G	D	S	Tube	
4N60L-TM3-T	4N60G-TM3-T	TO-251	G	D	S	Tube	
4N60L-TN3-R	4N60G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage		V _{GSS}	±30	V
Avalanche Current (Note 2)		I _{AR}	4.4	Α
Drain Current	Continuous	I _D	4.0	Α
	Pulsed (Note 2)	I_{DM}	16	Α
–	Single Pulsed (Note 3)	E _{AS}	240	mJ
Avalanche Energy	Repetitive (Note 2)	E _{AR} 10.6		mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220F/TO-220F1 TO-220F2	P_D	36	W
	TO-251/TO-252		50	W
Junction Temperature		TJ	+150	°C
Operating Temperature		T _{OPR}	-55 ~ +150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating : Pulse width limited by maximum junction temperature
- 3. L = 30mH, I_{AS} = 4A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 4.4A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220F/TO-220F1 TO-220F2	θја	62.5	°C/W
	TO-251/TO-252		110	°C/W
Junction to Case	TO-220F/TO-220F1		3.47	°C/W
	TO-220F2	θ_{JC}	3.28	°C/W
	TO-251/TO-252		2.5	°C/W

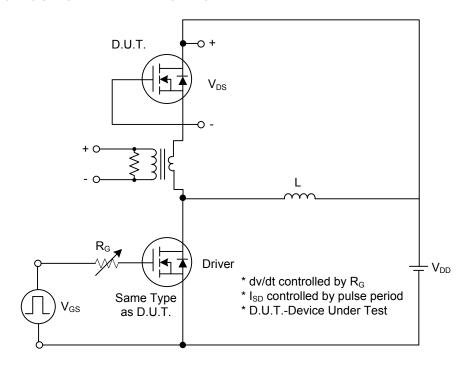
■ **ELECTRICAL CHARACTERISTICS** (T_C =25°C, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	600			V
Drain-Source Leakage Current		I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$			10	μΑ
			$V_{DS} = 480V$, $V_{GS} = 0V$,			100	
			$T_C = 125$ °C			100	μΑ
Gate-Source Leakage Current	Forward	1000	$V_{GS} = 30V, V_{DS} = 0V$			100	nA
Gale-Source Leakage Current	Reverse		$V_{GS} = -30V, V_{DS} = 0V$			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I _D =250μA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Res	istance	R _{DS(ON)}	$V_{GS} = 10 \text{ V}, I_D = 2.2 \text{A}$		1.9	2.5	Ω
DYNAMIC CHARACTERISTICS							
Input Capacitance		C_{ISS}	$V_{DS} = 25V, V_{GS} = 0V,$		600	700	pF
Output Capacitance		Coss	$V_{DS} = 25V$, $V_{GS} = 0V$, f = 1MHz		60	80	pF
Reverse Transfer Capacitance		C_{RSS}	1 - 11011 12		6	15	pF
SWITCHING CHARACTERISTIC	S						
Turn-On Delay Time		$t_{D(ON)}$			35	55	ns
Turn-On Rise Time		t_R	$V_{DD} = 300V, I_D = 4.0A,$		55	80	ns
Turn-Off Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega \text{ (Note 1, 2)}$		100	130	ns
Turn-Off Fall Time		t_{F}			40	60	ns
Total Gate Charge		Q_G	 V _{DS} = 480V.I _D = 4.0A.		20	50	nC
Gate-Source Charge		Q_GS	V _{GS} = 460 V,I _D = 4.0A, V _{GS} = 10V (Note 1, 2)		5		nC
Gate-Drain Charge		Q_GD	VGS= 10V (Note 1, 2)		3		nC
SOURCE- DRAIN DIODE RATIN	GS AND CI	HARACTERIST	TICS				
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0V, I_{S} = 4.4A$			1.4	V
Maximum Continuous Drain-Source Diode		I _{SD}				4.4	Α
Forward Current						4.4	^
Maximum Pulsed Drain-Source Diode		I _{SM}				17.6	Α
Forward Current						17.0	^
Reverse Recovery Time		t _{rr}	$V_{GS} = 0 \text{ V}, I_S = 4.4\text{A},$		250		ns
Reverse Recovery Charge		Q_{RR}	$dI_F/dt = 100 A/\mu s \text{ (Note 1)}$		1.5		μC

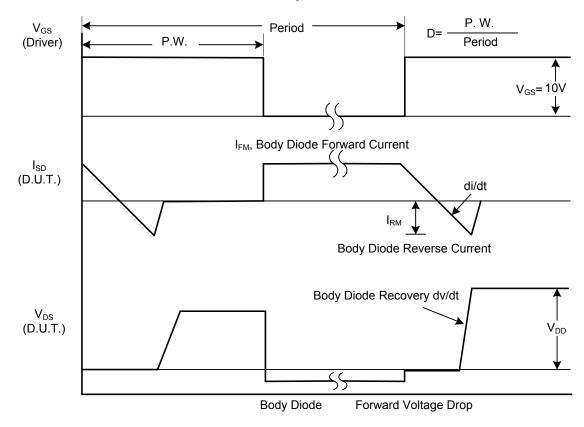
Notes: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%

^{2.} Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

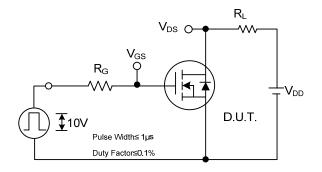


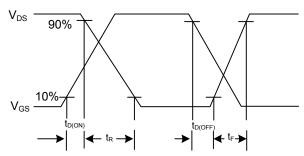
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

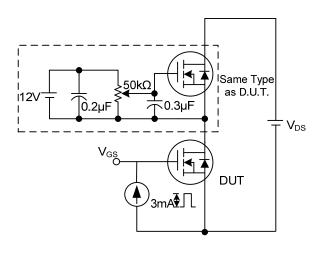
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

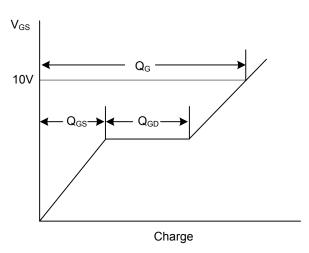




Switching Test Circuit

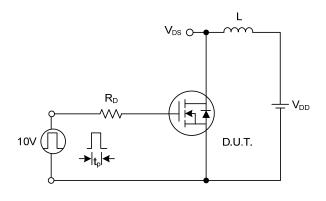
Switching Waveforms

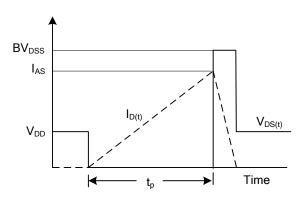




Gate Charge Test Circuit

Gate Charge Waveform

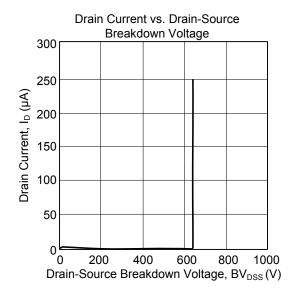


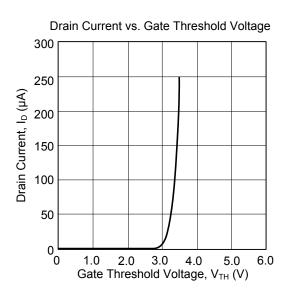


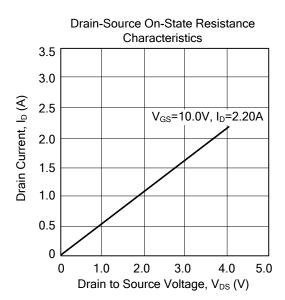
Unclamped Inductive Switching Test Circuit

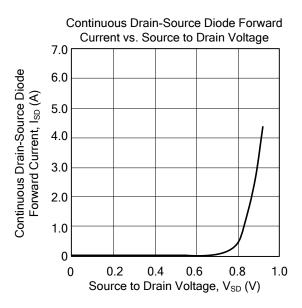
Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS









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