

# BSS84ZT

# Power MOSFET

# 0.13A, 50V P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

### DESCRIPTION

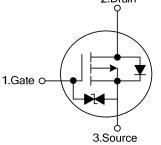
These P-Channel enhancement mode field vertical D-MOS transistors are in a SOT-523 SMD package, and in most applications they require up to 0.13A DC and can deliver current up to 0.52A.

This product is particularly suited to low voltage applications requiring a low current high side switch.

## FEATURES

\*  $R_{DS(ON)}$  < 10 $\Omega$  @  $V_{GS}$ =-4.5V,  $I_D$ =-0.1A

# SYMBOL 2.Drain

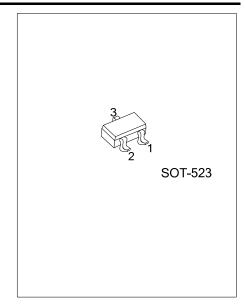


## ORDERING INFORMATION

Ordering Number	Dookago	Pin Assignment			Docking	
Ordering Number	Package	1	2	3	Packing	
BSS84ZTG-AN3-R	SOT-523	S	G	D	Tape Reel	
Note: Pin Assignment: S: Source G: Gate D: Drain						
(2)Package Type	(1) R: Tape Reel (2) AN3: SOT-52 (3) G: Halogen F		Lead Fr	ree		

#### MARKING





#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V <sub>DSS</sub>	-50	V	
Gate-Source Voltage		V <sub>GSS</sub>	±20	V	
Continuous Drain Current	DC	– I <sub>D</sub>	-0.13		
	Pulse		-0.52	A	
Power Dissipation		PD	0.15	W	
Junction Temperature		TJ	+150	°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	625	°C/W

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250µA	-50			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-50V, V <sub>GS</sub> =0V			-15	μA
Gate–Body Leakage, Forward	I <sub>GSS</sub>	$V_{DS}=0V, V_{GS}=\pm 20V$			±10	μA
ON CHARACTERISTICS (Note)						-
Gate-Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-1m A	-0.8	-1.7	-2	V
Static Drain–Source On–Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-0.1A		1.2	10	Ω
On-State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =-10 V, V <sub>DS</sub> =-5V	-0.6			А
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =-25V, I <sub>D</sub> =-0.1A	0.05	0.6		S
DYNAMIC PARAMETERS						
Input Capacitance	C <sub>ISS</sub>			73		рF
Output Capacitance	C <sub>OSS</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz		10		рF
Reverse Transfer Capacitance	C <sub>RSS</sub>			5		рF
SWITCHING PARAMETERS (Note)						
Total Gate Charge	$Q_{G}$	V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V,		0.9	1.3	nC
Gate Source Charge	$Q_{GS}$	V <sub>DS</sub> 30V, V <sub>GS</sub> 10V, -I⊳=-0.1A		0.2		nC
Gate Drain Charge	$Q_{GD}$	100.1A		0.3		nC
Turn-ON Delay Time	t <sub>D(ON)</sub>			2.5	5	ns
Turn-ON Rise Time	t <sub>R</sub>	$V_{DD}$ =-30V, $I_{D}$ =-0.1A, $V_{GS}$ =-10V,		6.3	13	ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =6Ω,		10	20	ns
Turn-OFF Fall-Time	t <sub>F</sub>			4.8	9.6	ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACTER	STICS				
Drain-Source Diode Forward Voltage	$V_{SD}$	V <sub>GS</sub> = 0V, I <sub>S</sub> =-0.13A (Note)		-0.8	-1.2	V
Max. Diode Forward Current	ls				-0.13	А
Pulsed Drain-Source Current	I <sub>Sm</sub>				-0.52	А
Note: Pulse test pulse width < 300us duty	$c_{vclo<2\%}$					

Note: Pulse test, pulse width  $\leq$  300us, duty cycle $\leq$  2%



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