

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

## 4N65-U 4A, 650V N-CHANNEL **POWER MOSFET**

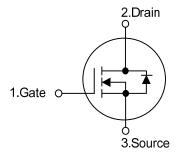
#### DESCRIPTION

The UTC 4N65-U is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristic. This power MOSFET is usually used in high speed switching applications including power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### **FEATURES**

- \*  $R_{DS(ON)}$  < 2.5 $\Omega$  @V<sub>GS</sub>=10V
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

#### SYMBOL



# 1 TO-220F1

#### **ORDERING INFORMATION**

	Ordering	Daakaga	Pin Assignment			Deaking	
	Lead Free	Halogen Free	Package	1	2	3	Packing
	4N65L- TF1-T	4N65G-TF1-T	TO-220F1	G	D	S	Tube
Note:	Pin Assignment: G:	Gate D: Drain S: Sou	irce				

4N65L-TF1-T (1)Packing Type (2)Package Type (3)Lead Free	(1) T: Tube (2) TF1: TO-220F1 (3) L: Lead Free, G: Halogen Free
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#### MARKING INFORMATION

PACKAGE	MARKING
TO-220F1	UTC 4N65 L: Lead Free G: Halogen Free Lot Code 1

#### Power MOSFET

#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>c</sub> = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V <sub>DSS</sub>	650	V
Gate-Source Voltage		V <sub>GSS</sub>	±30	V
Avalanche Current (Note2)		I <sub>AR</sub>	4.4	А
Drain Current	Continuous	I <sub>D</sub>	4.0	А
	Pulsed (Note2)	I <sub>DM</sub>	16	А
	Single Pulsed (Note3)	E <sub>AS</sub>	240	mJ
Avalanche Energy	Repetitive (Note2)	E <sub>AR</sub>	10.6	mJ
Peak Diode Recovery dv/dt (Note4)		dv/dt	4.5	V/ns
Power Dissipation		PD	36	W
Junction Temperature		ТJ	+150	°C
Operating Temperature		T <sub>OPR</sub>	-55 ~ +150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 30mH,  $I_{AS}$  = 4A,  $V_{DD}$  = 50V,  $R_G$  = 25  $\Omega$ , Starting  $T_J$  = 25°C

4.  $I_{SD} \leq 4.4A$ , di/dt  $\leq 200A/\mu s$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^{\circ}C$ 

#### THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT	
Junction to Ambient	θ <sub>JA</sub>	62.5	°C/W	
Junction to Case	θ <sub>JC</sub>	3.47	°C/W	



ELECTRICAL	CHARACTERISTICS (T <sub>C</sub> =25°C, unless otherwise specified)
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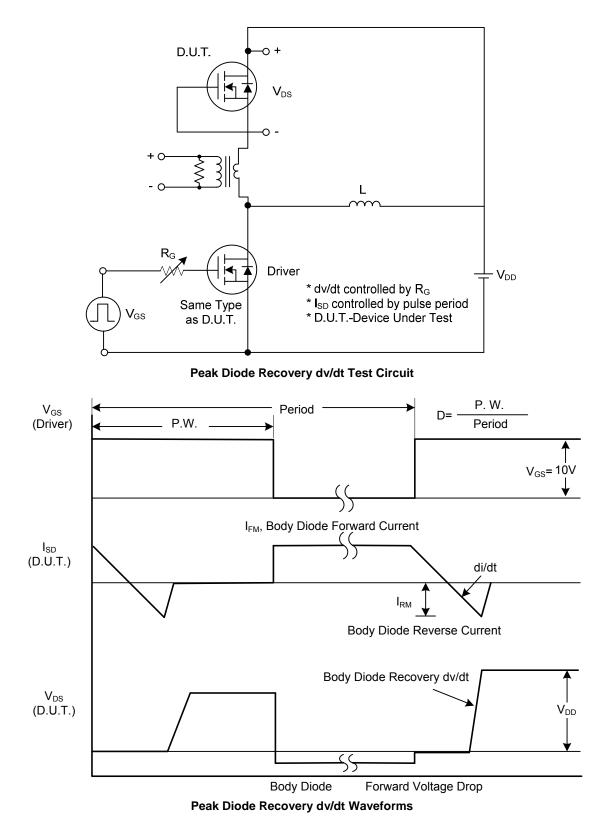
<b></b>		0.4.5.07					
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	ΓYΡ	MAX	UNIT
OFF CHARACTERISTICS			Ι				
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250µA	650			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V			10	μA
		1085	V <sub>DS</sub> = 520 V, T <sub>C</sub> =125°C			100	μA
Gate-Source Leakage Current	Forward	0.000	$V_{GS}$ = 30 V, $V_{DS}$ = 0 V			100	nA
	Reverse		V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V			-100	nA
Breakdown Voltage Temperature C	Breakdown Voltage Temperature Coefficient		I <sub>D</sub> =250µA, Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		V <sub>GS(TH)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
Static Drain-Source On-State Re	sistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 2.2A		2.4	2.5	Ω
DYNAMIC CHARACTERISTICS	;						
Input Capacitance		C <sub>ISS</sub>			980	1120	pF
Output Capacitance		C <sub>oss</sub>	$V_{DS} = 25 V, V_{GS} = 0V,$		55	70	pF
Reverse Transfer Capacitance		C <sub>RSS</sub>	f = 1MHz		44	55	pF
SWITCHING CHARACTERISTIC	CS						
Turn-On Delay Time		t <sub>D(ON)</sub>			68	108	ns
Turn-On Rise Time		t <sub>R</sub>	$V_{DS} = 30V, I_{D} = 1.0A,$		210	250	ns
Turn-Off Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> = 25Ω (Note 1, 2)		104	144	ns
Turn-Off Fall Time		t <sub>F</sub>	7		35	80	ns
Total Gate Charge		Q <sub>G</sub>			96		nC
Gate-Source Charge		Q <sub>GS</sub>	$V_{DS}$ =120V,I <sub>D</sub> =4.0A, I <sub>G</sub> =3.3mA,		11		nC
Gate-Drain Charge		Q <sub>GD</sub>	V <sub>GS</sub> =10V (Note 1, 2)		45		nC
SOURCE- DRAIN DIODE RATII	NGS AND	CHARACTERIS	STICS				
Drain-Source Diode Forward Vol	ltage	V <sub>SD</sub>	$V_{GS} = 0 V, I_{S} = 4A$			1.4	V
Maximum Continuous Drain-Sou	mum Continuous Drain-Source						•
Diode Forward Current		I <sub>S</sub>				4	A
Maximum Pulsed Drain-Source Diode						10	٨
Forward Current		I <sub>SM</sub>				16	A
Reverse Recovery Time		t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 4A,$		250		ns
Reverse Recovery Charge		Q <sub>RR</sub>	dI <sub>F</sub> /dt = 100 A/µs (Note 1) 1.5				μC

Note: 1. Pulse Test: Pulse width≤300µs, Duty cycle≤2%.

2. Essentially independent of operating temperature.

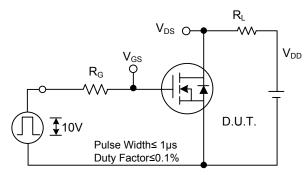


#### TEST CIRCUITS AND WAVEFORMS

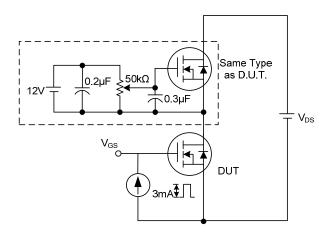


## 4N65-U

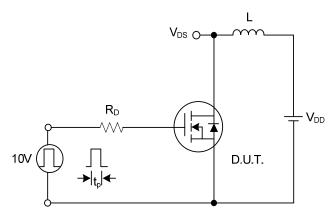
#### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



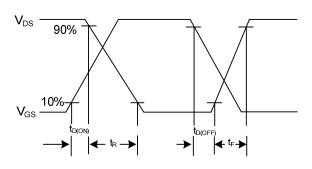
**Switching Test Circuit** 



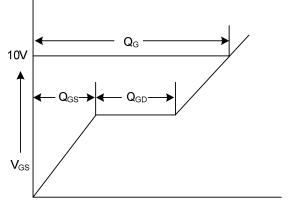
**Gate Charge Test Circuit** 



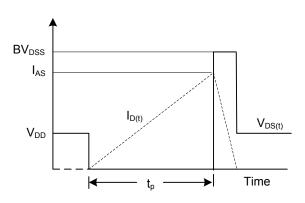
**Unclamped Inductive Switching Test Circuit** 



**Switching Waveforms** 



Charge Gate Charge Waveform



Unclamped Inductive Switching Waveforms



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