UTC UNISONIC TECHNOLOGIES CO., LTD

3N50K-MK

Power MOSFET

3A, 500V N-CHANNEL POWER MOSFET

DESCRIPTION

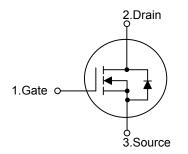
The UTC **3N50K-MK** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

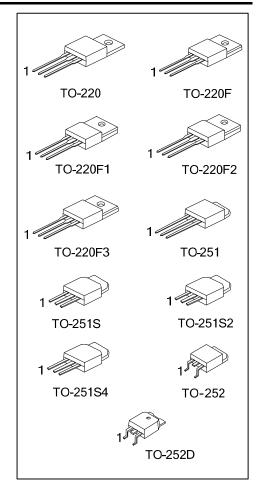
The UTC **3N50K-MK** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.

FEATURES

- * $R_{DS(ON)}$ < 3.2 Ω @ V_{GS} = 10V, I_D = 1.5A
- * High Switching Speed
- * 100% Avalanche Tested

SYMBOL



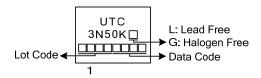


ORDERING INFORMATION

Orderin	Deelvere	Pin Assignment			During		
Lead Free	Halogen Free	Package	1	2	3	Packing	
3N50KL-TA3-T	3N50KG-TA3-T	TO-220	G	D	S	Tube	
3N50KL-TF3-T	3N50KG-TF3-T	TO-220F	G	D	S Tube		
3N50KL-TF1-T	3N50KG-TF1-T	TO-220F1 G		D	S	Tube	
3N50KL-TF2-T	3N50KG-TF2-T	TO-220F2	G	D	S	Tube	
3N50KL-TF3-T	3N50KG-TF3-T	TO-220F3	G	D	S	Tube	
3N50KL-TM3-T	3N50KG-TM3-T	TO-251	G	D	S	Tube	
3N50KL-TMS-T	3N50KG-TMS-T	TO-251S	G	D	S	Tube	
3N50KL-TMS2-T	3N50KG-TMS2-T	TO-251S2	G	D	S	Tube	
3N50KL-TMS4-T	3N50KG-TMS4-T	TO-251S4	G	D	S	Tube	
3N50KL-TN3-R	3N50KG-TN3-R	TO-252	G	D	S	Tape Reel	
3N50KL-TND-R	3N50KG-TND-R	TO-252D	G	D	S	Tape Reel	
Note: Pin Assignment: G: G	ate D: Drain S: Source						
3N50KL-TA3-T	(1) T: Tube, R: Ta	ape Reel					

3N50KL-TA3-T	(I) I: Tube, R: Tape Reel	
TT (1)Packing Type	(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1,	
	TF2: TO-220F2, TF3: TO-220F3, TM3: TO-251	
(2)Package Type	TMS: TO-251S, TMS2: TO-251S2,	
(3)Green Package	TMS4: TO-251S4, TN3: TO-252, TND: TO-252D	
	(3) L: Lead Free, G: Halogen Free and Lead Free	

MARKING





■ ABSOLUTE MAXIMUM RATINGS (T_c=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V _{DSS}	500	V	
Gate-Source Voltage		V _{GSS}	±30	V	
Drain Current	Continuous (T _C =25°C)	ID	3 (Note 5)	А	
Drain Current	Pulsed (Note 2)	I _{DM}	12 (Note 5)	А	
Avalanche Current (Note	e 2)	I _{AR}	3	А	
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	150	mJ	
Peak Diode Recovery d	v/dt (Note 4)	dv/dt	4.5	V/ns	
	TO-220		75	W	
	TO-220F/TO-220F1		25	W	
	TO-220F3		20	vv	
Power Dissipation	TO-220F2	PD	26	W	
	TO-251/TO-251S				
	TO-251S2/TO-251S4		50	W	
	TO-252/TO-252D				
	TO-220		0.5	W/°C	
	TO-220F/TO-220F1		0.2		
	TO-220F3		0.2	W/°C	
Derate above 25°C	TO-220F2	PD	0.208	W/°C	
	TO-251/TO-251S				
	TO-251S2/TO-251S4		0.4	W/°C	
	TO-252/TO-252D				
Power Dissipation		D	36	W	
Derate above 25°C		PD	0.288	W/°C	
Junction Temperature		TJ	+150	°C	
Storage Temperature		T _{STG}	-55~+150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. L = 33.3 mH, I_{AS} = 3A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

4. $I_{SD} \le 3A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

5. Drain current limited by maximum junction temperature.

THERMAL DATA

PARAME	TER	SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3		62.5	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	θ _{JA}	110	°C/W
Junction to Case	TO-220		1.67	
	TO-220F/TO-220F1 TO-220F3		4.9	
	TO-220F2	$\theta_{\rm JC}$	4.8	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D		2.5	



■ ELECTRICAL CHARACTERISTICS (T_c=25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS			T				
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V	500			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =500V, V _{GS} =0V			1	μA
Gate- Source Leakage Current	Forward	- I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse	IGSS	V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS					-		
Gate Threshold Voltage		V _{GS(TH)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$			5.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =1.5A			3.2	Ω
DYNAMIC PARAMETERS			_	<u>.</u>			
Input Capacitance		CISS			415	530	рF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		250	350	pF
Reverse Transfer Capacitance					50	60	рF
SWITCHING PARAMETERS			_	<u>.</u>			
Turn-ON Delay Time		t _{D(ON)}			42	60	ns
Rise Time		t _R	V _{DD} =30V, I _D =0.5A, R _G =25Ω		18	25	ns
Turn-OFF Delay Time		t _{D(OFF)}	(Note 1, 2)		103	130	ns
Fall-Time		t _F			18	25	ns
Total Gate Charge		Q_G			10	13	nC
Gate to Source Charge		Q _{GS}	V_{GS} =10V, V_{DS} =50V, I_{D} =1.3A		1.5		nC
Gate to Drain Charge		Q_{GD}	(Note 1, 2)		5.5		nC
SOURCE- DRAIN DIODE RATI	NGS AND (CHARACTERI	STICS	•			
Maximum Body-Diode Continuous Current		Is				3	Α
Maximum Body-Diode Pulsed C	urrent	I _{SM}				12	Α
Drain-Source Diode Forward Voltage		V _{SD}	I _S =3A, V _{GS} =0V			1.4	V
	*	-					

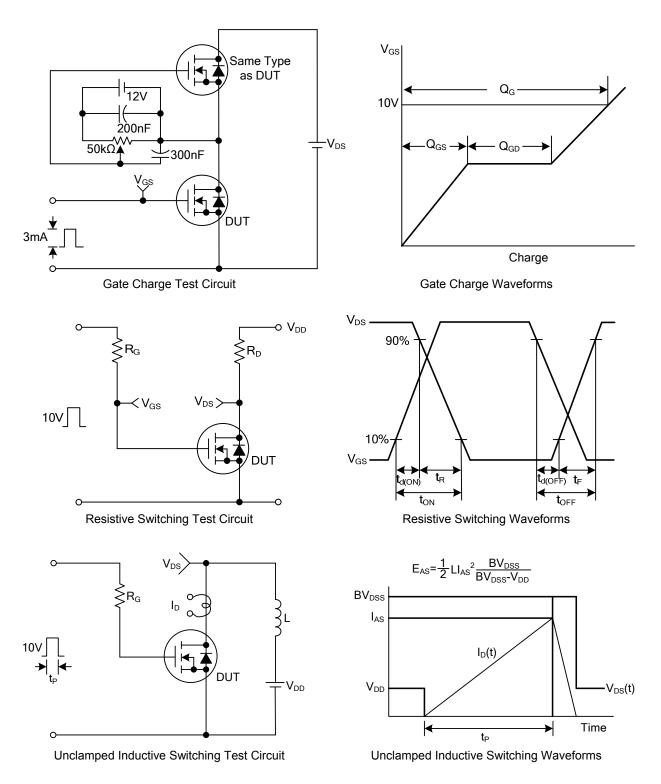
Notes: 1. Pulse Test: Pulse width \leq 300µs, Duty cycle \leq 2%

2. Essentially independent of operating temperature



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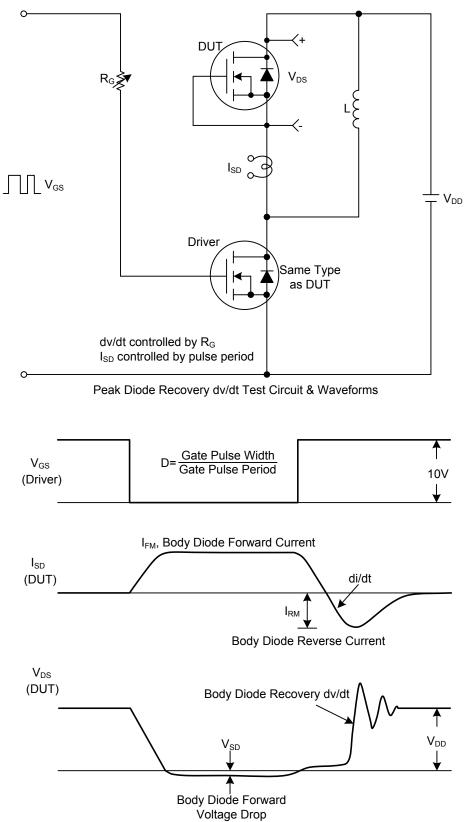
TEST CIRCUITS AND WAVEFORMS





3N50K-MK

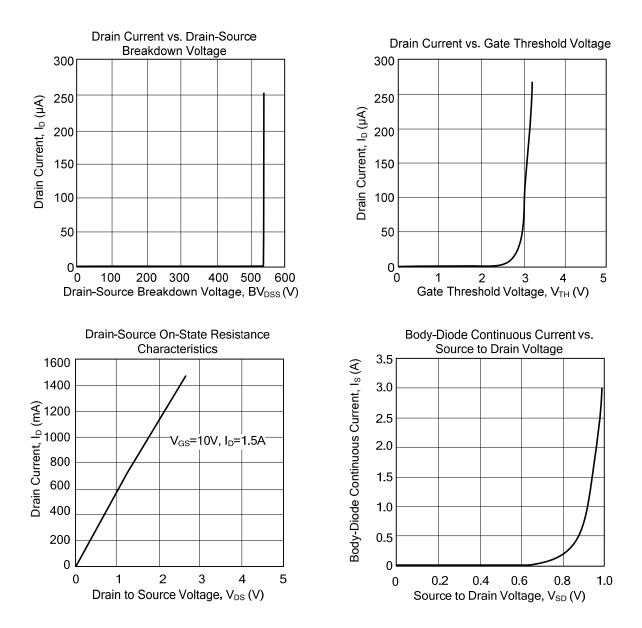
■ TEST CIRCUITS AND WAVEFORMS(Cont.)





3N50K-MK

TYPICAL CHARACTERISTICS



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