

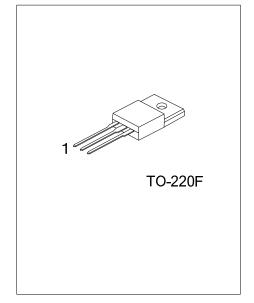
BTA12

12A TRIACS

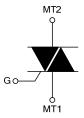
DESCRIPTION

The UTC **BTA12** is a 12A triacs which can be operated in 4 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC **BTA12** is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.



SYMBOL



ORDERING INFORMATION

Ordering Number		Packago	Pin	Assignr	Packing		
Lead Free	Halogen Free	Package	1	2	3	Facking	
BTA12L-x-x-TF3-T	BTA12G-x-x-TF3-T	TO-220F	MT1	MT2	G	Tube	

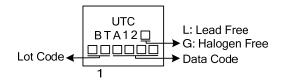
BTA12L-x-x-TF3-T (1)Packing Type (2)Package Type (3)Sensitivity and type (4)Voltage (5)Green Package	 (1) T: Tube (2) TF3: TO-220F (3) refer to SENSITIVITY AND TYPE (4) 6: 600V, 8: 800V (5) L: Lead Free, G: Halogen Free and Lead Free
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SENSITIVITY AND TYPE

PART NUMBER	VULI	AGE	SENSITIVITY	TYPE		
PART NUMBER	600V	800V	SENSITIVITY	TTPE		
В	\bigcirc	\odot	50mA	STANDARD		
С	\bigcirc	\odot	25mA	STANDARD		

○: Available

MARKING



ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
RMS On-State Current (Full S	Full Sine Wave) T _C =90°C		I _{T(RMS)}	12	А
Non Repetitive Surge Peak On-State Current (Full Cycle,			I _{TSM}	120	А
	F=60 Hz	t=16.7ms	IISM	126	А
I ² t Value for Fusing	t _P =10ms		l ² t	78	A ² s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz	T _J =125°C	dl/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms	Tյ=25°C	$V_{\text{DSM}}/V_{\text{RSM}}$	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	TJ=125°C	I _{GM}	4	А
Average Gate Power Dissipat	ion	TJ=125°C	P _{G(AV)}	1	W
Operating Junction Temperature		TJ	-40~+125	°C	
Storage Junction Temperature		T _{STG}	-40~+150	°C	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ_{JA}	60	°C/W
Junction to Case (AC)	θ _{JC}	2.3	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

FOR STANDARD TYPE (4 QUADRANTS)

PARAMETER	SYMBOL	TEST CONDITIONS			С			В		
FANAIWIETEN	STMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current	I		- -			25			50	mA
(Note 1)	I_{GT}	V _D =12V, R _L =33Ω	IV			50			100	mA
Gate Trigger Voltage	V_{GT}		ALL			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	V _D =V _{DRM} , R _L =3.3kΩ, T _J =125°C	ALL	0.2			0.2			V
Holding Current (Note 2)	I _H	I⊤=500mA				25			50	mA
Latabian Current		1 -1 0 1	I-III-IV			40			50	mA
Latching Current	ΙL	I _G =1.2 I _{GT} II				80			100	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		200			400			V/µs
Critical Rate of Rise of Off-State Voltage at Commutation(Note 2)	(dV/dt)c	(dl/dt)c=5.3A/ms, T _J = 125°C		5			10			V/µs

STATIC CHARACTERISTICS

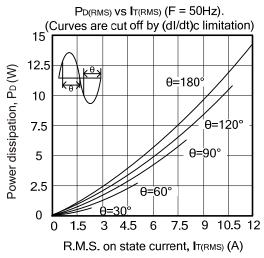
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note)	V _T	I _{TM} =17A, t _p =380μs	TJ=25°C			1.55	V
Threshold Voltage(Note)	V _{TO}		T _J =125°C			0.85	V
Dynamic Resistance(Note)	R₀		TJ=125°C			35	mΩ
Departitive Deak Off State Current	I _{DRM}		TJ=25°C			5	μA
Repetitive Peak Off-State Current	I _{RRM}	V _{DRM} =V _{RRM}	TJ=125°C			1	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.







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