

UNISONIC TECHNOLOGIES CO., LTD

BTB316A Preliminary TRIAC

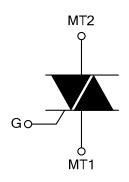
16A TRIACS

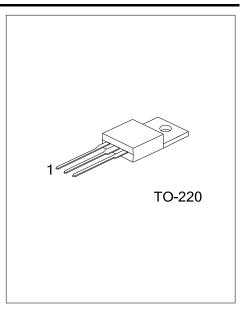
DESCRIPTION

The UTC **BTB316A** is a 16A triacs which can be operated in 3 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances.

The UTC **BTB316A** is suitable for inductive load switching operations, also can be used in ON/OFF function applications such as induction motor starting circuits, heating regulation, static relays etc.

■ SYMBOL

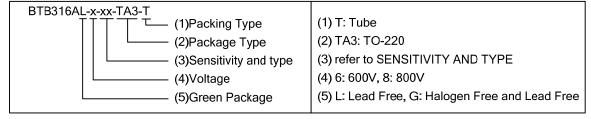




■ ORDERING INFORMATION

Ordering Number		Dookogo	Pin .	Assignn	Dooking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
BTB316AL-x-xx-TA3-T	BTB316AG-x-xx-TA3-T	TO-220	MT1	MT2	G	Tube	

Note: Pin Assignment: MT1: MT1 MT2: MT2 G: Gate

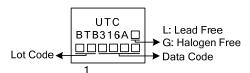


■ SENSITIVITY AND TYPE

PART NUMBER	VOLT	ΓAGE	SENSITIVITY	TYPF		
PART NUMBER	600V	800V	SENSITIVITY	ITPE		
BW	0	0	50mA	SNUBBERLESS		
CW	0	0	35mA	SNUBBERLESS		
SW	0	0	10mA	LOGIC LEVEL		

⊚: Available

■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full	RMS On-State Current (Full Sine Wave) T _C =86°C		I _{T(RMS)}	16	Α
Non Repetitive Surge Peak On-State Current (Full	F=50 Hz t=20ms		I _{TSM}	160	Α
Cycle, T _J initial=25°C)	F=60 Hz	t=16.7ms	TISM	168	Α
I ² t Value for Fusing	t _P =10ms		l ² t	144	A ² s
Critical Rate of Rise of On-State Current I _G =2xI _{GT} , tr≤100ns	F=120 Hz T _J =125°C		dI/dt	50	A/µs
Non Repetitive Surge Peak Off-State Voltage	t _P =10ms T _J =25°C		V_{DSM}/V_{RSM}	V _{DRM} /V _{RRM} +100	V
Peak Gate Current	t _P =20µs	T _J =125°C	I_{GM}	4	Α
Average Gate Power Dissipation T _J =125°C		$P_{G(AV)}$	1	W	
Operating Junction Temperature		T_J	-40~+125	°C	
Storage Junction Temperature			T_{STG}	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	60	°C/W	
Junction to Case (AC)	θјς	1.2	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_J =25°C unless otherwise specified)

FOR SNUBBERLESS TYPE and LOGIC LEVEL TYPE (3 QUADRANTS)

			1-	-,		/	1			1			1
PARAMETER	SYMBOL	TEST CONDITIONS		SW			CW			BW			UNIT
FARAIVIETER	STIVIBOL	TEST CONDITION	ONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	CIVIT
Gate Trigger Current (Note 1)	I _{GT}	V _D =12V, R _L =33Ω	1-11-111			10			35			50	mA
Gate Trigger Voltage	V_{GT}		1-11-111			1.3			1.3			1.3	V
Gate Non-Trigger Voltage	V_{GD}	$V_D = V_{DRM}$, $R_L = 3.3k\Omega$, $T_J = 125^{\circ}C$	1-11-111	0.2			0.2			0.2			>
Holding Current (Note 2)	I _H	I _T =500mA				15			35			50	mA
Latabias Cumant		1 401	I-III			25			50			70	mA
Latching Current	IL	I _G =1.2I _{GT}	II			30			60			80	mA
Critical Rate of Rise of Off-State Voltage (Note 2)	dV/dt	V _D =67%V _{DRM} , Gate Open, T _J =125°C		40			500			1000			V/µs
Critical Rate of Rise of Off-State Voltage at		(dV/dt)c=0.1V/μs, Τ _J =125°C		8.5									A/ms
	(dl/dt)c	(dI/dt)c $(dV/dt)c=10V/\mu s$, $T_J=125^{\circ}C$		3.0									A/ms
Commutation(Note 2)		Without Snubber T _J =125°C					8.5			14			A/ms

Notes: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage(Note 2)	V_{TM}	I _{TM} =22.5A, t _p =380μs	T _J =25°C			1.55	V
Threshold Voltage(Note 2)	V_{TO}		T _J =125°C			0.85	V
Dynamic Resistance(Note 2)	R_D		T _J =125°C			25	mΩ
D. C. D. L. OSS S. C. C.	I _{DRM}	\/ -\/	T _J =25°C			5	μA
Repetitive Peak Off-State Current	IppM	$V_{DRM}=V_{RRM}$	T ₁ =125°C			2	mA

Note: 1. Minimum I_{GT} is guaranteed at 5% of I_{GT} max.

2. For both polarities of MT2 referenced to MT1.

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