UTC UNISONIC TECHNOLOGIES CO., LTD

UT139F/G TRIAC

TRIAC

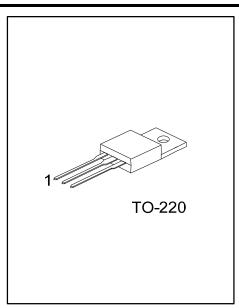
DESCRIPTION

The UTC UT139F/G is a triacs, it uses UTC's advanced technology to provide customers with high bidirectional transient and high thermal cycling performance.

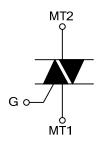
The UTC UT139F/G is suitable for motor control, heating and static switching, etc.

FEATURES

- * High bidirectional transient
- * High thermal cycling performance
- * Blocking voltage capability



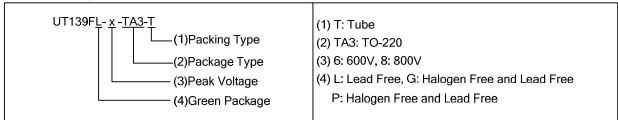
SYMBOL



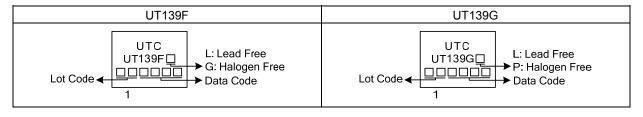
ORDERING INFORMATION

Order N	Deelsene	Pin Assignment			Dooking		
Normal	Lead Free Plating	Package	1	2	3	Packing	
UT139FL-x-TA3-T	UT139FG-x-TA3-T	TO-220	MT1	MT2	G	Tube	
UT139GL-x-TA3-T	UT139GP-x-TA3-T	TO-220	MT1	MT2	G	Tube	

Note: Pin Assignment: G: Gate



MARKING



www.unisonic.com.tw 1 of 4

■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT	
Depatitive mode off state valtages	UT139F/G-6		600 (Note 2)	V
Repetitive peak off-state voltages	UT139F/G-8	V_{DRM}	800	V
RMS on-state current full sine wave; T _{mb} s	I _{T(RMS)}	16	Α	
Non-repetitive peak on-state current	t = 20ms	I _{TSM}	140	^
(Full sine wave; $T_J = 25^{\circ}C$ prior to surge)	t = 16.7 ms		150	Α
I ² t for fusing	t = 10 ms	l ² t	21	A^2s
	T2+ G+		50	A/µs
Repetitive rate of rise of on-state current after triggering	T2+ G-	dl⊤/dt	50	A/µs
	T2- G-		50	A/µs
I_{TM} =20A; I_{G} =0.2A; d_{IG} /dt=0.2A/ μ s	T2- G+		10	A/µs
Peak gate voltage	V_{GM}	5	V	
Peak gate current	I _{GM}	2	Α	
Peak gate power	P _{GM}	5	W	
Average gate power (over any 20 ms perio	P _{G(AV)}	0.5	W	
Junction Temperature	TJ	125	°C	
Storage Temperature	T _{STG}	-40 ~ +150	°C	

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL RESISTANCES

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	
Thermal resistance Junction to Ambient In Free		θ_{JA}		60		°C/W
Thermal resistance Junction to mounting	Full cycle	0			1.2	°C/W
base	Half cycle	θ_{JC}			1.7	°C/W

■ STATIC CHARACTERISTICS (T_J =25°C, unless otherwise specified)

DADAMETED	SYMBOL	TEST CONDITIONS MIN		MINI	TYP	MAX		UNIT
PARAMETER	STIVIBUL			IVIIIN	ITP	UT139F	UT139G	UNIT
Gate Trigger Current	I _{GT}	V _D =12V, I _T =0.1A	T2+G+		5	25	50	mA
			T2+G-		8	25	50	
			T2-G-		11	25	50	
			T2-G+		30	70	100	
Latching Current	lι	V _D =12V, I _{GT} =0.1A	T2+G+		7	40	60	mA
			T2+G-		20	60	90	
			T2-G-		8	40	60	
			T2-G+		10	60	90	
Holding Current	l _Η	V_D =12V, I_{GT} =0.1A			6	30	60	mA
On-State Voltage	V_{T}	I _T =20A			1.2	1.6		V
Gate Trigger Voltage	V_{GT}	V _D =12V, I _T =0.1A			0.7	1.5		V
		V _D =400V, I _T =0.1A, T _J =125°C			0.40			V
Off-State Leakage Current	I_{D}	V _D =V _{DRM(max)} , T _J =125°C			0.1	0.5		mA

^{2.} Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6A/µs.

UT139F/G TRIAC

■ **DYNAMIC CHARACTERISTICS** (T_J =25°C, unless otherwise specified)

PARAMETER	CVMDOL	TEST CONDITIONS	MIN		TYP	NANY	LINIT
PARAMETER	SYMBOL	TEST CONDITIONS	UT139F	UT139G	ITP	MAX	UNII
Critical Rate Of Rise Of Off-State Voltage	dV _D /dt	V _{DM} =67% V _{DRM(max)} , T _J =125°C, Exponential waveform, gate open circuit	50	200	250		V/µs
Critical Rate Of Change Of Commutating Voltage	dV _{com} /dt	V _{DM} =400V, T _J =95°C, I _{T(RMS)} =16A, dI _{com} /dt=7.2A/ms, gate open circuit		10	20		V/µs
Gate Controlled Turn-On Time	Task	I_{TM} =20A, V_D = $V_{DRM(max)}$, I_G =0.1A, dI_G/dt =5A/ μ s			2		μs

UT139F/G

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