

UNISONIC TECHNOLOGIES CO., LTD

UT139E

TRIAC

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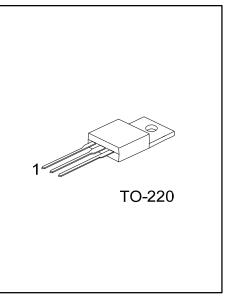
DESCRIPTION

The UTC **UT139E** is a triacs, it uses UTC's advanced technology to provide customers with high bidirectional transient and high thermal cycling performance.

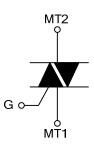
The UTC $\ensuremath{\text{UT139E}}$ is suitable for motor control, heating and static switching, etc.

FEATURES

- * High bidirectional transient
- * High thermal cycling performance
- * Blocking voltage capability



SYMBOL

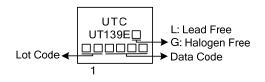


ORDERING INFORMATION

Order N	Dookogo	Pin Assignment			Deaking	
Normal	Lead Free Plating	Package	1	2	3	Packing
UT139EL-x-TA3-T UT139EG-x-TA3-T		TO-220	MT1	MT2	G	Tube
Note: Pin Assignment: G: Gate						

UT139EL- x - <u>TA3-T</u> (1)Packing Type (2)Package Type (3)Peak Voltage (4)Green Package	 (1) T: Tube (2) TA3: TO-220 (3) 6: 600V, 8: 800V (4) L: Lead Free, G: Halogen Free and Lead Free 	
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MARKING



ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT	
Depetitive people off state veltages	UT139E-6	N/	600 (Note 2)	V
Repetitive peak off-state voltages	UT139E-8	V _{DRM}	800	V
RMS on-state current full sine wave; T _{mb} ≤99°C		I _{T(RMS)}	16	А
Non-repetitive peak on-state current	t = 20ms		140	٨
(Full sine wave; T _J = 25°C prior to surge)	t = 16.7 ms	I _{TSM}	150	A
I ² t for fusing	t = 10 ms	l ² t	21	A ² s
Repetitive rate of rise of on-state current after triggering	T2+ G+		50	A/µs
	T2+ G-	dl⊤ /dt	50	A/µs
	T2- G-		50	A/µs
I _{TM} =20A; I _G =0.2A; d _{IG} /dt=0.2A/μs	T2- G+		10	A/µs
Peak gate voltage		V_{GM}	5	V
Peak gate current	I _{GM}	2	А	
Peak gate power	P _{GM}	5	W	
Average gate power (over any 20 ms period	$P_{G(AV)}$	0.5	W	
Junction Temperature	TJ	125	°C	
Storage Temperature		T _{STG}	-40 ~ +150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6A/µs.

THERMAL RESISTANCES

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT
Thermal resistance Junction to Ambient In Free Air		θ _{JA}		60		°C/W
Thermal resistance Junction to mounting	Full cycle	0			1.2	°C/W
base	Half cycle	θις			1.7	°C/W

■ STATIC CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
		V _D =12V, I _T =0.1A	T2+G+			10	mA
			T2+G-			10	
Gate Trigger Current	I _{GT}		T2-G-			10	
			T2-G+			25	
Latching Current	١L	V _D =12V, I _{GT} =0.1A	T2+G+		7	40	mA
			T2+G-		20	60	
			T2-G-		8	40	
			T2-G+		10	60	
Holding Current	I _H	V _D =12V, I _{GT} =0.1A			6	30	mA
On-State Voltage	VT	I _T =20A			1.2	1.6	V
Gate Trigger Voltage	Vot	V _D =12V, I _T =0.1A			0.7	1.5	V
		V _D =400V, I _T =0.1A, T _J =125°C		0.25	0.40		V
Off-State Leakage Current	Ι _D	V _D =V _{DRM(max)} , T _J =125°C			0.1	0.5	mA

■ **DYNAMIC CHARACTERISTICS** (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Critical Rate Of Rise Of Off-State Voltage		V _{DM} =67% V _{DRM(max)} , T _J =125°C, Exponential waveform, gate open circuit	100	250		V/µs
Critical Rate Of Change Of Commutating Voltage		V _{DM} =400V, T _J =95°C, I _{T(RMS)} =16A, dl _{com} /dt=7.2A/ms, gate open circuit		20		V/µs
Gate Controlled Turn-On Time	Int	I _{TM} =20A, V _D =V _{DRM(max)} , I _G =0.1A, dI _G /dt=5A/μs		2		μs



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