

# UNISONIC TECHNOLOGIES CO., LTD

MGBR15V60 Preliminary DIODE

# MOS GATED BARRIER RECTIFIER

### DESCRIPTION

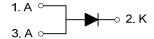
The UTC MGBR15V60 is a surface mount mos gated barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high current capability, etc.

The UTC **MGBR15V60** suitable for free wheeling, high frequency inverters, polarity protection, and low voltage.

### **■ FEATURES**

- \* Very low forward voltage drop
- \* High current capability
- \* High surge capability
- \* High efficiency

### ■ SYMBOL

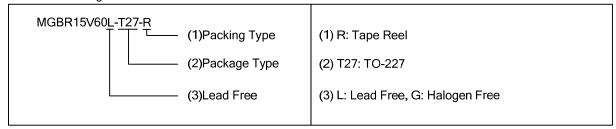


# 1 TO-277

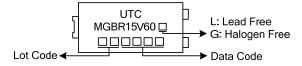
### **■ ORDERING INFORMATION**

| Ordering Number  |                  | Doolsons | Pin Assignment |   |   | Dealing   |  |
|------------------|------------------|----------|----------------|---|---|-----------|--|
| Lead Free        | Halogen Free     | Package  | 1              | 2 | 3 | Packing   |  |
| MGBR15V60L-T27-R | MGBR15V60G-T27-R | TO-277   | Α              | K | Α | Tape Reel |  |

Note: Pin Assignment: A: Anode K: Common Cathode



# ■ MARKING



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# ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

| PARAMETER   |                       | SYMBOL           | RATINGS  | UNIT |
|---|-----------------------|------------------|----------|------|
| DC Blocking Voltage (Note 1)  |                       | $V_{RM}$         | 60       | V    |
| Working Peak Reverse Voltage  |                       | $V_{RWM}$        | 60       | V    |
| Peak Repetitive Reverse Voltage   |                       | $V_{RRM}$        | 60       | V    |
| RMS Reverse Voltage   |                       | $V_{R(RMS)}$     | 42       | V    |
| Average Rectified Output Current  | T <sub>C</sub> =125°C | lo               | 15       | Α    |
| Non-Repetitive Peak Forward Surge Current 8.3ms<br>Single Half Sine-Wave Superimposed on Rated Load |                       | I <sub>FSM</sub> | 200      | Α    |
| Operating Junction Temperature  |                       | $T_J$            | -65~+150 | °C   |
| Storage Temperature   |                       | $T_{STG}$        | -65~+150 | °C   |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### **■ THERMAL CHARACTERISTICS**

| PARAMETER           | SYMBOL        | RATINGS | UNIT |
|---------------------|---------------|---------|------|
| Junction to Ambient | $\theta_{JA}$ | 73      | °C/W |
| Junction to Case    | $\theta_{JC}$ | 13      | °C/W |

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> =25°C unless otherwise specified.)

| PARAMETER                          | SYMBOL          | TEST CONDITIONS                            | MIN | TYP | MAX  | UNIT |
|------------------------------------|-----------------|--|-----|-----|------|------|
| Reverse Breakdown Voltage (Note 1) | $V_{(BR)R}$     | I <sub>R</sub> =0.50mA                     | 60  |     |      | V    |
| Forward Voltage Drop               | V <sub>EM</sub> | I <sub>F</sub> =15A, T <sub>C</sub> =25°C  |     |     | 0.6  | V    |
|                                    |                 | I <sub>F</sub> =15A, T <sub>C</sub> =125°C |     |     | 0.55 | V    |
| Peak Reverse Current at Rated DC   |                 | V <sub>R</sub> =60V, T <sub>C</sub> =25°C  |     |     | 500  | μΑ   |
| locking Voltage (Note 1)           | I <sub>RM</sub> | V <sub>R</sub> =60V, T <sub>C</sub> =125°C |     |     | 50   | mΑ   |

Notes: 1. Short duration pulse test used to minimize self-heating effect.

- 2. Thermal resistance junction to case mounted on heatsink.
- 3. Mounted on an FR4 PCB, single-sided copper, with 100cm<sup>2</sup> copper pad area.

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