

MGBR10L200C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

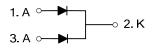
DESCRIPTION

The UTC **MGBR10L200C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

* Low forward voltage drop * High switching speed

SYMBOL



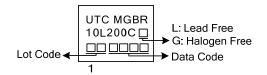
ORDERING INFORMATION

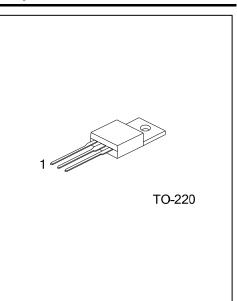
Ordering Number		Dookogo	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
MGBR10L200CL-TA3-T MGBR10L200CG-TA3-T		TO-220	Α	К	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode

MGBR10L200CL- <u>TA3-T</u> T T T	
(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) TA3: TO-220 (3) L: Lead Free, G: Halogen Free and Lead Free

MARKING





■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20 %.					
PARAMETER	SYMBOL	RATINGS	UNIT		
DC Blocking Voltage	V _{RM}	200	V		
Working Peak Reverse Voltage	V _{RWM}	200	V		
Peak Repetitive Reverse Voltage	V _{RRM}	200	V		
Average Destified Output Current Per Device Per Leg		5	А		
Average Rectified Output Current Per Device Total	Io	10	А		
Non-Repetitive Peak Forward Surge Current 8.3ms Sing Half Sine-Wave Superimposed on Rated Load	e I _{FSM}	110	А		
Operating Junction Temperature	TJ	-65~+150	°C		
Storage Temperature	T _{STG}	-65~+150	°C		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	62.5	°C/W
Junction to Case	θ _{JC}	2	°C/W

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	200			V
Forward Voltage Drop		I _F =5A, T _J =25°C			0.9	V
		I _F =5A, T _J =125°C			0.74	V
Leakage Current (Note 1)	DM	V _R =200V, T _J =25°C			100	μA
		V _R =200V, T _J =125°C			25	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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