

UNISONIC TECHNOLOGIES CO., LTD

TGBR10V100 Advance DIODE

TRENCH MOS SCHOTTKY BARRIER RECTIFIER

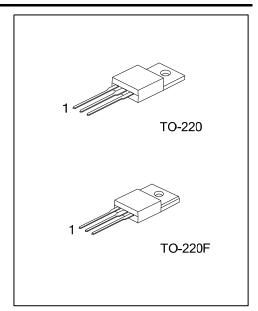
■ DESCRIPTION

The UTC **TGBR10V100** is a trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high current capability, etc.

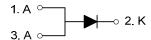
The UTC **TGBR10V100** suitable for free wheeling, high frequency inverters, polarity protection, and low voltage.

■ FEATURES

- * Very low forward voltage drop
- * High current capability
- * High surge capability
- * High efficiency



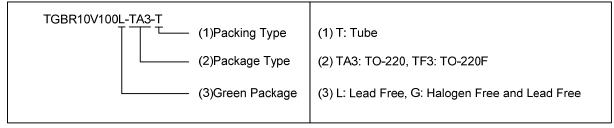
■ SYMBOL



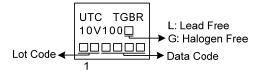
ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
TGBR10V100L-TA3-T	TGBR10V100G-TA3-T	TO-220	Α	K	Α	Tube	
TGBR10V100L-TF3-T	TGBR10V100G-TF3-T	TO-220F	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage (Note 1)	V_{RM}	100	V
Working Peak Reverse Voltage	V_{RWM}	100	V
Peak Repetitive Reverse Voltage	V_{RRM}	100	V
Average Rectified Output Current T _C =125°C	Ιο	10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	150	Α
Operating Junction Temperature	TJ	-65~+150	°C
Storage Temperature	T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		θ_{JA}	62.5	°C/W	
lunction to Coop	TO-220	0	2	°C/W	
Junction to Case	TO-220F	$\theta_{ m JC}$	3.31	C/VV	

■ **ELECTRICAL CHARACTERISTICS** (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	100			V
Forward Voltage Drop	V _{EM}	I _F =10A, T _C =25°C			0.75	V
		I _F =10A, T _C =125°C			0.60	V
Peak Reverse Current at Rated DC		V _R =100V, T _C =25°C			100	μΑ
Blocking Voltage (Note 1)	IRM	V _R =100V, T _C =125°C			6	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

- 2. Thermal resistance junction to case mounted on heatsink.
- 3. Mounted on an FR4 PCB, single-sided copper, with 100cm² copper pad area.

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