

# UNISONIC TECHNOLOGIES CO., LTD

**TGBR30U100C** 

**Preliminary** 

**DIODE** 

### DUAL TRENCH MOS SCHOTTKY BARRIER RECTIFIER

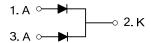
### DESCRIPTION

The UTC **TGBR30U100C** is a dual trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

### **■ FEATURES**

- \* Ultra low forward voltage drop
- \* High switching speed

### ■ SYMBOL



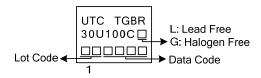
## ■ ORDERING INFORMATION

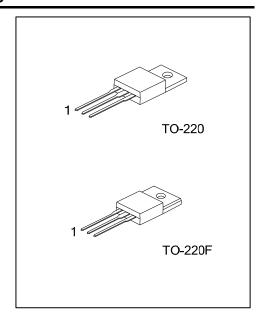
| Ordering Number    |                    | Dookogo | Pin Assignment |   |   | Dooking |  |
|--------------------|--------------------|---------|----------------|---|---|---------|--|
| Lead Free          | Halogen Free       | Package | 1              | 2 | 3 | Packing |  |
| TGBR30U100CL-TA3-T | TGBR30U100CG-TA3-T | TO-220  | Α              | K | Α | Tube    |  |
| TGBR30U100CL-TF3-T | TGBR30U100CG-TF3-T | TO-220F | Α              | K | Α | Tube    |  |

Note: Pin Assignment: A: Anode K: Cathode

TGBR30U100CL-TA3-T
(1)Packing Type
(2) TA3: TO-220, TF3: TO-220F
(3)Green Package
(3) L: Lead Free, G: Halogen Free and Lead Free

### **■ MARKING**





### ■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T<sub>A</sub>=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

| PARAMETER  |         | SYMBOL           | RATINGS  | UNIT |
|--|---------|------------------|----------|------|
| DC Blocking Voltage  |         | $V_{RM}$         | 100      | V    |
| Working Peak Reverse Voltage   |         | $V_{RWM}$        | 100      | V    |
| Peak Repetitive Reverse Voltage  |         | $V_{RRM}$        | 100      | V    |
| Average Rectified Output Current Per Device  | Per Leg | l <sub>o</sub>   | 15       | Α    |
|  | Total   |                  | 30       | Α    |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load |         | I <sub>FSM</sub> | 300      | Α    |
| Operating Junction Temperature   |         | TJ               | -65~+150 | °C   |
| Storage Temperature  |         | $T_{STG}$        | -65~+150 | °C   |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ THERMAL CHARACTERISTICS (PER LEG)

| PARAMETER           |         | SYMBOL        | RATINGS | UNIT   |
|---------------------|---------|---------------|---------|--------|
| Junction to Ambient | _       | $\theta_{JA}$ | 62.5    | °C/W   |
| lunation to Coop    | TO-220  | 0             | 2       | °C/\\/ |
| Junction to Case    | TO-220F | $\theta_{JC}$ | 4       | °C/W   |

### ■ ELECTRICAL CHARACTERISTICS (PER LEG) (T<sub>A</sub> =25°C unless otherwise specified.)

| PARAMETER                          | SYMBOL      | TEST CONDITIONS                             | MIN | TYP | MAX  | UNIT |
|------------------------------------|-------------|---|-----|-----|------|------|
| Reverse Breakdown Voltage (Note 1) | $V_{(BR)R}$ | I <sub>R</sub> =0.50mA                      | 100 |     |      | V    |
| Forward Voltage Drop               | I VEM       | I <sub>F</sub> =15A, T <sub>J</sub> =25°C   |     |     | 0.70 | V    |
|                                    |             | I <sub>F</sub> =15A, T <sub>J</sub> =125°C  |     |     | 0.63 | V    |
| Leakage Current (Note 1)           | I DM        | V <sub>R</sub> =100V, T <sub>J</sub> =25°C  |     |     | 200  | μA   |
|                                    |             | V <sub>R</sub> =100V, T <sub>J</sub> =125°C |     |     | 30   | mΑ   |

Notes: 1. Short duration pulse test used to minimize self-heating effect.

<sup>2.</sup> Thermal resistance junction to case mounted on heatsink.

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