

TGBR40L100C

Preliminary

DIODE

DUAL TRENCH MOS SCHOTTKY BARRIER RECTIFIER

DESCRIPTION

The UTC **TGBR40L100C** is a dual trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

* Low forward voltage drop * High switching speed

SYMBOL

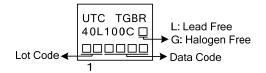
ORDERING INFORMATION

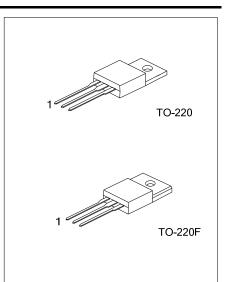
Ordering Number		Packago	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
TGBR40L100CL-TA3-T	TGBR40L100CG-TA3-T	TO-220	А	К	А	Tube	
TGBR40L100CL-TF3-T	TGBR40L100CG-TF3-T	TO-220F	А	К	А	Tube	

Note: Pin Assignment: A: Anode K: Cathode

TGBR40L100CL-TA3-T		
	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) TA3: TO-220, TF3: TO-220F (3) L: Lead Free, G: Halogen Free and Lead Free

MARKING





■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%

Tor capacitance load, derate current by 2076				
PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V _{RM}	100	V
Working Peak Reverse Voltage		V _{RWM}	100	V
Peak Repetitive Reverse Voltage		V _{RRM}	100	V
Average Rectified Output Current Per	Per Leg		20	А
Device	Total	I _O	40	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	280	А
Operating Junction Temperature		TJ	-65~+150	°C
Storage Temperature		T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA (PER LEG)

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient		θ _{JA}	62.5	°C/W
lunction to Coop	TO-220	0	2	°C/M
Junction to Case	TO-220F	θις	3.31	°C/W

ELECTRICAL CHARACTERISTICS (PER LEG) (T_A=25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	100			V
Forward Voltage Drop	V _{FM}	I _F =20A, T _J =25°C			0.80	V
		I _F =20A, T _J =125°C			0.75	V
Leakage Current (Note 1)	RM	V _R =100V, T _J =25°C			100	μA
		V _R =100V, T _J =125°C			10	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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