

UNISONIC TECHNOLOGIES CO., LTD

MGBR20L80C

Preliminary

DIODE

DUAL MOS GATED BARRIER RECTIFIER

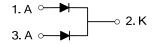
DESCRIPTION

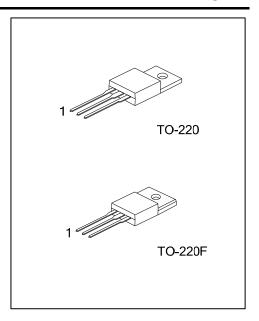
The UTC **MGBR20L80C** is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

■ SYMBOL

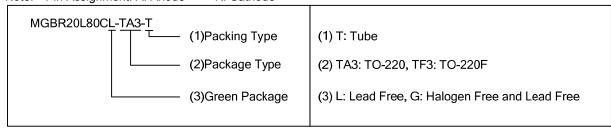




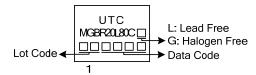
■ ORDERING INFORMATION

Ordering Number		Packago	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
MGBR20L80CL-TA3-T	MGBR20L80CG-TA3-T	TO-220	A	K	Α	Tube	
MGBR20L80CL-TF3-T	MGBR20L80CG-TF3-T	TO-220F	Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



■ MARKING



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■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage		V_{RM}	80	V
Working Peak Reverse Voltage		V_{RWM}	80	V
Repetitive Peak Reverse Voltage		V_{RRM}	80	V
Average Rectified Output Current Per	Per Leg		10	Α
Device	Total	I _O	20	Α
on-Repetitive Peak Forward Surge Current 8.3ms Single lalf Sine-Wave Superimposed on Rated Load		I _{FSM}	100	Α
Operating Junction Temperature		T_J	-65 ~ +175	°C
Storage Temperature		T_{STG}	-65 ~ + 175	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62.5	°C/W	
Junction to Case	θ _{JC}	2	°C/W	

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	80			V
		I _F =10A, T _J =25°C			0.81	V
		I _F =10A, T _J =125°C			0.70	V
Leakage Current (Note 1)	DM	V _R =80V, T _J =25°C			600	μΑ
		V _R =80V, T _J =125°C			20	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.

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