

TGBR40V100C

Advance

DIODE

DUAL TRENCH MOS SCHOTTKY BARRIER RECTIFIER

DESCRIPTION

The UTC **TGBR40V100C** is a dual trench mos schottky barrier rectifier, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

FEATURES

* Very low forward voltage drop * High switching speed



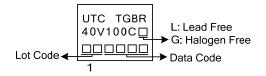
ORDERING INFORMATION

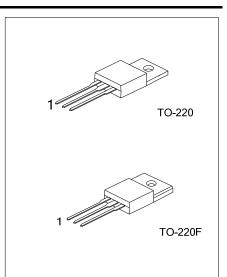
Ordering Number		Dookago	Pin Assignment			Booking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
TGBR40V100CL-TA3-T	TGBR40V100CG-TA3-T	TO-220	А	к	А	Tube	
TGBR40V100CL-TF3-T	TGBR40V100CG-TF3-T	TO-220F	А	к	А	Tube	

Note: Pin Assignment: A: Anode K: Cathode

TGBR40V100CL-TA3-T	(1) T: Tube
(2)Package Type	(2) TA3: TO-220, TF3: TO-220F
(3)Green Package	(3) L: Lead Free, G: Halogen Free and Lead Free

MARKING





■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.			
PARAMETER	SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V _{RM}	100	V
Working Peak Reverse Voltage	V _{RWM}	100	V
Peak Repetitive Reverse Voltage	V _{RRM}	100	V
Average Rectified Output Current Per Device Per Leg	· I _o	20	А
Total		40	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	300	А
Operating Junction Temperature	TJ	-65~+150	°C
Storage Temperature	T _{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL CHARACTERISTICS (PER LEG)

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		θ _{JA}	62.5	°C/W	
Junction to Case	TO-220	0	2	°C/W	
	TO-220F	θ _{JC}	3.31		

■ ELECTRICAL CHARACTERISTICS (PER LEG) (T_A =25°C unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	V _{(BR)R}	I _R =0.50mA	100			V
Forward Voltage Drop	V _{FM}	I _F =20A, T _J =25°C			0.75	V
		I _F =20A, T _J =125°C			0.70	V
Leakage Current (Note 1)	DM	V _R =100V, T _J =25°C			200	μA
		V _R =100V, T _J =125°C			20	mA

Notes: 1. Short duration pulse test used to minimize self-heating effect.

2. Thermal resistance junction to case mounted on heatsink.



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