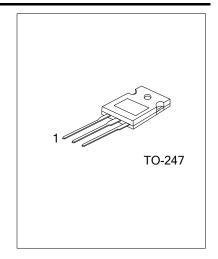
URFP064 Preliminary Power MOSFET

# 70A, 60V N-CHANNEL POWER MOSFET

#### **■** DESCRIPTION

The UTC **URFP064** is an N-channel enhancement power MOSFET using UTC's advanced technology to provide the customers with a minimum on-state resistance and high switching speed.



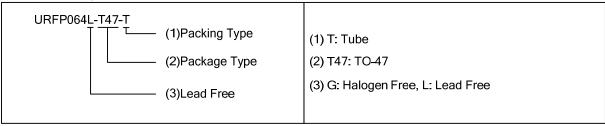
## **■ FEATURES**

- \*  $R_{DS(ON)}$ <20m $\Omega$  @  $V_{GS}$ =10V, $I_D$ =70A
- \* High Switching Speed

#### ORDERING INFORMATION

Ordering	Daelsege	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing
URFP064L-T47-T	URFP064G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source



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#### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	60	V	
Gate-Source Voltage		$V_{GSS}$	±20	V	
Drain Current	Continuous	I <sub>D</sub>	70	^	
	Pulsed (Note 2)	I <sub>DM</sub>	280	A	
Avalanche Current		I <sub>AR</sub>	70	Α	
Single Pulsed Avalanche Energy		E <sub>AS</sub>	1000	mJ	
Power Dissipation		$P_D$	190	W	
Junction Temperature		T <sub>J</sub>	-55~+150		
Storage Temperature		T <sub>STG</sub>	-55~+150		

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. L = 69mH,  $I_{AS}$  = 70A,  $V_{DD}$  = 25V,  $R_{G}$  = 25  $\Omega$ 

### **■ ELECTRICAL CHARACTERISTICS**

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS		OTMBOL	, including the second	10		11111 0 1	0.4
Drain-Source Breakdown Voltage		BV <sub>DSS</sub>	I <sub>D</sub> =250μA	60			V
Drain-Source Leakage Current		I <sub>DSS</sub>	V <sub>DS</sub> =60V			10	μA
Gate-Source Leakage Current	Forward	I <sub>GSS</sub>	V <sub>GS</sub> =+20V			+100	nA
	Reverse		V <sub>GS</sub> =-20V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	I <sub>D</sub> =250μA	2		4	V
Static Drain-Source On-State Resistance		R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =70A			20	mΩ
DYNAMIC PARAMETERS							
Input Capacitance		C <sub>ISS</sub>			7400		pF
Output Capacitance Reverse Transfer Capacitance		Coss	$V_{GS}$ =0V, $V_{DS}$ =25V, f=1.0MHz		3200		pF
		$C_{RSS}$			540		pF
SWITCHING PARAMETERS							
Total Gate Charge		$Q_G$	V <sub>DD</sub> =50V, V <sub>GS</sub> =10V,			190	nC
Gate to Source Charge		$Q_GS$	I <sub>D</sub> =1.3A, I <sub>D</sub> =100μA,			55	nC
Gate to Drain Charge		$Q_GD$				90	nC
Turn-ON Delay Time		$t_{D(ON)}$			21		ns
Rise Time		$t_R$	V <sub>DD</sub> =30V, I <sub>D</sub> =70A,		190		ns
Turn-OFF Delay Time		t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω, V <sub>GS</sub> =0~10V		110		ns
Fall-Time		$t_{F}$			190		ns
SOURCE- DRAIN DIODE RATIN	IGS AND	CHARACTE	RISTICS				
Maximum Body-Diode Continuous Current		Is				70	Α
Maximum Body-Diode Pulsed Current		I <sub>SM</sub>				280	Α
Drain-Source Diode Forward Voltage		$V_{SD}$	I <sub>S</sub> =70A			1.28	V

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