

## UTT50P06

Power MOSFET

-50A, -60V P-CHANNEL (D-S)  
POWER MOSFET

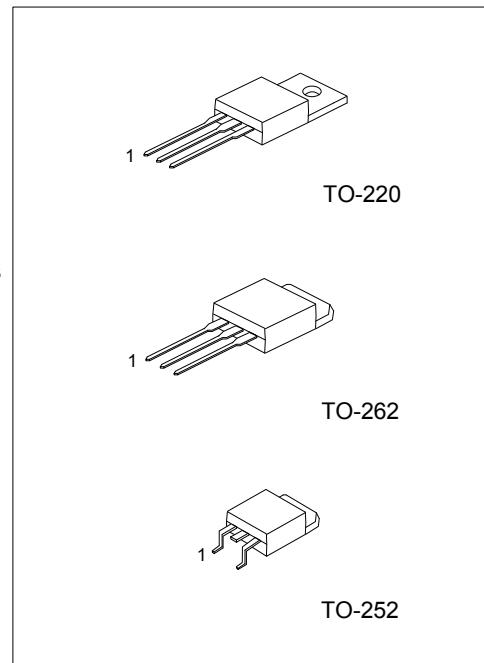
## ■ DESCRIPTION

The UTC **UTT50P06** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance, and it can also withstand high energy in the avalanche.

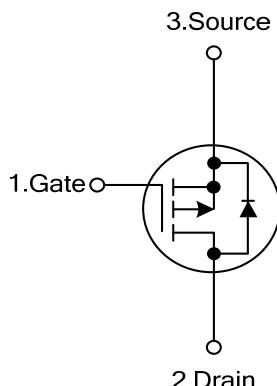
This UTC **UTT50P06** is suitable for load switch, etc.

## ■ FEATURES

- \*  $V_{DS} = -60V$
- \*  $I_D = -50A$
- \*  $R_{DS(ON)}=0.012\Omega$  @  $V_{GS}=-10V$ ,  $I_D=-17A$
- \* High Switching Speed



## ■ SYMBOL



## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTT50P06L-TA3-T	UTT50P06G-TA3-T	TO-220	G	D	S	Tube
UTT50P06L-TN3-T	UTT50P06G-TN3-T	TO-252	G	D	S	Tube
UTT50P06L-TN3-R	UTT50P06G-TN3-R	TO-252	G	D	S	Tape Reel
UTT50P06L-T2Q-R	UTT50P06G-T2Q-R	TO-262	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UTT50P06L-TA3-T 	(1)Packing Type	(1) T: Tube, R: Tape Reel  (2) TA3: TO-220, TN3: TO-252, T2Q: TO-262  (3) L: Lead Free, G: Halogen Free
	(2)Package Type	
	(3)Lead Free	

■ ABSOLUTE MAXIMUM RATINGS ( $T_A=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	-60	V
Gate-Source Voltage		$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous ( $T_J=175^\circ\text{C}$ )	$T_c=25^\circ\text{C}$	$I_D$	-50 (Note 5)
		$T_c=125^\circ\text{C}$		-27.5
	Pulsed		$I_{DM}$	-80
Avalanche Current		$I_{AR}$	-50	A
Single Pulse Avalanche Energy (Note 2)	$L=0.1\text{mH}$	$E_{AS}$	125	$\text{mJ}$
Power Dissipation	$T_c=25^\circ\text{C}$	TO-220	$P_D$	113 (Note 4)
		TO-252		50
		TO-262		113
		TO-220		2.5 (Note 3, 4)
	$T_A=25^\circ\text{C}$	TO-252		1.13
		TO-262		2.5
Junction Temperature		$T_J$	-55~+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55~+150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Duty cycle≤1%.
3. When Mounted on 1" square PCB (FR-4 material).
4. See SOA curve for voltage derating.
5. Package limited.

## ■ THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	$\theta_{JA}$	50	$^\circ\text{C/W}$
	TO-252		110	$^\circ\text{C/W}$
	TO-262		50	$^\circ\text{C/W}$
Junction to Case	TO-220	$\theta_{JC}$	1.1	$^\circ\text{C/W}$
	TO-252		2.5	$^\circ\text{C/W}$
	TO-262		1.1	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

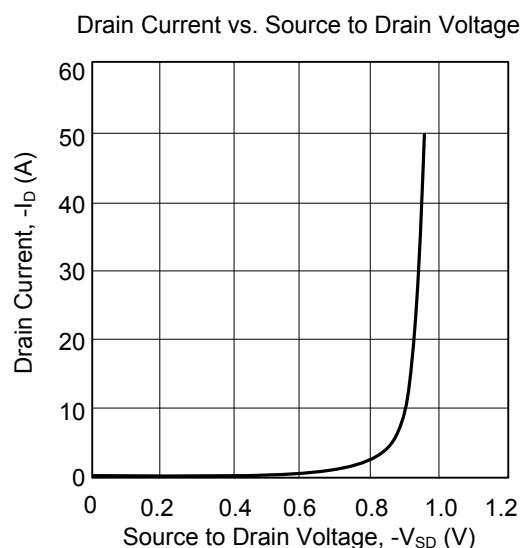
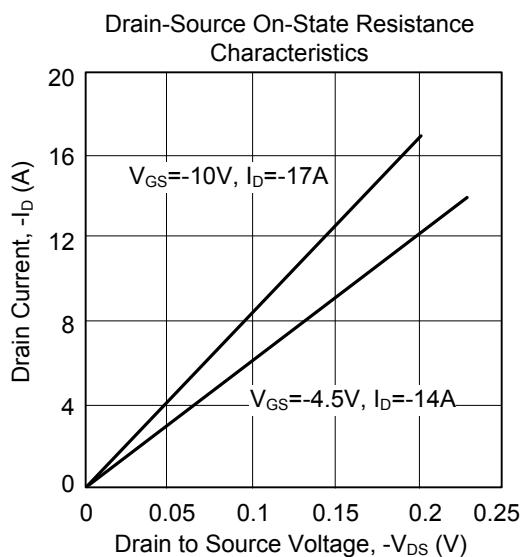
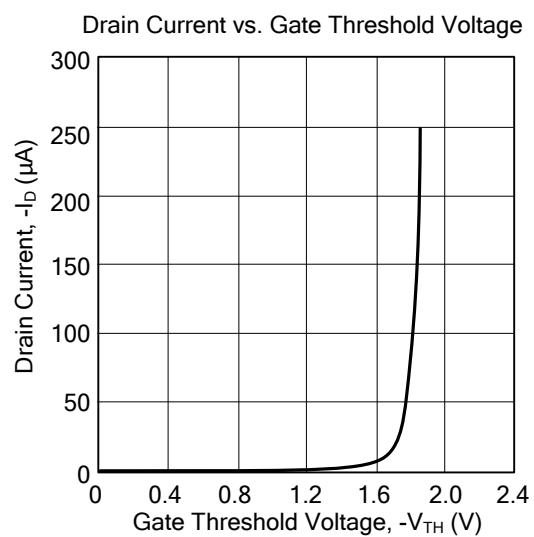
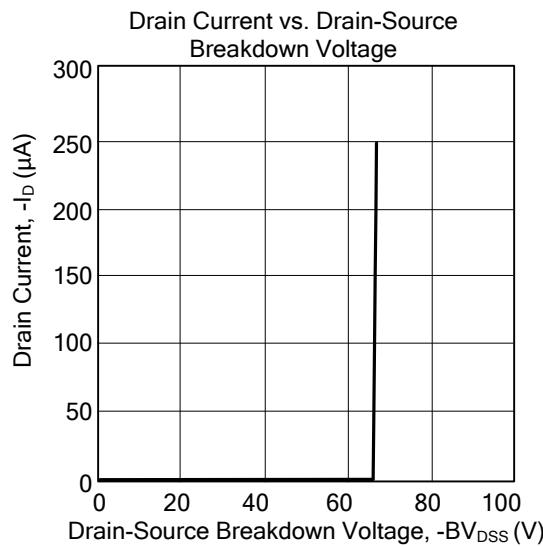
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=-250\mu\text{A}$	-60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{TH})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=-250\mu\text{A}$	-1		-3	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}$			-1	$\mu\text{A}$
		$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$			-50	
		$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}, T_J=150^\circ\text{C}$			-100	
Gate- Source Leakage Current	Forward	$V_{\text{GS}}=+20\text{V}, V_{\text{DS}}=0\text{V}$			+100	nA
	Reverse	$V_{\text{GS}}=-20\text{V}, V_{\text{DS}}=0\text{V}$			-100	nA
<b>ON CHARACTERISTICS</b>						
Static Drain-Source On-State Resistance (Note 1)	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_D=-17\text{A}$		0.012	0.015	$\Omega$
		$V_{\text{GS}}=-10\text{V}, I_D=-50\text{A}, T_J=125^\circ\text{C}$			0.025	
		$V_{\text{GS}}=-10\text{V}, I_D=-50\text{A}, T_J=150^\circ\text{C}$			0.028	
		$V_{\text{GS}}=-4.5\text{V}, I_D=-14\text{A}$			0.020	
Forward Transconductance (Note 1)	$g_{\text{FS}}$	$V_{\text{DS}}=-15\text{V}, I_D=-17\text{A}$		61		S
On State Drain Current (Note 1)	$I_{\text{D}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-5\text{V}$	-50			A
<b>DYNAMIC PARAMETERS</b> (Note 2)						
Input Capacitance	$C_{\text{ISS}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-25\text{V}, f=1\text{MHz}$		4950		pF
Output Capacitance	$C_{\text{OSS}}$			480		pF
Reverse Transfer Capacitance	$C_{\text{RSS}}$			405		pF
<b>SWITCHING PARAMETERS</b> (Note 2, 3)						
Total Gate Charge	$Q_G$	$V_{\text{GS}}=-10\text{V}, V_{\text{DS}}=-30\text{V}, I_D=-50\text{A}$		110	165	nC
Gate to Source Charge	$Q_{\text{GS}}$			19		nC
Gate to Drain Charge	$Q_{\text{GD}}$			28		nC
Turn-ON Delay Time	$t_{\text{D}(\text{ON})}$	$V_{\text{DD}}=-30\text{V}, R_L=0.6\Omega, I_D \approx -50\text{A}, V_{\text{GEN}}=-10\text{V}, R_G=6\Omega$		15	23	ns
Rise Time	$t_R$			70	105	ns
Turn-OFF Delay Time	$t_{\text{D}(\text{OFF})}$			175	260	ns
Fall-Time	$t_F$			175	260	ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b> ( $T_C=25^\circ\text{C}$ ) (Note 2)						
Maximum Body-Diode Continuous Current	$I_S$				-50	A
Maximum Body-Diode Pulsed Current	$I_{\text{SM}}$				-80	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{\text{SD}}$	$I_F=-50\text{A}, V_{\text{GS}}=0\text{V}$		-1.0	-1.6	V
Body Diode Reverse Recovery Time	$t_{\text{RR}}$	$I_F=-50\text{A}, dI/dt=100\text{A}/\mu\text{s}$		45	70	ns

Notes: 1. Pulse test; pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

2. Guaranteed by design, not subject to production testing.

3. Independent of operating temperature.

■ TYPICAL CHARACTERISTICS



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