



## UTF1404

Preliminary

Power MOSFET

### 160A, 40V N-CHANNEL POWER MOSFET

#### DESCRIPTION

The UTC **UTF1404** is an N-channel enhancement MOSFET, it uses UTC's advanced technology to provide the customers with perfect  $R_{DS(ON)}$  and high switching speed.

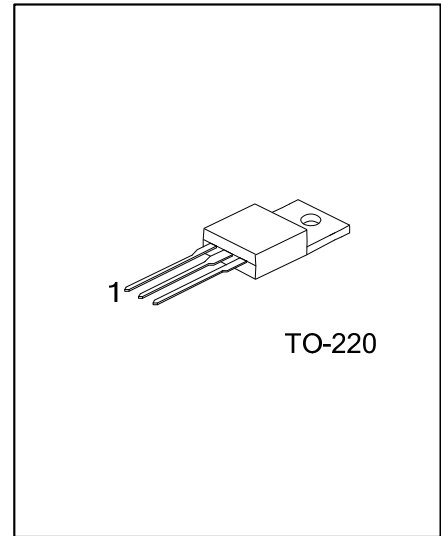
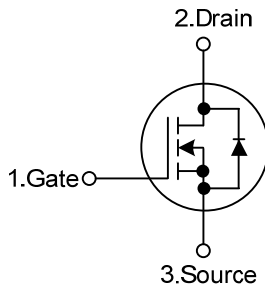
The UTC **UTF1404** is suitable for all commercial-industrial applications at power dissipation levels to approximately 50 watts, etc.

#### FEATURES

\*  $R_{DS(ON)}=3.5m\Omega @ V_{GS}=10V, I_D=95A$

\* High Switching Speed

#### SYMBOL



#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UTF1404L-TA3-T	UTF1404G-TA3-T	TO-220	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

UTF1404L-TA3-T	(1)Packing Type	(1) T: Tube
	(2)Package Type	(2) TA3: TO-220
	(3)Lead Free	(3) G: Halogen Free, L: Lead Free

### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
Drain-Source Voltage			$V_{DSS}$	40	V
Gate-Source Voltage			$V_{GSS}$	$\pm 20$	V
Drain Current	Continuous ( $V_{GS}=10V$ )	$T_C=25^\circ C$	$I_D$	162 (Note 4)	A
		$T_C=100^\circ C$		115 (Note 4)	A
	Pulsed (Note 2)		$T_C=25^\circ C$	$I_{DM}$	650
Avalanche Current (Note 2)			$I_{AR}$	95	A
Avalanche Energy	Single Pulsed (Note 3)		$E_{AS}$	519	mJ
	Repetitive (Note 2)		$E_{AR}$	20	mJ
Power Dissipation ( $T_C=25^\circ C$ )			$P_D$	166	W
Junction Temperature			$T_J$	+150	$^\circ C$
Storage Temperature			$T_{STG}$	-55~+150	$^\circ C$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive rating: pulse width limited by maximum junction temperature

3. Starting  $T_J=25^\circ C$ ,  $L=0.12mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=95A$

4. Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A

### ■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	62	$^\circ C/W$
Junction to Case	$\theta_{JC}$	0.75	$^\circ C/W$

■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>=25°C, unless otherwise specified)

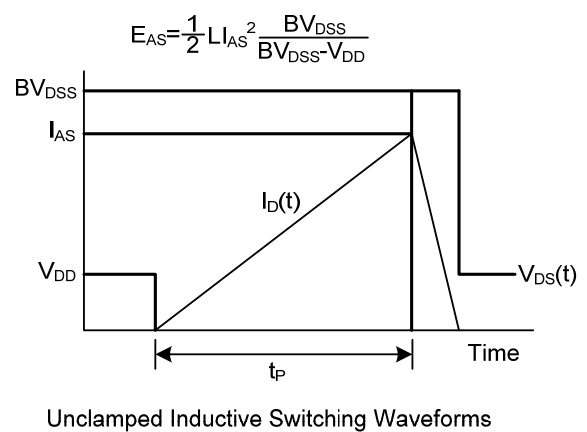
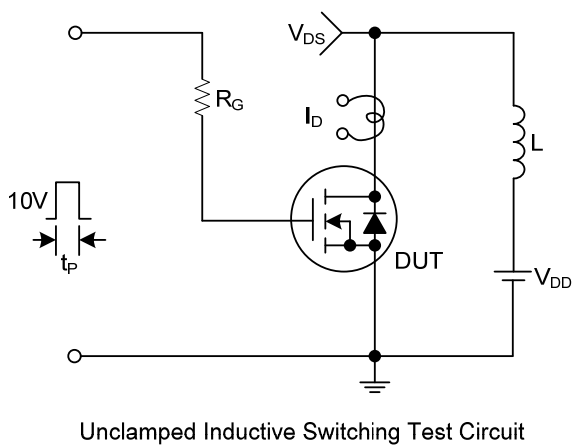
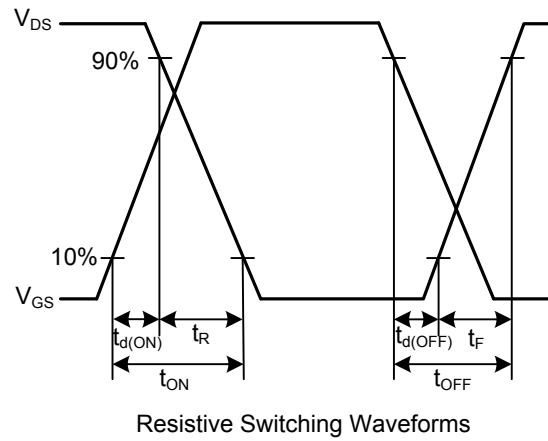
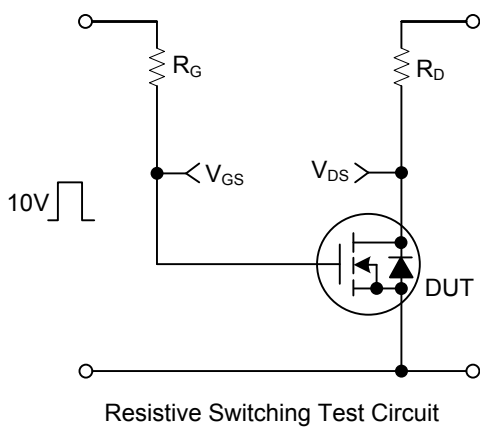
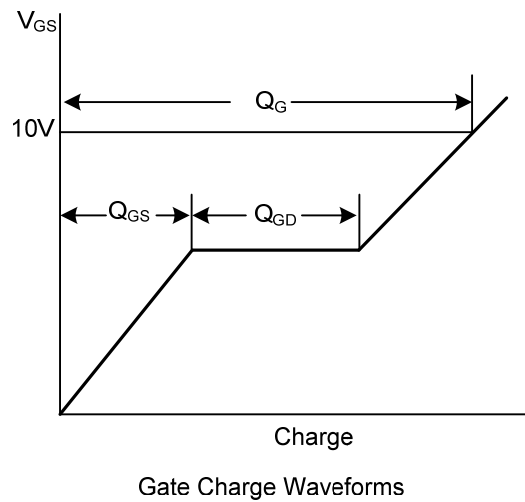
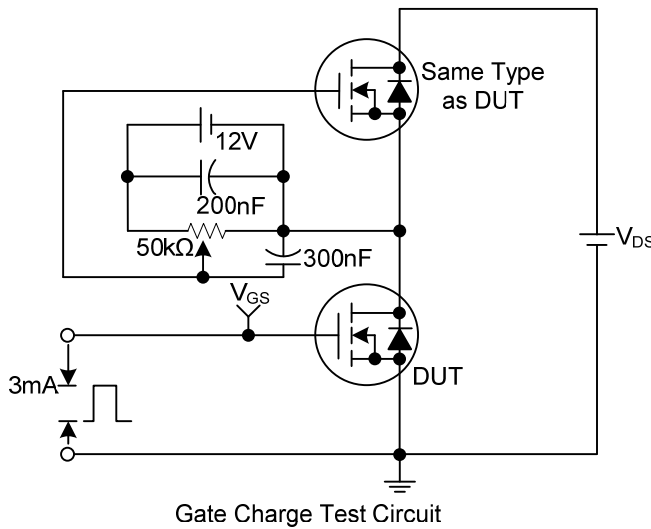
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			20	μA
		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =150°C			250	μA
Gate- Source Leakage Current	Forward	I <sub>GSS</sub>				
	Reverse					
		V <sub>GS</sub> =+20V			+200	nA
		V <sub>GS</sub> =-20V			-200	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =95A (Note 2)		3.5	4	mΩ
<b>DYNAMIC PARAMETERS</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1.0MHz		7360		pF
Output Capacitance	C <sub>OSS</sub>			1680		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>			240		pF
<b>SWITCHING PARAMETERS</b>						
Total Gate Charge	Q <sub>G</sub>	I <sub>D</sub> =95A, V <sub>DS</sub> =32V, V <sub>GS</sub> =10V (Note 2)		160	200	nC
Gate to Source Charge	Q <sub>GS</sub>			35		nC
Gate to Drain Charge	Q <sub>GD</sub>			42	60	nC
Turn-ON Delay Time	t <sub>D(ON)</sub>	V <sub>DD</sub> =20V, I <sub>D</sub> =95A, R <sub>G</sub> =2.5Ω, R <sub>D</sub> =0.21Ω (Note 2)		17		ns
Rise Time	t <sub>R</sub>			140		ns
Turn-OFF Delay Time	t <sub>D(OFF)</sub>			72		ns
Fall-Time	t <sub>F</sub>			26		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	I <sub>S</sub>	(Note 3)			162	A
Maximum Body-Diode Pulsed Current	I <sub>SM</sub>	(Note 1)			650	A
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =95A, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C (Note 2)			1.3	V

Notes: 1. Repetitive rating: pulse width limited by maximum junction temperature

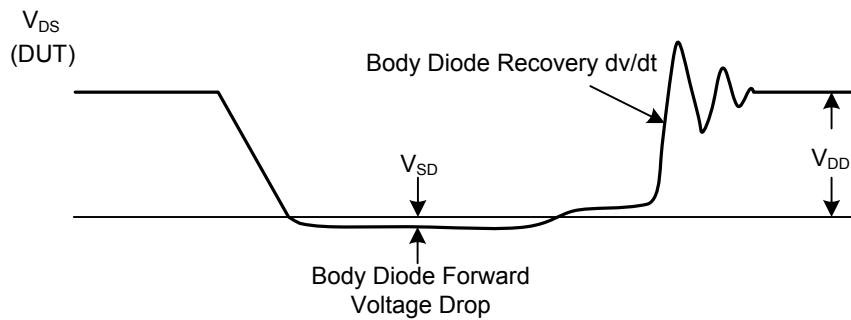
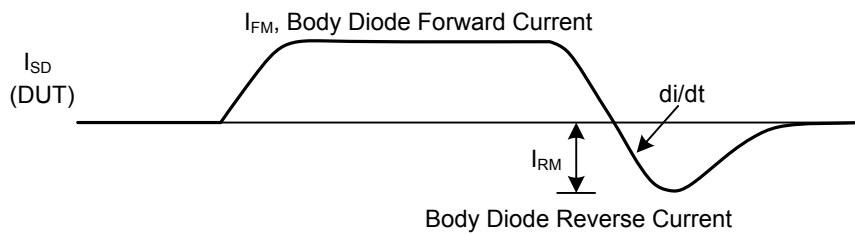
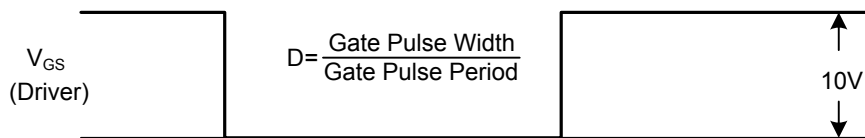
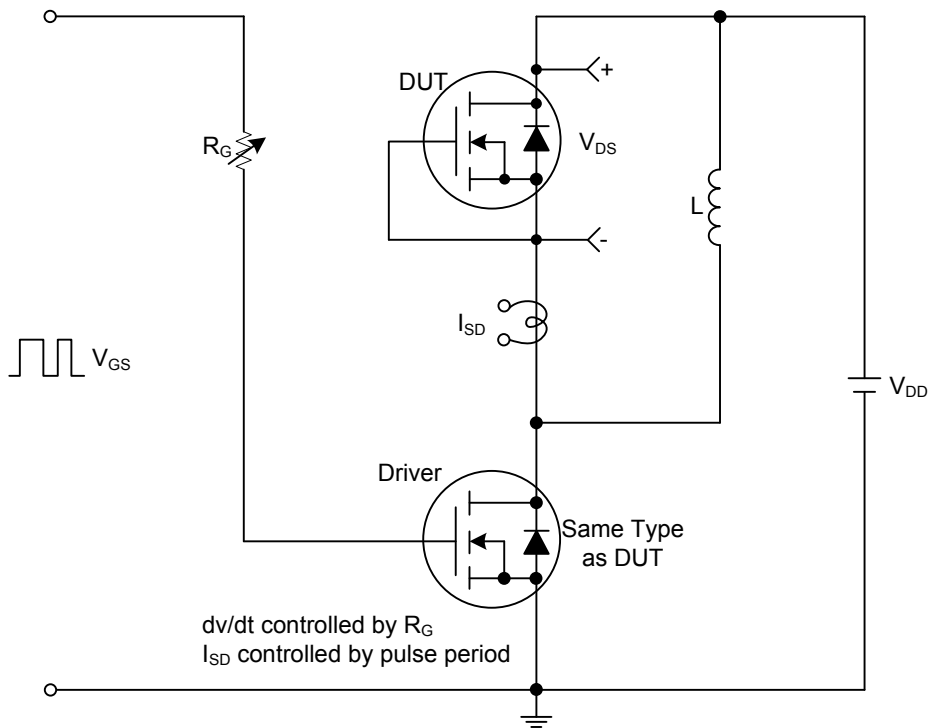
2. Pulse width≤300μs, Duty cycle≤2%

3. Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 75A

■ TEST CIRCUITS AND WAVEFORMS



■ TEST CIRCUITS AND WAVEFORMS(Cont.)



Peak Diode Recovery  $dv/dt$  Test Circuit and Waveforms

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