UPC8026 Preliminary Power MOSFET

30V, 13A N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **UPC8026** is an N-channel enhancement mode power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance, low leakage current and high forward transfer admittance.

SOP-8

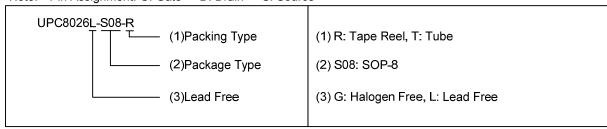
■ FEATURES

- * $V_{DS} = 30V$, $I_{D} = 13A$
- * $R_{DS(ON)}$ =0.0051 Ω @ V_{GS} =10V,
- $R_{DS(ON)}$ =0.0075 Ω @ V_{GS} =4.5V
- * High forward transfer admittance: |Y_{fs}|=30S
- * Low leakage current: IDSS<10µA @ VDS=30 V

■ ORDERING INFORMATION

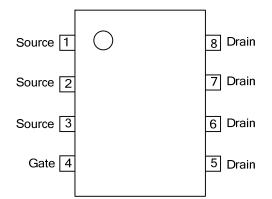
Ordering Number		Dookogo	Dooking	
Lead Free	Halogen Free	Package	Packing	
UPC8026L-S08-R	UPC8026G-S08-R	SOP-8	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Source



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■ PIN CONFIGURATION



■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage	e	V_{DSS}	30	
Gate-Source Voltage	rce Voltage V _{GSS} ±20		±20	V
Drain-Gate Voltage (R _{GS} =20kΩ)		V_{DGR}	30	V
Drain Current	Continuous (Note 2)	I _D	13	Α
	Pulsed (Note 2)	I _{DM}	52	Α
Avalanche Current		I _{AR}	13	Α
Avalanche Energy	Single Pulsed (Note 4)	E _{AS}	44	mJ
	Repetitive (Note 3, 5)	E _{AR}	0.048	mJ
Power Dissipation (N	lota 3)	P_D	1.9	W
Channel Temperatur	re	T _{CH}	150 °	
Storage Temperature	e	T _{STG}	-55~+150 °	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Ensure that the channel temperature does not exceed 150°C.
- 3. Device mounted on a glass-epoxy board FR-4,25.4×25.4×0.8(unit: mm)
- 4. V_{DD} =24V, T_{CH} =25°C (initial), L=0.2mH, I_{AR} = 13A
- 5. Repetitive rating: pulse width limited by max channel temperature

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient (Nota 3)	θ_{JA}	65.8	°C/W

■ ELECTRICAL CHARACTERISTICS (T_A=25°C)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		V _{(BR)DSS}	I _D =10mA, V _{GS} =0V 30 I _D =10mA, V _{GS} =-20 V 10				V
		$V_{(BR)DSX}$					
Drain-Source Leakage Current		I _{DSS}	V _{DS} =30V, V _{GS} =0 V			10	μΑ
Cata Cauraa Laakaga Current	Forward	GSS	V _{GS} =+20V, V _{DS} =0V			+100	nA
Gate- Source Leakage Current	Reverse		V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	V _{DS} =10 V, I _D =1mA	1.3		2.5	V
Static Drain Source On State De	oiotonoo		V _{GS} =4.5V, I _D =6.5A		7.5	10	mΩ
Static Drain-Source On-State Re	sistance	R _{DS(ON)}	V_{GS} =10V, I_D =6.5A		5.1	6.6	
Forward Transfer Admittance		Y _{FS}	V_{DS} =10V, I_{D} =6.5A	15	30		S
DYNAMIC PARAMETERS		_			-		
Input Capacitance		C _{ISS}			1800		pF
Output Capacitance Reverse Transfer Capacitance		Coss	V _{DS} =10V, V _{GS} =0V, f=1MHz		570		pF
		C _{RSS}	1		370		pF
SWITCHING PARAMETERS							
Total Gate Charge		Q_{G}			42		nC
Gate to Source Charge		Q_{GS}	V _{DD} ≈24V, V _{GS} =10V, I _D =13 A		6.5		nC
Gate to Drain Charge		Q_{GD}			14		nC
Turn-ON Delay Time		t _{D(ON)}	V _{GS} 10V I _D =6.5A OV _{OUT}		28		ns
Rise Time		t _R			15		ns
Turn-OFF Delay Time		t _{D(OFF)}	$\begin{array}{c c} & & & \\ \hline & &$		54		ns
		=(3.1,	$\left \begin{array}{c c} 4.7\Omega & \end{array} \right \left \begin{array}{c} 2.3\Omega \end{array} \right $				
Fall-Time		t _F	± ± δ V _{DD} ≈15V		21		ns
			Duty≤1%, t _W =10µs				
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Drain Reverse Current Pulse	(Note 1)	I _{DRP}				52	Α
Forward Voltage (Diode)		V_{DSF}	I _{DR} =13A, V _{GS} =0V			-1.2	V

Note: 1. Ensure that the channel temperature does not exceed 150°C.

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