

UNISONIC TECHNOLOGIES CO., LTD

3N65

3A, 650V N-CHANNEL POWER MOSFET

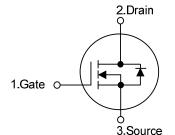
DESCRIPTION

The UTC **3N65** is a high voltage and high current power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

FEATURES

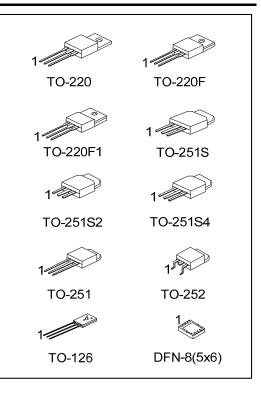
- * $R_{DS(ON)}$ < 3.8 Ω @ V_{GS} = 10V, I_D = 1.5A
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

SYMBOL



ORDERING INFORMATION

Ordering Number		Dookago		Pin Assignment							Decking	
Lead Free	Halogen Free	Packag	e	1	2	3	4	5	6	7	8	Packing
3N65L-TA3-T	3N65G-TA3-T	G-TA3-T TO-220		G	D	S	-	-	-	-	1	Tube
3N65L-TF1-T	3N65G-TF1-T	TO-220F	-1	G	D	S	-	-	-	-	1	Tube
3N65L-TF3-T	3N65G-TF3-T	TO-220	F	G	D	S	-	-	-	-	1	Tube
3N65L-TM3-T	3N65G-TM3-T	TO-25 ²	1	G	D	S	-	-	-	-	1	Tube
3N65L-TMS-T	3N65G-TMS-T	TO-251	S	G	D	S	-	-	-	-	1	Tube
3N65L-TMS2-T	3N65G-TMS2-T	TO-2518	52	G	D	S	-	-	-	-	1	Tube
3N65L-TMS4-T	3N65G-TMS4-T	TO-2518	54	G	D	S	-	-	-	-	1	Tube
3N65L-TN3-R	3N65G-TN3-R TO-252		2	G	D	S	-	-	-	-	1	Tape Reel
3N65L-T60-K	3N65G-T60-K TO-126		6	G	D	S	-	-	-	-	1	Bulk
-	- 3N65G-K08-5060-R DFN-8(5)		×6)	S	S	s	G	D	D	D	D	Tape Reel
Note: Pin Assignment	: G: Gate D: Drain	S: Source	•									
3N65L-TA3-T				(1) R: Tape Reel, T: Tube, K: Bulk								
(1)Packing Type			(2) TA3: TO-220, TF1: TO-220F1, TF3: TO-220F,									
(2)Package Type				TM3: TO-251, TN3: TO-252, TMS: TO-251S								
(3)Green Package				T60: TO-126, K08-5060: DFN-8(5×6)								
(3) L: Lead Free, G: Halogen Free and Lead Free						ree						



Power MOSFET

MARKING

Package		MARKING
TO-220 TO-220F TO-220F1 TO-251	TO-251S TO-251S2 TO-251S4 TO-252	UTC 3 N 6 5 □ L: Lead Free G: Halogen Free Data Code 1
TO-126		UTC Data Code 3 N 6 5 L: Lead Free G: Halogen Free
DFN-8(5×6)		UTC 3N65 Lot Code \leftarrow Date Code



ABSOLUTE MAXIMUM RATINGS (T_c = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT		
Drain-Source Voltage		V _{DSS}	650	V		
Gate-Source Voltage		V _{GSS}	±30	V		
Avalanche Current (Not	Inche Current (Note 2)		3.0	А		
Continuous Drain Current		I _D	3.0	А		
Pulsed Drain Current (N	Note 2)			А		
Avalanaha Enarav	Single Pulsed (Note 3)	E _{AS}	200	mJ		
Avalanche Energy	Repetitive (Note 2)	E _{AR}	7.5	mJ		
Peak Diode Recovery of	lv/dt (Note 4)	dv/dt 4.5 \		V/ns		
	TO-220		75			
	TO-220F/TO-220F1		34			
Power Dissipation	TO-251/TO-252/TO-251S TO-251S2/TO-251S4	P _D	50	V V A A A M J mJ		
	TO-126		17			
	DFN-8(5×6)		25			
Junction Temperature		TJ	+150	°C		
Operating Temperature		T _{OPR}	-55 ~ +150	°C		
Storage Temperature		T _{STG}	-55 ~ +150	°C		

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. L = 64mH, I_{AS} = 2.4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C

4. $I_{SD} \le 3.0A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1		62.5	
	TO-251/TO-252/TO-251S TO-251S2/TO-251S4	θ_{JA}	110	°C/W
	TO-126		132	
	DFN-8(5×6)		75 (Note)	
	TO-220		1.67	
Junction to Case	TO-220F/TO-220F1		3.68	
	TO-251/TO-252/TO-251S TO-251S2/TO-251S4	θ _{JC}	2.5	°C/W
	TO-126		7.36	
	DFN-8(5×6)		5 (Note)	

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.



PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							-
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250 μA	650			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 650 V, V _{GS} = 0 V			10	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
	Reverse		V_{GS} = -30 V, V_{DS} = 0 V			-100	nA
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS} / \triangle T_J$	I _D =250µA,Referenced to 25°C		0.6		V/°C
ON CHARACTERISTICS							-
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
Static Drain-Source On-State Res	istance	R _{DS(ON)}	V _{GS} = 10V, I _D = 1.5A		2.8	3.8	Ω
DYNAMIC CHARACTERISTICS		, <i>i</i>					
Input Capacitance		C _{ISS}			430	500	pF
Output Capacitance		C _{OSS}	$V_{\rm DS} = 25V, V_{\rm GS} = 0V,$		50	65	pF
Reverse Transfer Capacitance		C _{RSS}	f = 1MHz		11	20	pF
SWITCHING CHARACTERISTIC	S						
Turn-On Delay Time		t _{D(ON)}			32	45	ns
Turn-On Rise Time		t _R	$V_{DD} = 30V, I_D = 0.5A,$		64	80	ns
Turn-Off Delay Time		t _{D(OFF)}	R _G = 25Ω (Note 1, 2)		115	140	ns
Turn-Off Fall Time		t _F	-		60	75	ns
Total Gate Charge		Q _G			51	70	nC
Gate-Source Charge		Q _{GS}	V_{DS} = 50V, I _D =1.3A,		13		nC
Gate-Drain Charge		Q_{GD}	V _{GS} = 10 V (Note 1, 2)		11		nC
SOURCE- DRAIN DIODE RATIN	GS AND CH	HARACTERIS	TICS				
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} = 0 V, I _S = 3.0 A			1.4	V
Maximum Continuous Drain-Source Diode						2.0	
Forward Current		ls				3.0	A
Maximum Pulsed Drain-Source D	iode					12	А
Forward Current		I _{SM}				12	A

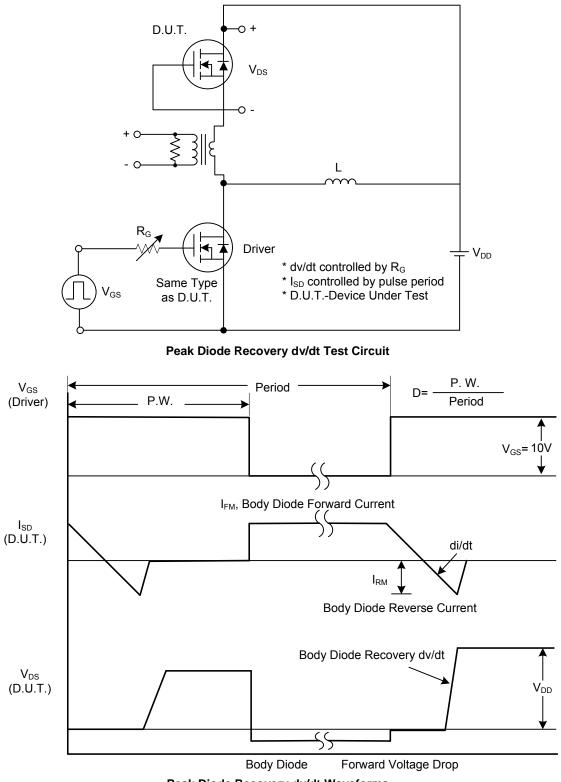
■ ELECTRICAL CHARACTERISTICS (T_C =25°C, unless otherwise specified)

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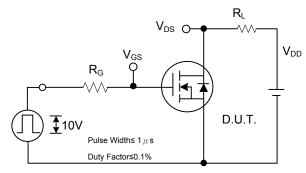
■ TEST CIRCUITS AND WAVEFORMS



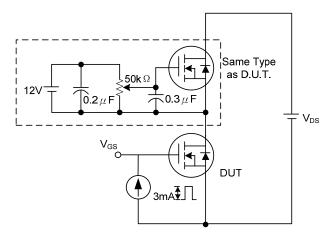
Peak Diode Recovery dv/dt Waveforms



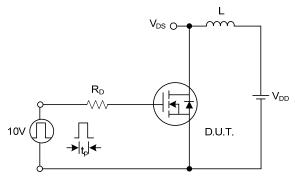
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



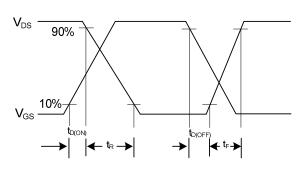
Switching Test Circuit



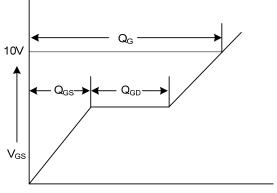
Gate Charge Test Circuit



Unclamped Inductive Switching Test Circuit

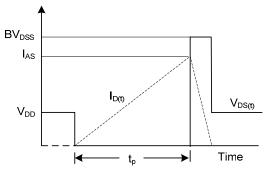


Switching Waveforms



Charge

Gate Charge Waveform

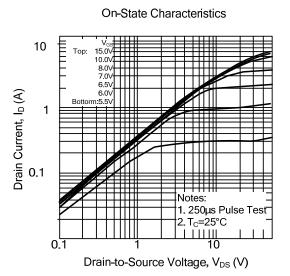


Unclamped Inductive Switching Waveforms

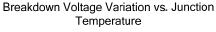


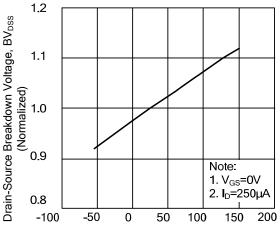
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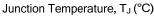
TYPICAL CHARACTERISTICS

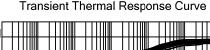


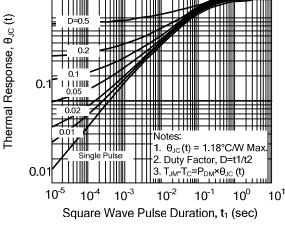




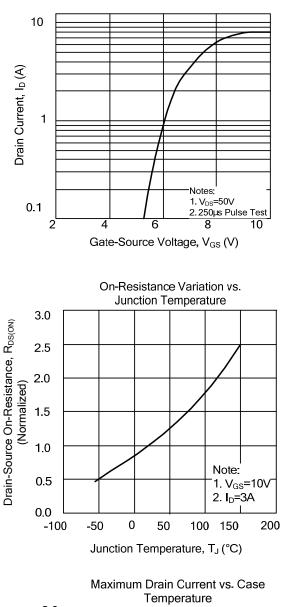


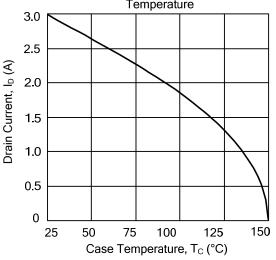








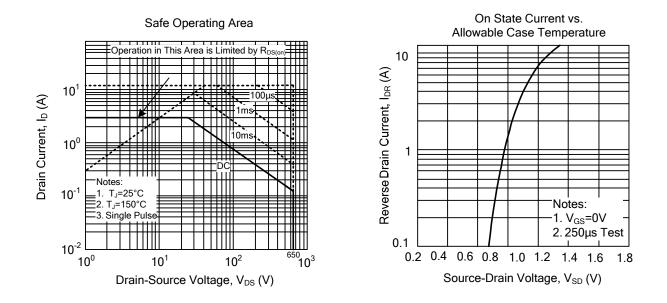




Transfer Characteristics

TYPICAL CHARACTERISTICS(Cont.)

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