

1N65A

Power MOSFET

0.5A, 650V N-CHANNEL POWER MOSFET

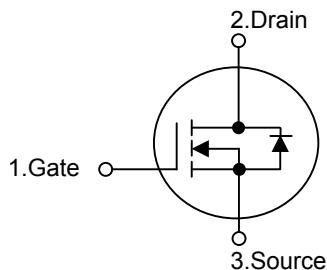
■ DESCRIPTION

The UTC **1N65A** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)} < 15.5\Omega$ @ $V_{GS} = 10V$, $I_D = 0.5A$
- * Ultra Low gate charge (typical 8.0nC)
- * Low reverse transfer capacitance ($C_{RSS} = 3.0 \text{ pF(max)}$)
- * Fast switching capability
- * Avalanche energy specified
- * Improved dv/dt capability, high ruggedness

■ SYMBOL



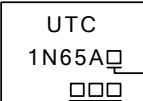
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
-	1N65AG-AA3-R	SOT-223	G	D	S	Tape Reel
1N65AL-T92-B	1N65AG-T92-B	TO-92	G	D	S	Tape Box
1N65AL-T92-K	1N65AG-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

1N65AG-AA3-R	(1)Packing Type (2)Package Type (3)Green Package	(1) B: Tape Box, K: Bulk, R: Tape Reel (2) AA3: SOT-223, T92: TO-92 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING

SOT-223	TO-92
 1	 1 L: Lead Free G: Halogen Free

■ ABSOLUTE MAXIMUM RATINGS ($T_c = 25^\circ\text{C}$, unless otherwise specified.)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current	I_D	0.5	A
Pulsed Drain Current (Note 2)	I_{DM}	2	A
Avalanche Energy Single Pulse(Note 3)	E_{AS}	50	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	4.5	V/ns
Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	1	W
Derate above 25°C		8	$\text{mW}/^\circ\text{C}$
Junction Temperature	T_J	+150	$^\circ\text{C}$
Operating Temperature	T_{OPR}	-55 ~ +150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L=92\text{mH}$, $I_{AS}=0.8\text{A}$, $V_{DD}=50\text{V}$, $R_G=0\Omega$, Starting $T_J=25^\circ\text{C}$

4. $I_{SD} \leq 1.0\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150
	TO-92		140

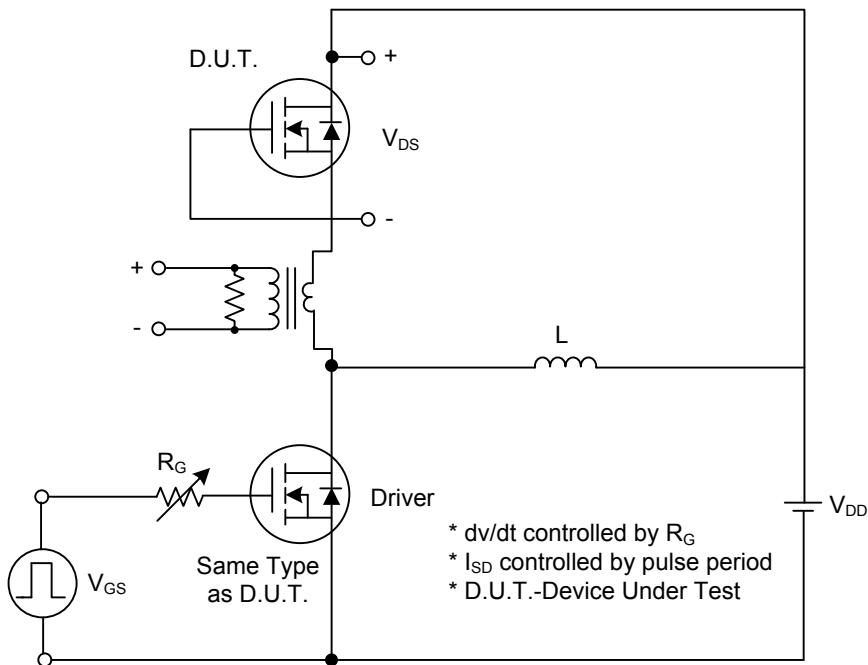
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	650			V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$V_{\text{GS}} = 20\text{V}, V_{\text{DS}} = 0\text{V}$		100		nA
	Reverse	$V_{\text{GS}} = -20\text{V}, V_{\text{DS}} = 0\text{V}$		-100		nA
Breakdown Voltage Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$I_{\text{D}} = 250\text{mA}$, referenced to 25°C	0.4			$\text{V}/^\circ\text{C}$
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{\text{GS(TH)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	2.0		4.5	V
Static Drain-Source On-State Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 0.5\text{A}$		11.5	15.5	Ω
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		100		pF
Output Capacitance	C_{OSS}			20		pF
Reverse Transfer Capacitance	C_{RSS}			3		pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{\text{D (ON)}}$	$V_{\text{DD}}=325\text{V}, I_{\text{D}}=0.5\text{A}, R_{\text{G}}=5\Omega$ (Note 1,2)		12	34	ns
Turn-On Rise Time	t_{R}			11	32	ns
Turn-Off Delay Time	$t_{\text{D (OFF)}}$			40	90	ns
Turn-Off Fall Time	t_{F}			18	46	ns
Total Gate Charge	Q_{G}	$V_{\text{DS}}=520\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=0.8\text{A}$ (Note 1,2)		8	10	nC
Gate-Source Charge	Q_{GS}			1.8		nC
Gate-Drain Charge	Q_{GD}			4.0		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{SD}} = 1.2\text{A}$			1.6	V
Maximum Continuous Drain-Source Diode Forward Current	I_{S}				1.2	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				4.8	A
Reverse Recovery Time	t_{rr}	$V_{\text{GS}}=0\text{V}, I_{\text{SD}} = 1.2\text{A}$ $di/dt = 100\text{A}/\mu\text{s}$		136		ns
Reverse Recovery Charge	Q_{RR}			0.3		μC

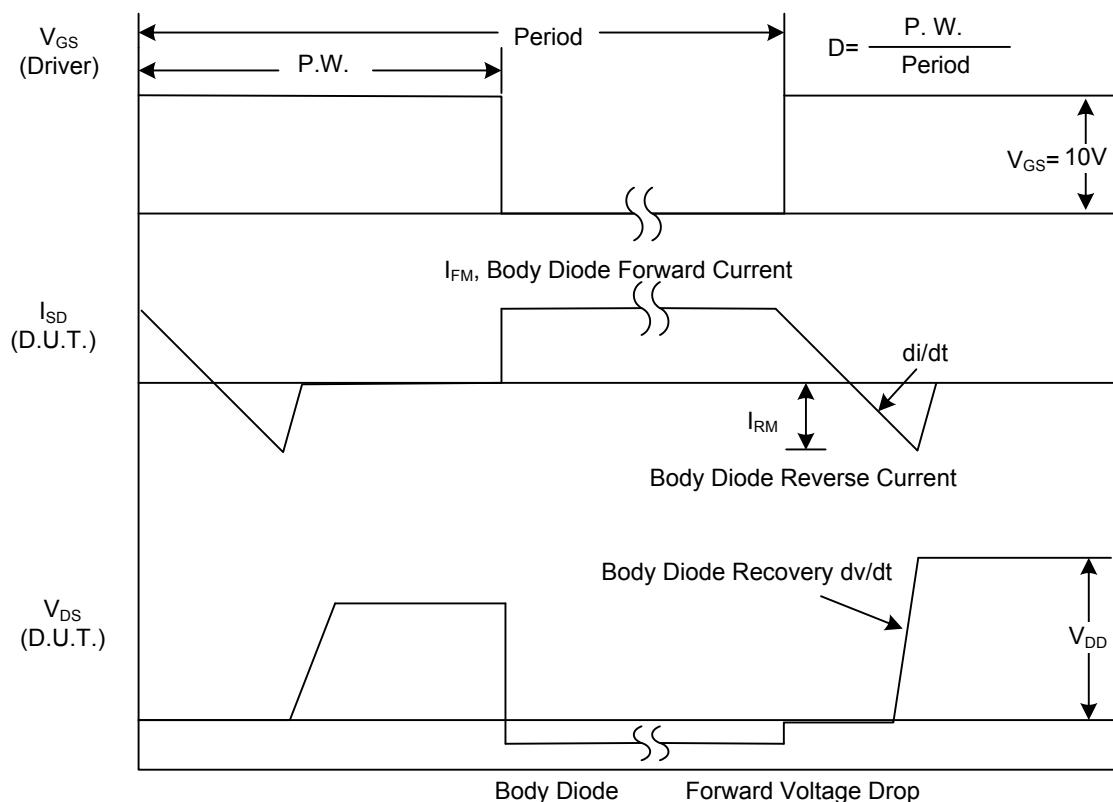
Note: 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

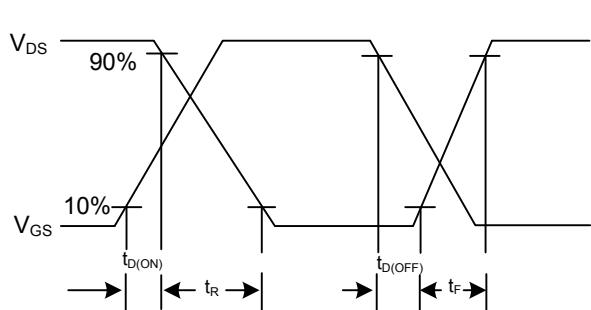
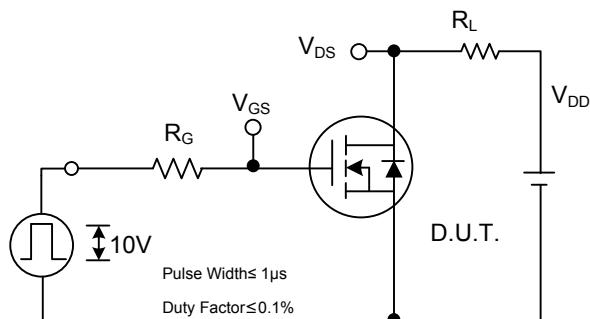


Peak Diode Recovery dv/dt Test Circuit



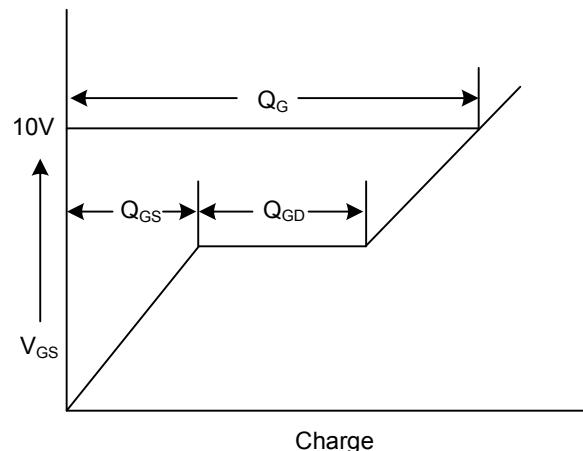
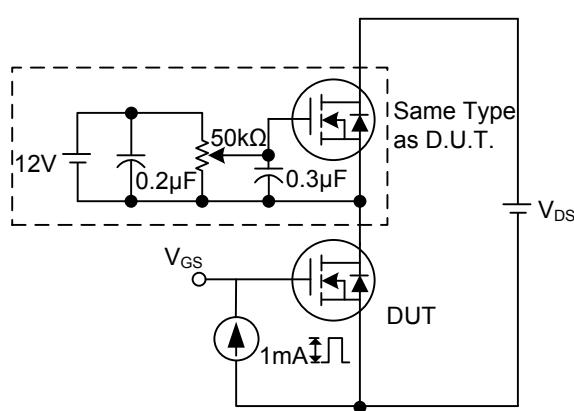
Peak Diode Recovery dv/dt Waveforms

■ TEST CIRCUITS AND WAVEFORMS (Cont.)



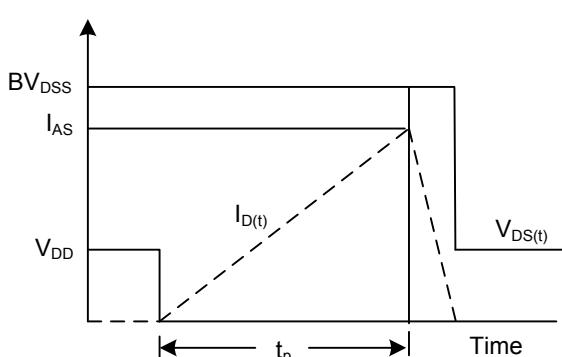
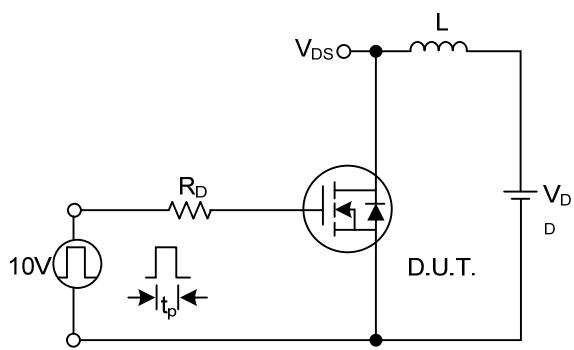
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

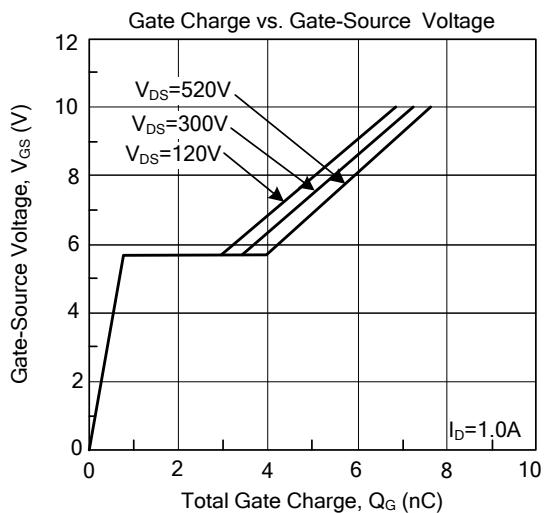
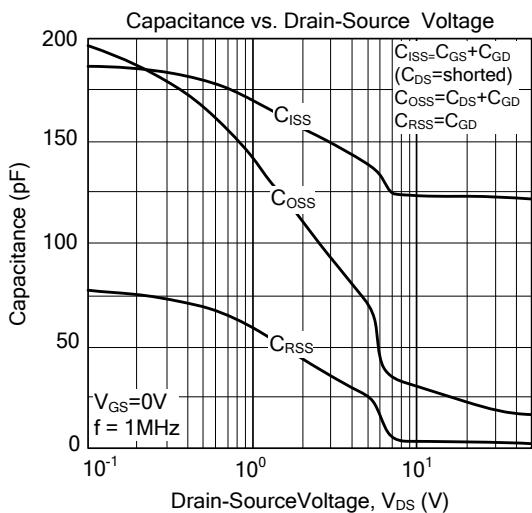
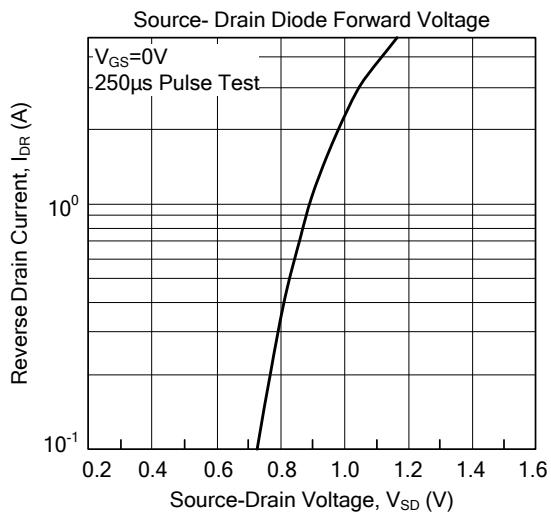
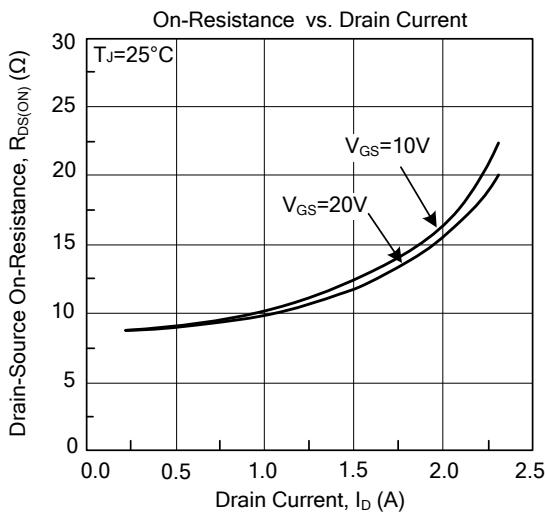
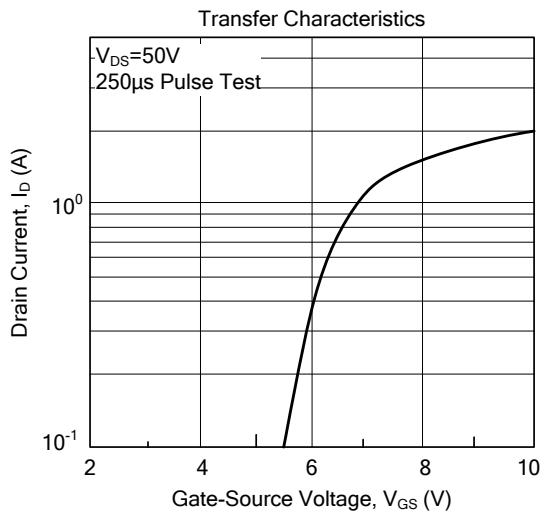
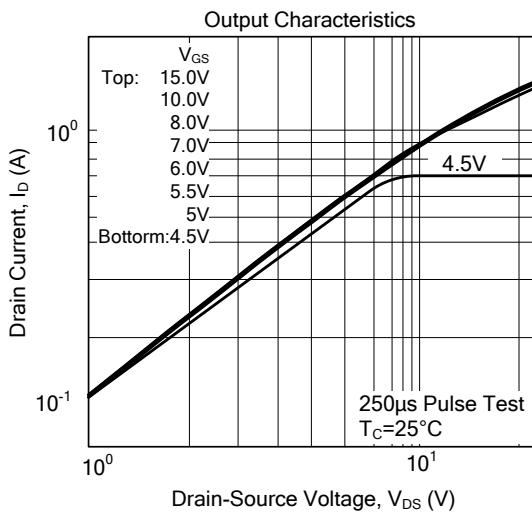
Gate Charge Waveform



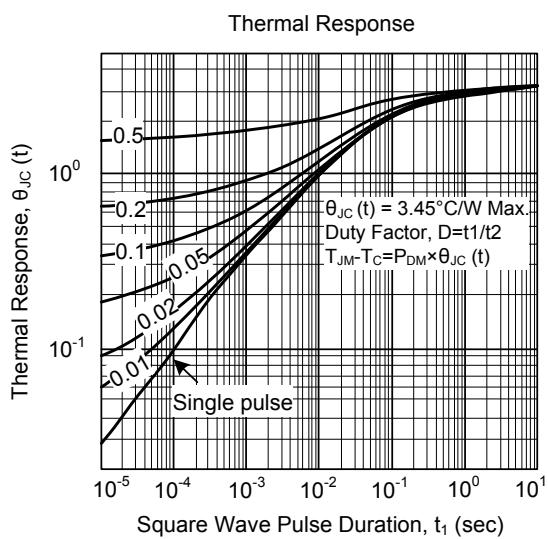
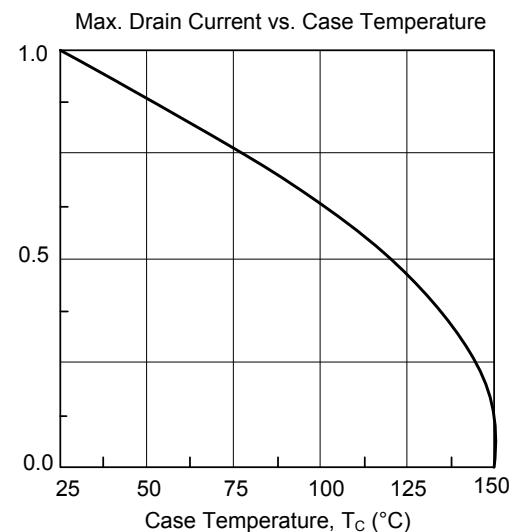
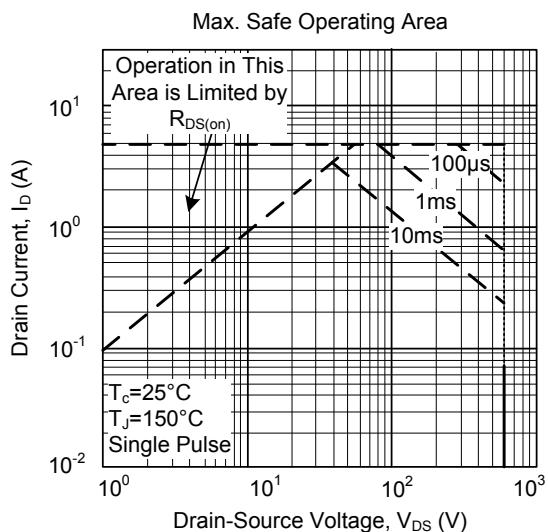
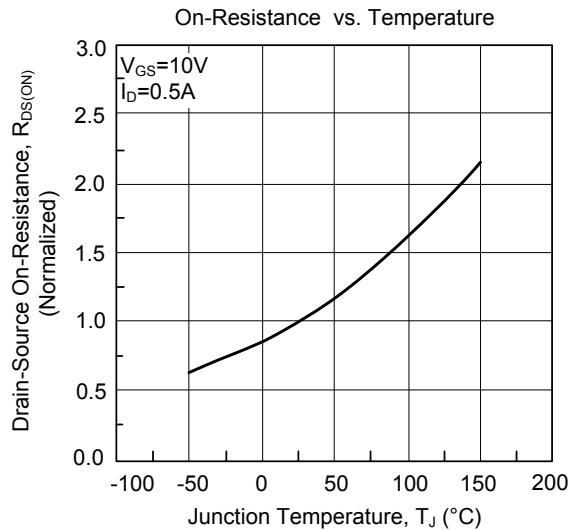
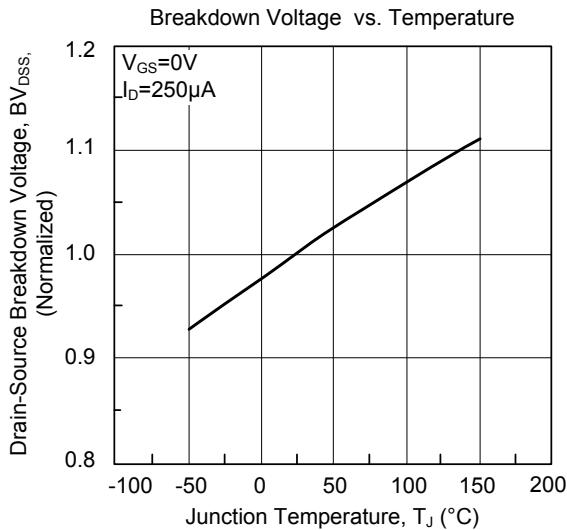
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



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