

UNISONIC TECHNOLOGIES CO., LTD

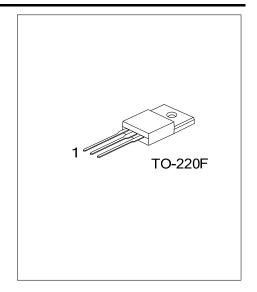
13N40 **Preliminary Power MOSFET**

13A, 400V N-CHANNEL **POWER MOSFET**

DESCRIPTION

The UTC 13N40 is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

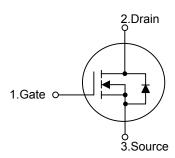
The UTC 13N40 is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.



FEATURES

- * $R_{DS(ON)}$ =0.35 Ω @ V_{GS} =10V
- * High switching speed
- * 100% avalanche tested

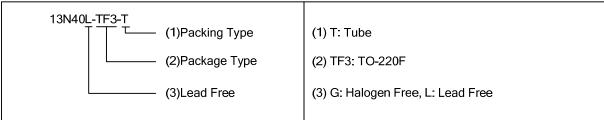
SYMBOL



ORDERING INFORMATION

Ordering Number		Doolsono	Pin	Daakina		
Lead Free	Halogen Free	Package	1	2	3	Packing
13N40L-TF3-T	13N40G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source



■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C, unless otherwise specified)

PARAMI	ETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	400	V	
Gate-Source Voltage		V _{GSS}	±30	V	
Danie Oceanne	Continuous (T _C =25°C)	I _D	13	Α	
Drain Current	Pulsed (Note 2)	I _{DM}	52	Α	
Avalanche Energy Single Pulsed (Note 3)		E _{AS}	705	mJ	
Power Dissipation		P _D	48	W	
Junction Temperature		TJ	+150	°C	
Storage Temperature Range		T _{STG}	-55~+150	°C	

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 2. Repetitive Rating: Pulse width limited by maximum junction temperature
 - 3. L = 8.34mH, I_{AS} = 13A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	60	°C/W	
Junction to Case	θ_{JC}	2.58	°C/W	

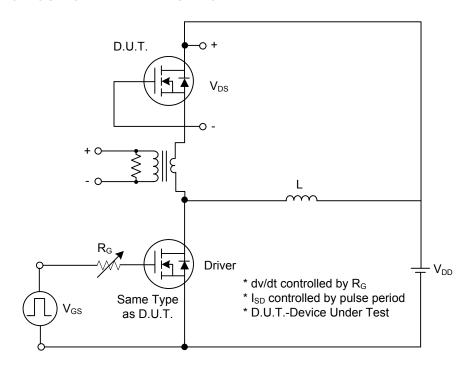
■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =250μA, V _{GS} =0V 40				V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μΑ
Cata Cauraa Laakaga Current	Forward		V_{GS} =+30V, V_{DS} =0V			+100	nA
Gate- Source Leakage Current	Reverse	I _{GSS}	V _{GS} =-30V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_{D}=250\mu A$ 2.0			4.0	V
Static Drain-Source On-State Re	sistance	R _{DS(ON)}	V _{GS} =10V, I _D =6.5A			0.35	Ω
DYNAMIC PARAMETERS							
Input Capacitance	out Capacitance				1283		pF
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		218		pF
Reverse Transfer Capacitance		C_{RSS}			120		pF
SWITCHING PARAMETERS							
Turn-ON Delay Time		t _{D(ON)}			16		ns
Rise Time		t _R	V_{DD} =200V, I_{D} =13A, R_{G} =25 Ω		20		ns
Turn-OFF Delay Time		t _{D(OFF)}	(Note 1, 2)		100		ns
Fall-Time		t _F			42		ns
Total Gate Charge		Q_G	 V _{DS} =320 V, I _D =13A,		79	100	nC
Gate-Source Charge		Q_GS	V _{DS} -320 V, I _D -13A, -V _{GS} =10 V (Note 1,2)		7.2	12	nC
Gate-Drain Charge		Q_GD	VGS=10 V (Note 1,2)		43	55	nC
SOURCE- DRAIN DIODE RATIN	NGS AND C	CHARACTERI	STICS				
Drain-Source Diode Forward Vol	tage	V_{SD}	I _S =13A, V _{GS} =0V			1.2	V
Maximum Body-Diode Continuou	us Current	I _S				13	Α
Maximum Body-Diode Pulsed Cu	urrent	I _{SM}				52	Α

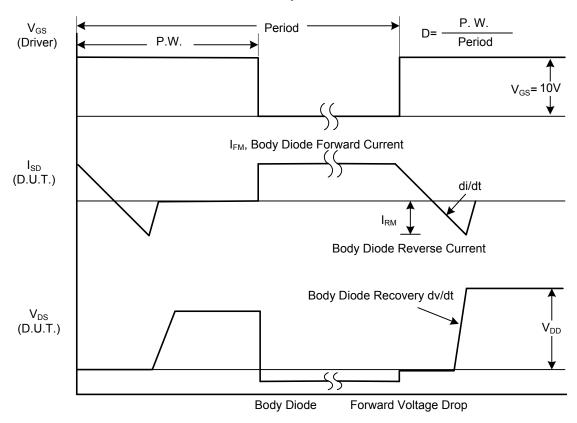
Notes: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

2. Essentially independent of operating temperature

■ TEST CIRCUITS AND WAVEFORMS

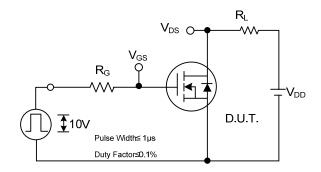


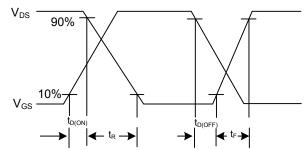
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

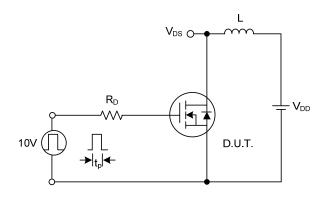
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

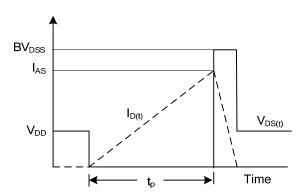




Switching Test Circuit

Switching Waveforms





Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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