

UNISONIC TECHNOLOGIES CO., LTD

9N40 **Preliminary Power MOSFET**

9A, 400V N-CHANNEL **POWER MOSFET**

DESCRIPTION

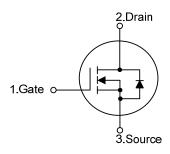
The UTC 9N40 is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology specializes in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC 9N40 is universally applied in electronic lamp ballast based on half bridge topology and high efficient switched mode power supply.

FEATURES

- * High switching speed
- * $R_{DS(ON)}$ < 0.75 Ω @ V_{GS} =10V, I_{D} =4.5A
- * 100% avalanche tested

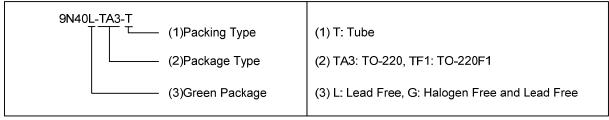
SYMBOL



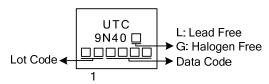
ORDERING INFORMATION

Ordering Number		Dookogo	Pin	Dooking			
Lead Free	Halogen Free	Package	1	2	3	Packing	
9N40L-TA3-T	9N40G-TA3-T	TO-220	G	D	S	Tube	
9N40L-TF1-T	9N40G-TF1-T	TO-220F1	G	D	S	Tube	

Note: Pin Assignment: G: Gate D: Drain S: Source



MARKING



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TO-220

TO-220F1

■ **ABSOLUTE MAXIMUM RATINGS** (T_C=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	400	V
Gate-Source Voltage		V _{GSS}	±30	V
Drain Current	Continuous (T _C =25°C)	I _D	9	Α
	Pulsed (Note 2)	I_{DM}	36	Α
Avalanche Current (Note 2)		I _{AR}	9	Α
Avalanche Energy	Single Pulsed (Note 3)	E _{AS}	427	mJ
	Repetitive (Note 2)	E _{AR}	4.0	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Power Dissipation	TO-220		113	W
	TO-220F1	Б	40	W
Derate above 25°C	TO-220	P_D	0.9	W/°C
	TO-220F1		0.32	W/°C
Junction Temperature		T_J	+150	°C
Storage Temperature		T _{STG}	-55~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 3. L = 10.5mH, I_{AS} = 9A, V_{DD} = 90V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}$ C.
- 4. $I_{SD} \le 9A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25$ °C.

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT	
Junction to Ambient		θ_{JA}	62.5	°C/W	
Junction to Case	TO-220	0	1.1	°C/W	
	TO-220F1	θ _{JC}	3.125	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C, unless otherwise noted)

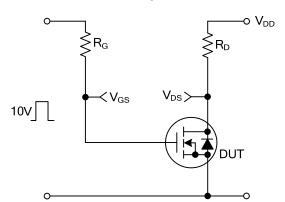
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	$I_D=250\mu A, V_{GS}=0V$	400			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} =400V, V _{GS} =0V			1	μΑ
Gate- Source Leakage Current	orward	I _{GSS}	V _{GS} =+30V, V _{DS} =0V			+100	nA
	Reverse		V_{GS} =-30V, V_{DS} =0V			-100	nA
ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$			4.0	V
Static Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =4.5A		0.6	0.75	Ω
DYNAMIC PARAMETERS	_						
Input Capacitance		C _{ISS}			1340	1700	pF
Output Capacitance		Coss	V_{GS} =0V, V_{DS} =25V, f=1.0MHz		490	520	pF
Reverse Transfer Capacitance		C_{RSS}	7		160	180	pF
SWITCHING PARAMETERS	_						
Total Gate Charge		Q_G	V _{GS} =10V, V _{DS} =320V, I _D =9A		34		nC
Gate to Source Charge		Q_GS	(Note 1, 2)		18		nC
Gate to Drain Charge		Q_GD	(Note 1, 2)		16		nC
Turn-ON Delay Time		$t_{D(ON)}$	V_{DD} =200V, I_{D} =9A, R_{G} =25 Ω		22		ns
Rise Time		t_R			60		ns
Turn-OFF Delay Time		$t_{D(OFF)}$	(Note 1, 2)		32		ns
Fall-Time		t_{F}			140		ns
SOURCE- DRAIN DIODE RATING	S AND CH	HARACTERIST	TICS	ā.	ā.	ā.	a.
Maximum Body-Diode Continuous Current		Is				9	Α
Maximum Body-Diode Pulsed Current		I _{SM}				36	Α
Drain-Source Diode Forward Voltage		V _{SD}	I _S =9A, V _{GS} =0V			1.7	V
Body Diode Reverse Recovery Tim	ne	t _{RR}	I _S =9A, V _{GS} =0V, dI _F /dt=100A/μs		350		ns
Body Diode Reverse Recovery Charge		Q _{RR}	(Note 1)		2.6	·	μC

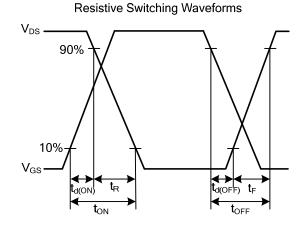
Notes: 1. Pulse Test: Pulse width \leq 300 μ s, Duty cycle \leq 2%.

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

Resistive Switching Test Circuit





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