

# UF8010

**Power MOSFET**

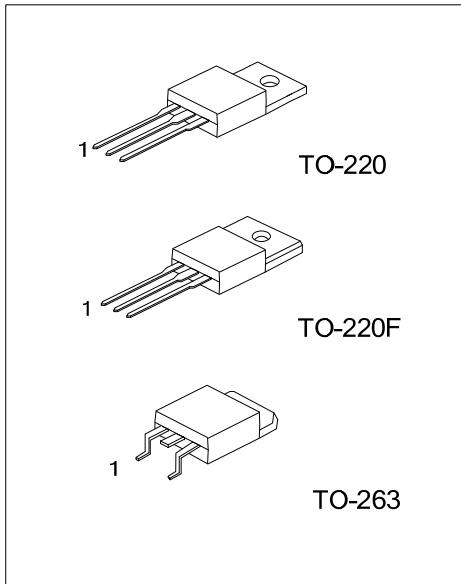
## 80A, 100V N-CHANNEL POWER MOSFET

### ■ DESCRIPTION

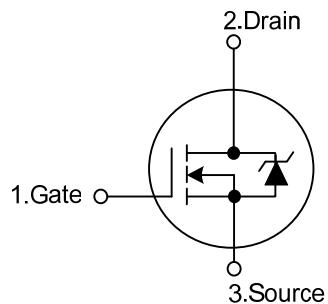
The UTC **UF8010** uses advanced technology to provide excellent  $R_{DS(ON)}$ , fast switching speed, low gate charge, and excellent efficiency. This device is suitable for high frequency DC-DC converters, UPS and motor control.

### ■ FEATURES

- \*  $R_{DS(ON)}$ : 12m $\Omega$  (Typ.)
- \* Lower gate-drain charge for lower switching losses
- \* Perfect avalanche voltage and current performance
- \* Fully characterized capacitance including effective  $C_{oss}$  to simplify design



### ■ SYMBOL



### ■ ORDERING INFORMATION

| Ordering Number | Lead Free | Halogen Free  | Package | Pin Assignment |   |   | Packing |
|-----------------|-----------|---------------|---------|----------------|---|---|---------|
|                 |           |               |         | 1              | 2 | 3 |         |
| UF8010L-TA3-T   |           | UF8010G-TA3-T | TO-220  | G              | D | S | Tube    |
| UF8010L-TF3-T   |           | UF8010G-TF3-T | TO-220F | G              | D | S | Tube    |
| UF8010L-TQ2-T   |           | UF8010G-TQ2-T | TO-263  | G              | D | S | Tube    |

Note: Pin Assignment: G: Gate D: Drain S: Source

|                   |   |  |
|-------------------|---|--|
| UF8010L-TA3-T<br> | (1) Packing Type<br>(2) Package Type<br>(3) Lead Free | (1) T: Tube<br>(2) TA3: TO-220, TF3: TO-220F, TQ2: TO-263<br>(3) L: Lead Free, G: Halogen Free |
|-------------------|---|--|

■ ABSOLUTE MAXIMUM RATINGS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

| PARAMETER  |                       | SYMBOL    | RATINGS     | UNIT                |
|--|-----------------------|-----------|-------------|---------------------|
| Gate to Source Voltage   |                       | $V_{GS}$  | $\pm 20$    | V                   |
| Continuous Drain Current ( $V_{GS}=10\text{V}, T_C=25^\circ\text{C}$ ) |                       | $I_D$     | 80 (Note 2) | A                   |
| Pulsed Drain Current   |                       | $I_{DM}$  | 320         | A                   |
| Avalanche Energy   | Single Pulse (Note 2) | $E_{AS}$  | 310         | mJ                  |
|  | Repetitive            | $E_{AR}$  | 26          | mJ                  |
| Avalanche Current  |                       | $I_{AR}$  | 45          | A                   |
| Peak Diode Recovery dv/dt (Note 3)                                     |                       | dv/dt     | 16          | V/ns                |
| Power Dissipation( $T_C=25^\circ\text{C}$ )                            | TO-220 / TO-263       | $P_D$     | 260         | W                   |
|  | TO-220F               |           | 54          | W                   |
| Derating above $25^\circ\text{C}$                                      | TO-220 / TO-263       |           | 1.8         | W/ $^\circ\text{C}$ |
|  | TO-220F               |           | 0.36        | W/ $^\circ\text{C}$ |
| Junction Temperature   |                       | $T_J$     | +150        | $^\circ\text{C}$    |
| Storage Temperature  |                       | $T_{STG}$ | -55 ~ + 150 | $^\circ\text{C}$    |

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Starting  $T_J = 25^\circ\text{C}$ ,  $L = 0.31\text{mH}$ ,  $R_G = 25\Omega$ ,  $I_{AS} = 45\text{A}$ .

3.  $I_{SD} \leq 45\text{A}$ ,  $di/dt \leq 110\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ ,  $T_J \leq 150^\circ\text{C}$

■ THERMAL DATA

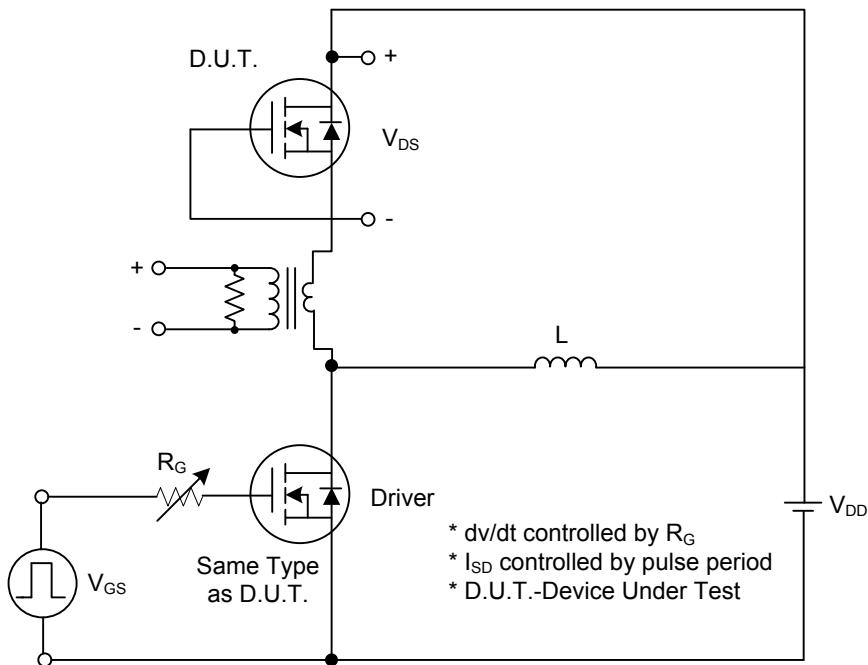
| PARAMETER           |                 | SYMBOL        | RATINGS | UNIT               |
|---------------------|-----------------|---------------|---------|--------------------|
| Junction to Ambient |                 | $\theta_{JA}$ | 62.5    | $^\circ\text{C/W}$ |
| Junction to Case    | TO-220 / TO-263 | $\theta_{JC}$ | 0.57    | $^\circ\text{C/W}$ |
|                     | TO-220F         |               | 2.3     | $^\circ\text{C/W}$ |

■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

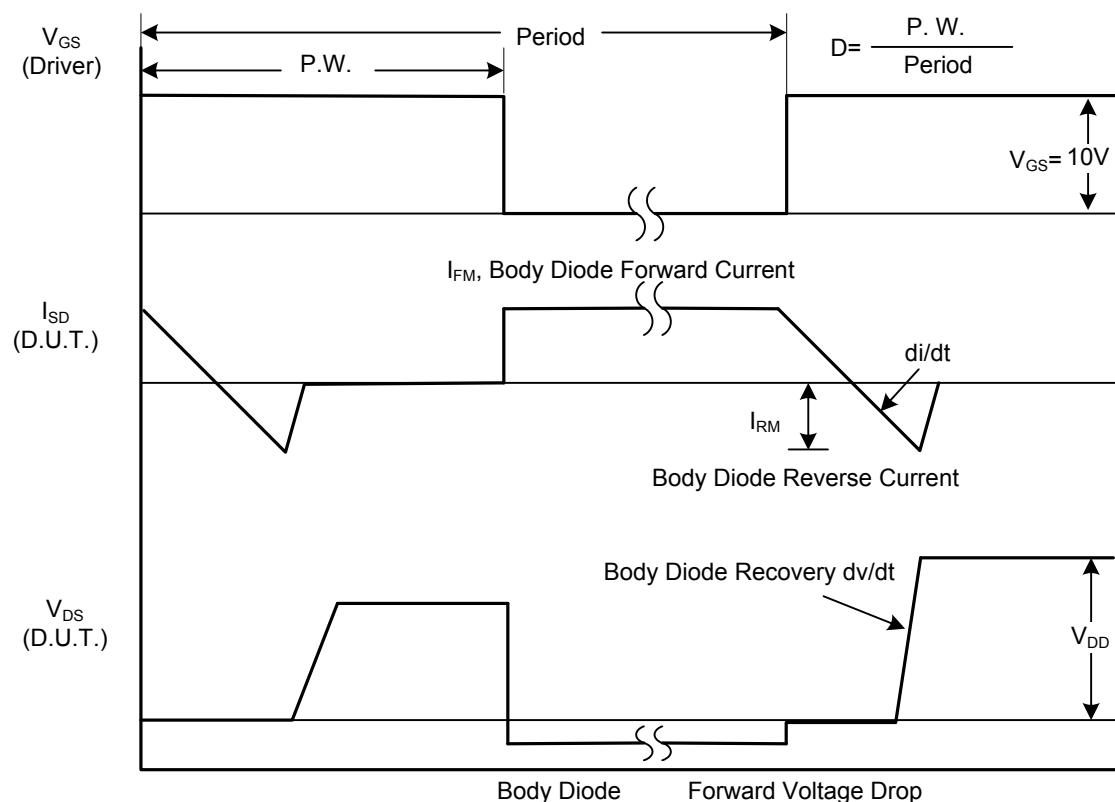
| PARAMETER  | SYMBOL                   | TEST CONDITIONS   | MIN | TYP  | MAX  | UNIT             |
|--|--------------------------|---|-----|------|------|------------------|
| <b>STATIC CHARACTERISTICS</b>                              |                          |   |     |      |      |                  |
| Drain-Source Breakdown Voltage                             | $\text{BV}_{\text{DSS}}$ | $V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$  | 100 |      |      | V                |
| Drain-Source Leakage Current                               | $I_{\text{DSS}}$         | $V_{\text{DS}} = 100 \text{ V}, V_{\text{GS}} = 0 \text{ V}$  |     |      | 20   | $\mu\text{A}$    |
| Gate-Source Forward Current                                | $I_{\text{GSS}}$         | $V_{\text{GS}} = 20 \text{ V}$  |     |      | 200  | nA               |
| Gate-Source Reverse Current                                |                          | $V_{\text{GS}} = -20 \text{ V}$   |     |      | -200 | nA               |
| <b>ON CHARACTERISTICS</b>                                  |                          |   |     |      |      |                  |
| Gate Threshold Voltage                                     | $V_{\text{GS(TH)}}$      | $V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$  | 2.0 |      | 4.0  | V                |
| Drain-Source On-State Resistance                           | $R_{\text{DS(ON)}}$      | $V_{\text{GS}} = 10 \text{ V}, I_D = 45 \text{ A}$ (Note 1)   |     | 12   | 15   | $\text{m}\Omega$ |
| <b>DYNAMIC CHARACTERISTICS</b>                             |                          |   |     |      |      |                  |
| Input Capacitance  | $C_{\text{ISS}}$         | $V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}$                              |     | 3617 |      | pF               |
| Output Capacitance   | $C_{\text{OSS}}$         |   |     | 620  |      | pF               |
| Reverse Transfer Capacitance                               | $C_{\text{RSS}}$         |   |     | 59   |      | pF               |
| <b>SWITCHING CHARACTERISTICS</b>                           |                          |   |     |      |      |                  |
| Turn-On Delay Time   | $t_{\text{D(ON)}}$       | $V_{\text{DS}} = 30 \text{ V}, I_D = 1 \text{ A}, R_G = 39 \Omega$<br>$V_{\text{GS}} = 10 \text{ V}$ (Note 1) |     | 174  | 200  | ns               |
| Rise Time  | $t_R$                    |   |     | 370  | 450  | ns               |
| Turn-Off Delay Time  | $t_{\text{D(OFF)}}$      |   |     | 757  | 850  | ns               |
| Fall Time  | $t_F$                    |   |     | 392  | 450  | ns               |
| Total Gate Charge  | $Q_G$                    | $V_{\text{DS}} = 80 \text{ V}, V_{\text{GS}} = 10 \text{ V}$<br>$I_D = 80 \text{ A}$ (Note 1)                 |     | 399  | 450  | nC               |
| Gate-Source Charge   | $Q_{\text{GS}}$          |   |     | 41   |      | nC               |
| Gate-Drain Charge  | $Q_{\text{GD}}$          |   |     | 96   |      | nC               |
| <b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>     |                          |   |     |      |      |                  |
| Drain-Source Diode Forward Voltage                         | $V_{\text{SD}}$          | $I_S = 80 \text{ A}, V_{\text{GS}} = 0 \text{ V}, T_J = 25^\circ\text{C}$ (Note 1)                            |     |      | 1.3  | V                |
| Maximum Continuous Drain-Source Diode Forward Current      | $I_S$                    |   |     |      | 80   | A                |
| Maximum Pulsed Drain-Source Diode Forward Current (Note 1) | $I_{\text{SM}}$          |   |     |      | 320  | A                |
| Reverse Recovery Time                                      | $t_{\text{RR}}$          | $I_F = 80 \text{ A}, V_{\text{DD}} = 50 \text{ V}, T_J = 150^\circ\text{C}$                                   |     | 99   | 150  | ns               |
| Reverse Recovery Charge                                    | $Q_{\text{RR}}$          | $dI/dt = 100 \text{ A}/\mu\text{s}$ (Note 1)  |     | 460  | 700  | nC               |

Note: 1. Pulse width  $\leq 300 \mu\text{s}$ ; duty cycle  $\leq 2\%$

■ TEST CIRCUITS AND WAVEFORMS

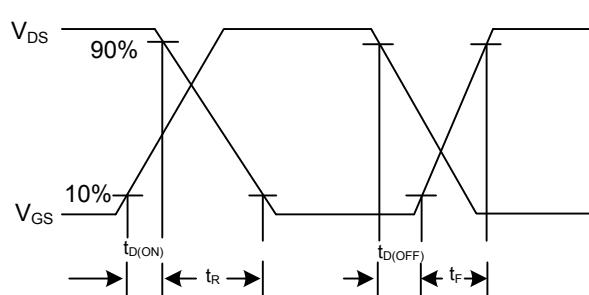
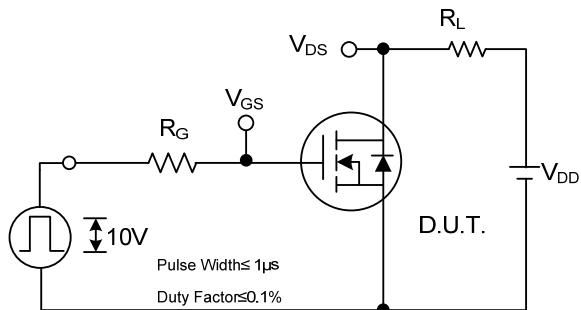


**Peak Diode Recovery dv/dt Test Circuit**



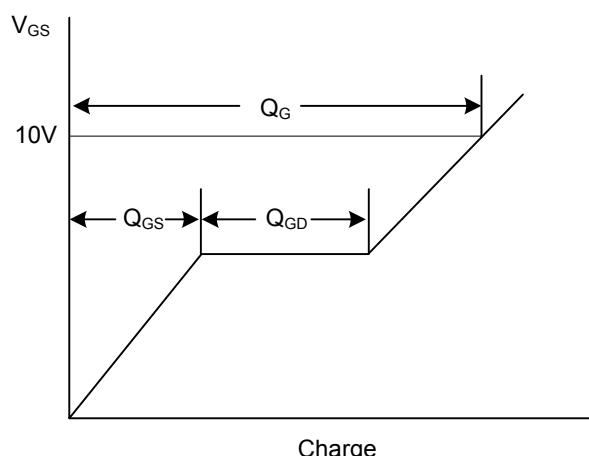
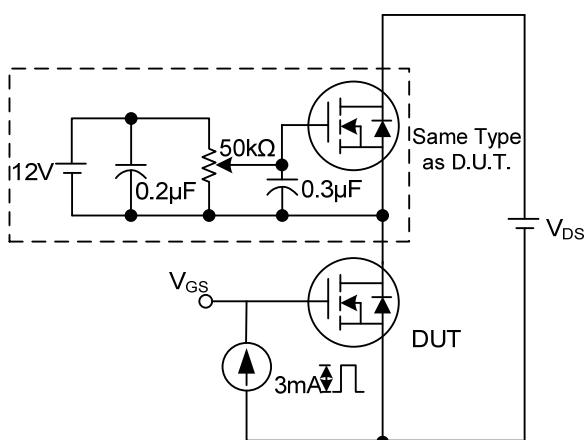
**Peak Diode Recovery dv/dt Waveforms**

### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



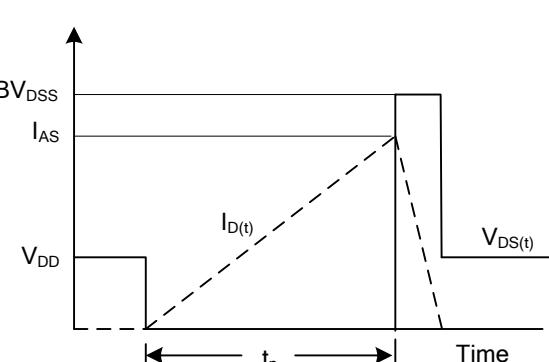
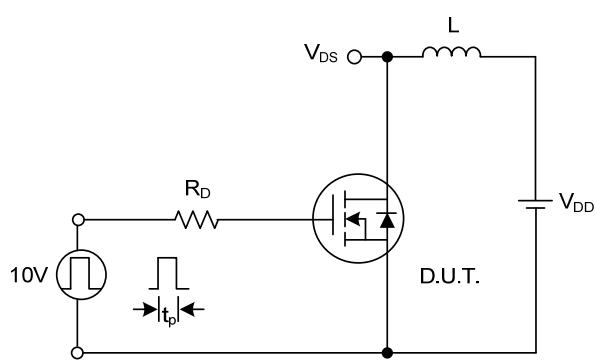
**Switching Test Circuit**

**Switching Waveforms**



**Gate Charge Test Circuit**

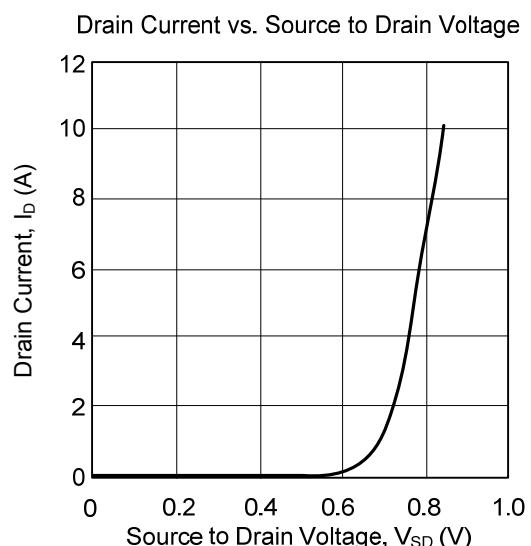
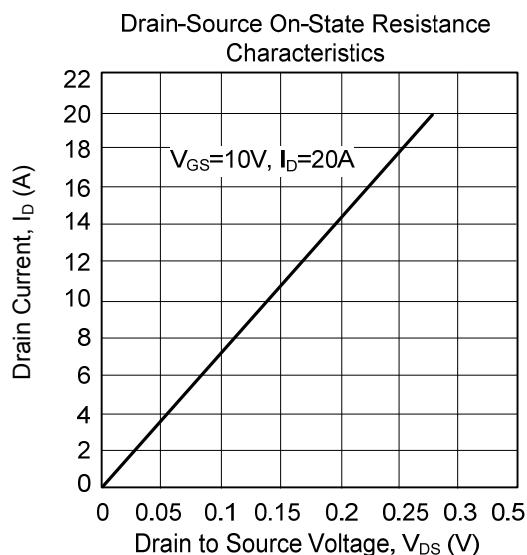
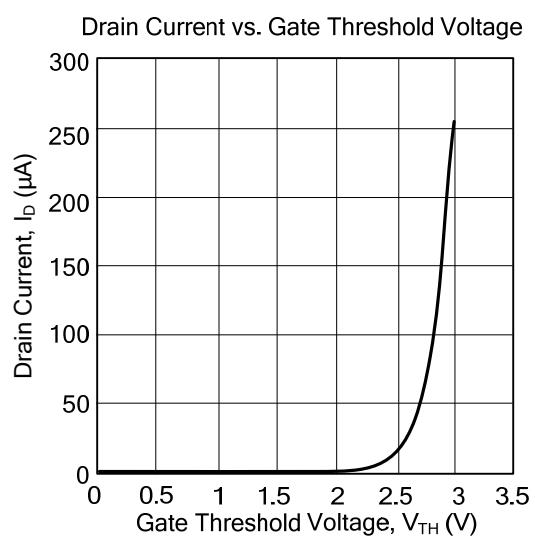
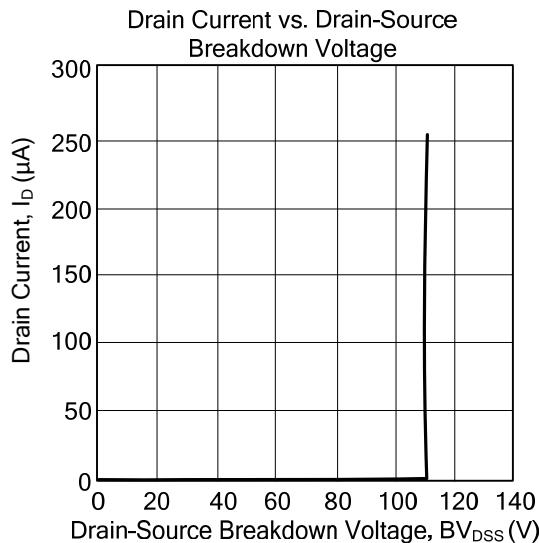
**Gate Charge Waveform**



**Unclamped Inductive Switching Test Circuit**

**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.