



UD9926

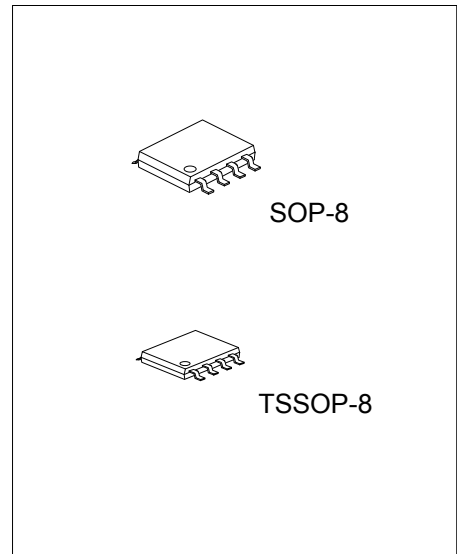
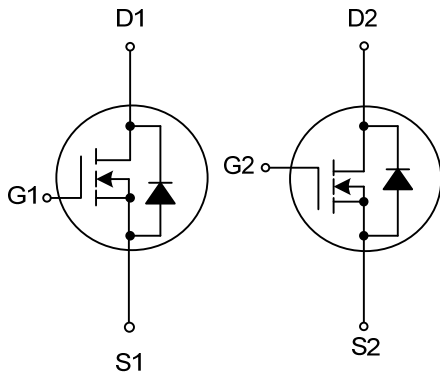
Power MOSFET

DUAL N-CHANNEL ENHANCEMENT MODE

■ FEATURES

- * 20V/6A
- * Low $R_{DS(ON)}$
- * Reliable and Rugged

■ SYMBOL



Lead-free: UD9926L
Halogen-free: UD9926G

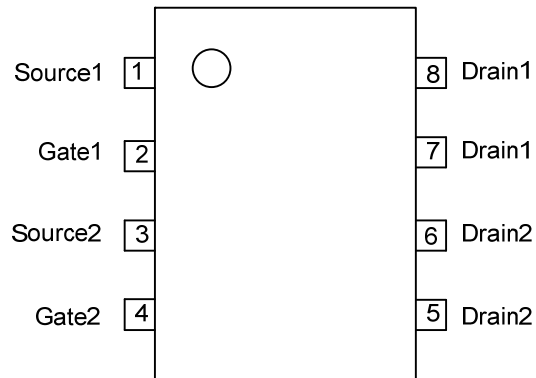
■ ORDERING INFORMATION

Ordering Number			Package	Pin Assignment								Packing
Normal	Lead Free Plating	Halogen Free		1	2	3	4	5	6	7	8	
UD9926-S08-R	UD9926L-S08-R	UD9926G-S08-R	SOP-8	S1	G1	S2	G2	D2	D2	D1	D1	Tape Reel
UD9926-P08-R	UD9926L-P08-R	UD9926G-P08-R	TSSOP-8	D1	S1	S1	G1	G2	S2	S2	D2	Tape Reel

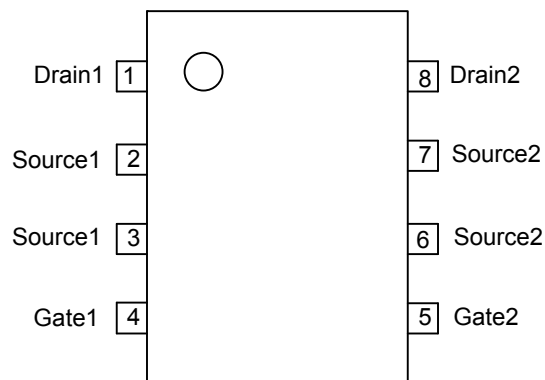
<p>UD9226L-S08-R</p> <ul style="list-style-type: none"> (1)Packing Type (2)Package Type (3)Lead Plating 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) S08: SOP-8, P08: TSSOP-8 (3) G: Halogen Free, L: Lead Free, Blank: Pb/Sn
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■ PIN CONFIGURATION

For SOP-8 Package



For TSSOP-8 Package



■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain Source Voltage	V_{DSS}	20	V
Gate Source Voltage	V_{GSS}	± 10	V
Drain Current	Continuous(Note 3)	I_D	6
	Pulsed (Note 2)	I_{DM}	20
Power Dissipation	SOP-8	P_D	1.6
	TSSOP-8		1.0
Junction Temperature	T_J	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Surface Mounted on 1in^2 pad area, $t \leq 10\text{sec}$.

3. Pulse width limited by $T_{J(\text{MAX})}$

■ THERMAL DATA

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Junction to Ambient	θ_{JA}			80	$^\circ\text{C}/\text{W}$

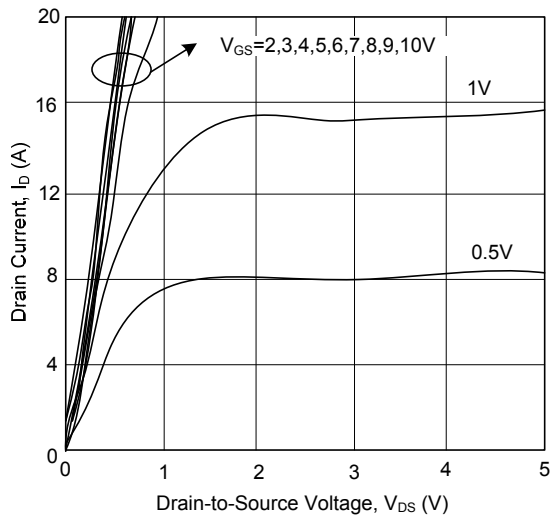
■ ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	20			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=16\text{V}, V_{GS}=0\text{V}$			1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	0.5	0.7	1.5	V
Drain-Source On-State Resistance (Note2)	$R_{DS(\text{ON})}$	$V_{GS}=4.5\text{V}, I_D=6\text{A}$		28	32	$\text{m}\Omega$
		$V_{GS}=2.5\text{V}, I_D=5.2\text{A}$		38	45	$\text{m}\Omega$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1.0\text{MHz}$		520		pF
Output Capacitance	C_{OSS}			110		pF
Reverse Transfer Capacitance	C_{RSS}			70		pF
SWITCHING CHARACTERISTICS						
Turn-ON Delay Time (Note)	$t_{D(\text{ON})}$	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=1\text{A}, R_G=0.2\Omega$		17		ns
Turn-ON Rise Time	t_R			15		ns
Turn-OFF Delay Time	$t_{D(\text{OFF})}$			45		ns
Turn-OFF Fall Time	t_F			25		ns
Total Gate Charge (Note)	Q_G	$V_{DS}=10\text{V}, V_{GS}=-4.5\text{V}, I_D=6\text{A}$		10		nC
Gate-Source Charge	Q_{GS}			3.6		nC
Gate-Drain Charge	Q_{GD}			2		nC
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Drain-Source Diode Forward Voltage(Note2)	V_{SD}	$I_S=1.7\text{A}, V_{GS}=0\text{V}$	0.6		1.3	V
Maximum Continuous Drain-Source Diode Forward Current	I_S				2.5	A

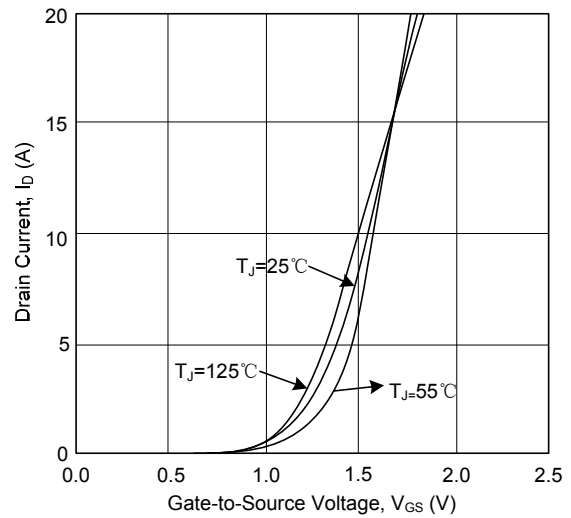
Note: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

TYPICAL CHARACTERISTICS

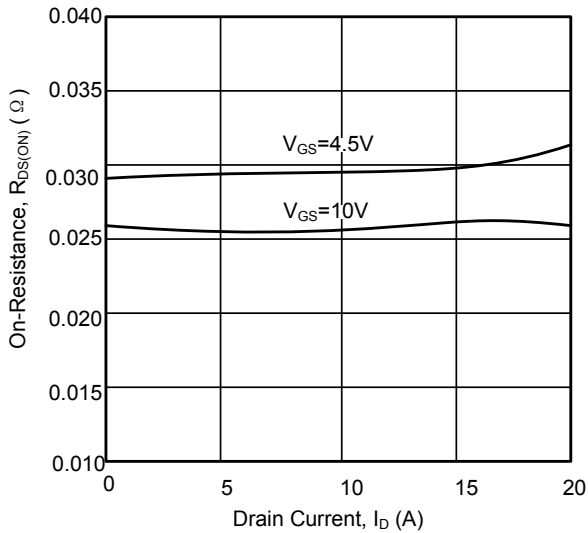
Output Characteristics



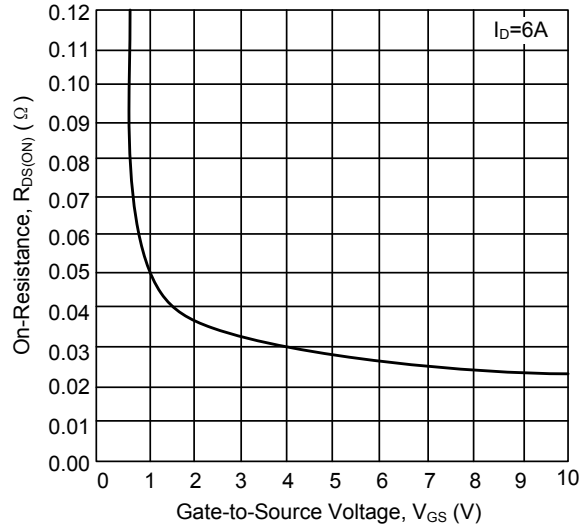
Transfer Characteristics



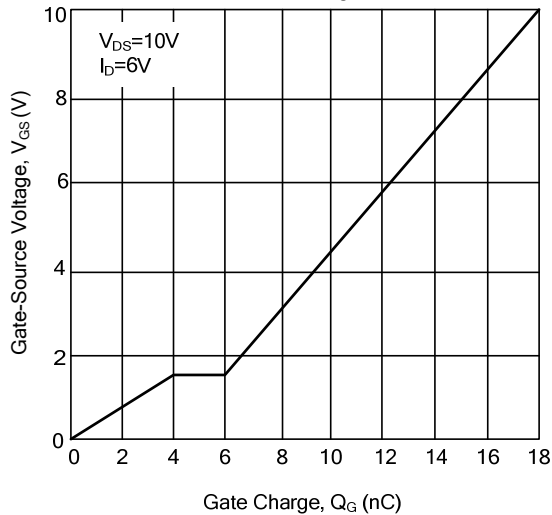
On-Resistance vs. Drain Current



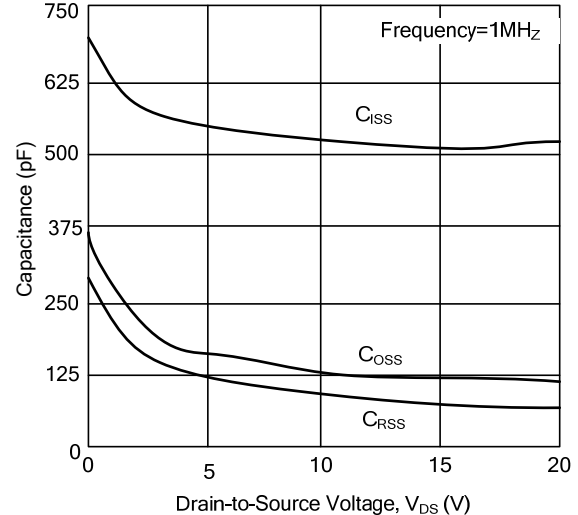
On-Resistance vs. Gate-to-Source Voltage



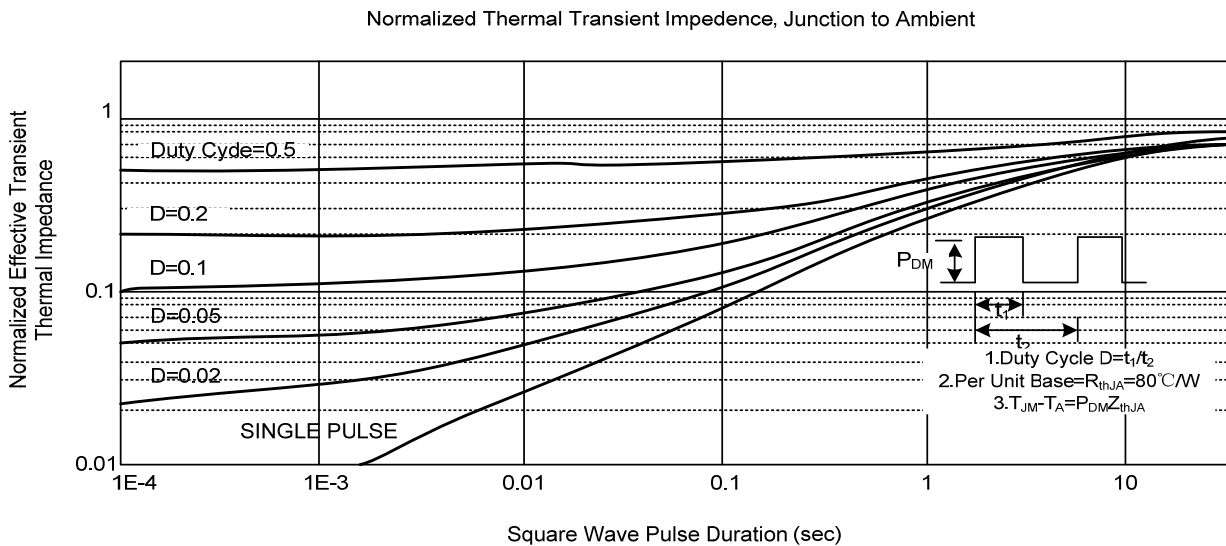
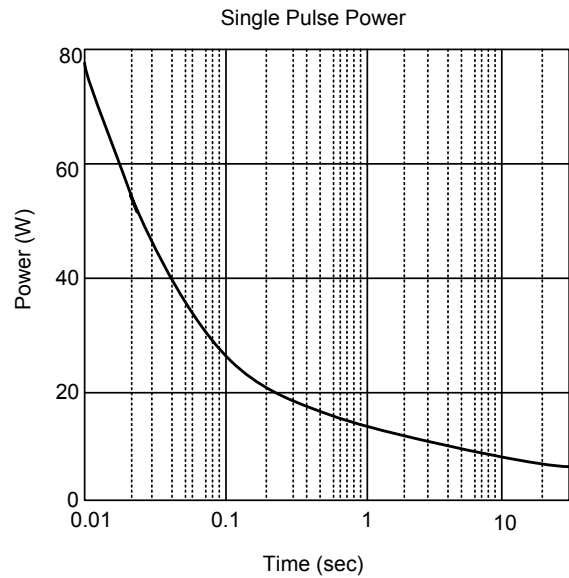
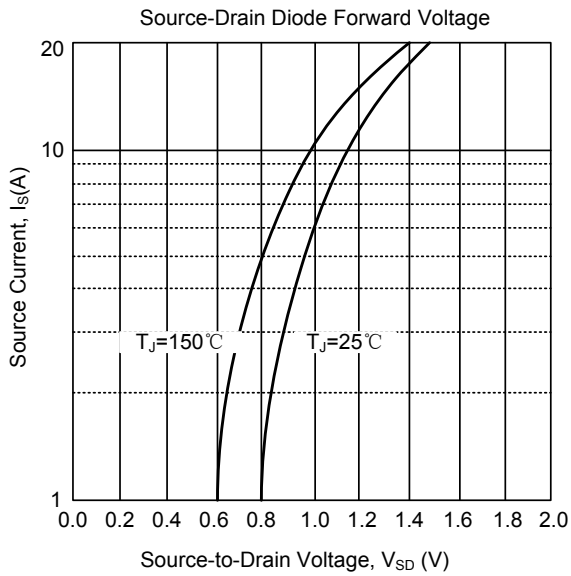
Gate Charge



Capacitance



■ TYPICAL CHARACTERISTICS(Cont.)



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