

NCE5558K

NCE N-Channel Enhancement Mode Power MOSFET

Description

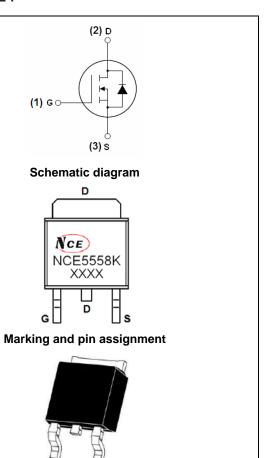
The NCE5558K uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- $V_{DS} = 55V, I_D = 58A$ $R_{DS(ON)} < 13mΩ @ V_{GS} = 10V$ (Typ:10.5mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Synchronous rectifiers for, industrial power supplies
- LED backlighting



TO-252-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE5558K	NCE5558K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_c=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	55	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current-Continuous	I _D	58	Α	
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	41	Α	
Pulsed Drain Current	I _{DM}	100	А	
Maximum Power Dissipation	P _D	80	W	
Derating factor		0.64	W/°C	
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$	

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	1.6	°C/W



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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	55	-	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =55V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2	3	4	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	10.5	13	mΩ	
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	30	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	\/ 20\/\/ 0\/	-	2850	-	PF	
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	258	-	PF	
Reverse Transfer Capacitance	C _{rss}	F=1.0WIDZ	-	26	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	8	-	nS	
Turn-on Rise Time	t _r	V_{DD} =30V, R_L =1.5 Ω	-	2	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	29	-	nS	
Turn-Off Fall Time	t _f		-	4	-	nS	
Total Gate Charge	Q_g	\/ -20\/ I -20A	-	33	-	nC	
Gate-Source Charge	Q _{gs}	V_{DS} =30V, I_D =20A, V_{GS} =10V	-	9	-	nC	
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	4	-	nC	
Drain-Source Diode Characteristics	•		-				
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	58	Α	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =20A	-	25		nS	
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)		50		nC	
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)					

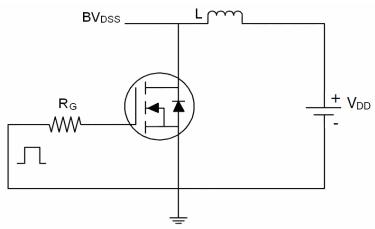
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω

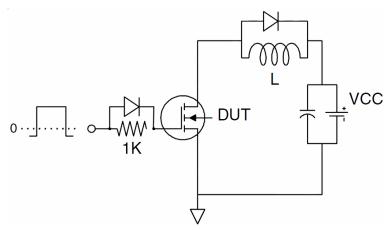


Test circuit

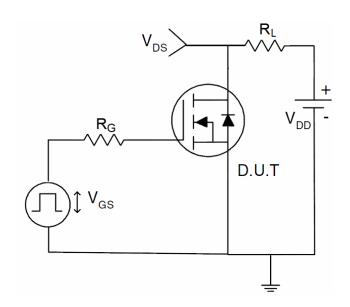
1) E_{AS} test Circuits



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

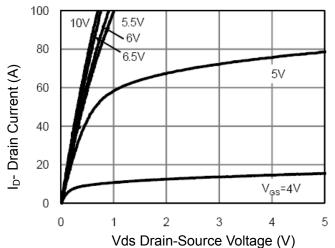


Figure 1 Output Characteristics

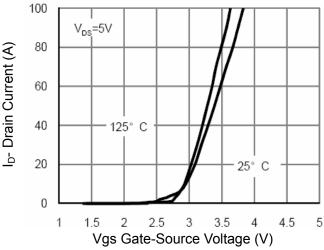


Figure 2 Transfer Characteristics

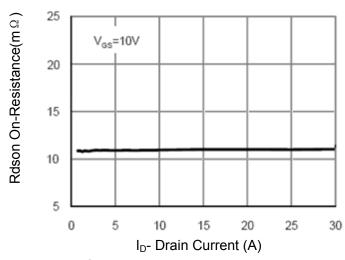


Figure 3 Rdson- Drain Current

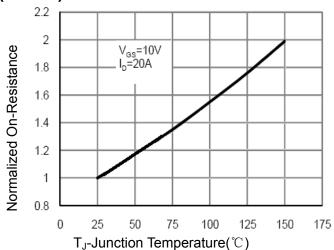


Figure 4 Rdson-JunctionTemperature

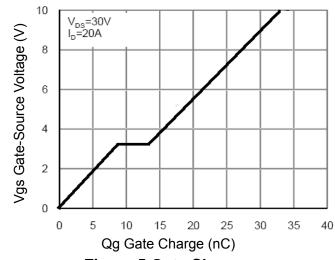


Figure 5 Gate Charge

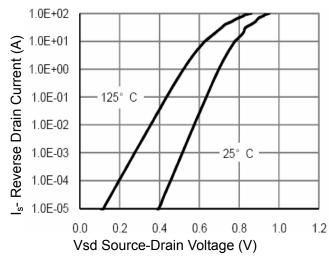
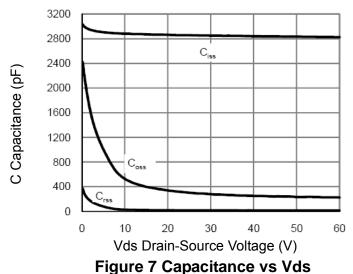


Figure 6 Source- Drain Diode Forward





1000.0 100.0 R_{DS(ON)} Ip- Drain Current (A) 10.0 IIII DC 1.0 T_{J(Max)}=150° C T_c=25° C 0.1 0.0 0.01 10 100 1000 Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area

100 80 (M) uoitedissid 40 20 0 25 50 75 100 125 150 T_J-Junction Temperature (°C)

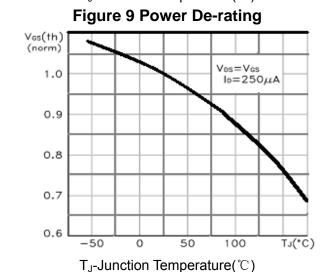
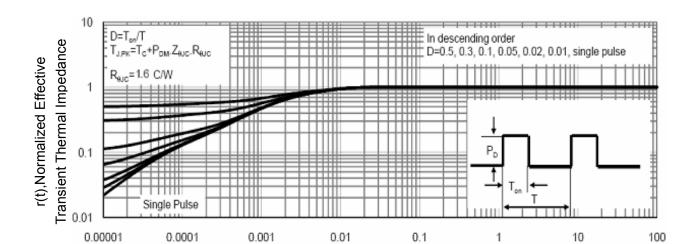


Figure 10 V_{GS(th)} vs Junction Temperature



Square Wave Pluse Duration(sec)

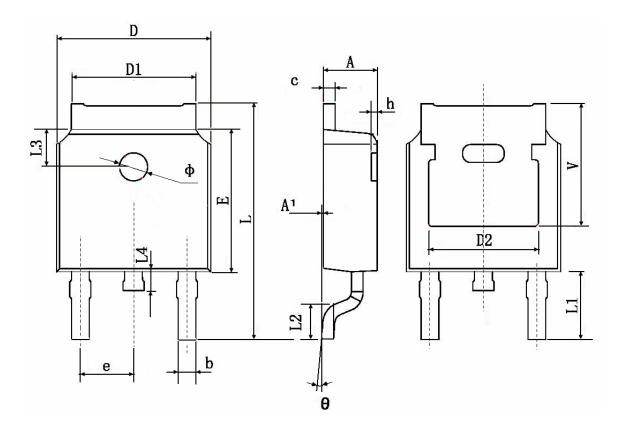
Figure 11 Normalized Maximum Transient Thermal Impedance

Pb Free Product



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TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	0.483	TYP.	0.190 TYP.		
Е	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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