

# **UTC** UNISONIC TECHNOLOGIES CO., LTD

# K1875

Preliminary

JFET

# FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

#### DESCRIPTION

The UTC K1875 is an N-channel JFET, it uses UTC's advanced technology to provide customers low input capacitance and high forward transfer admittance.

The UTC K1875 is suitable for high frequency amplifier and audio frequency amplifier applications, etc.

#### **FEATURES**

\* High forward transfer admittance

\* Low input capacitance

#### **ORDERING INFORMATION**

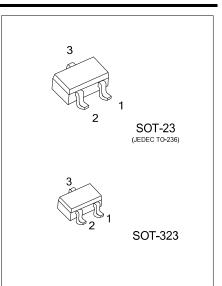
Ordering Number	Package	Pin Assignment			Deaking	
Ordering Number		1	2	3	Packing	
K1875G-xx-AE3-R	SOT-23	D	S	G	Tape Reel	
K1875G-xx-AQ3-R	SOT-723	D	S	G	Tape Reel	
Noto: Din Assignment: D: Droin S: Source C: Cate						

Note: Pin Assignment: D: Drain S: Source G: Gate

K1875 <u>G-xx-AE3-R</u>	
(1)Packing Type	(1) R: Tape Reel
(2)Package Type	(2) AE3: SOT-23, AL3: SOT-323
(3)Rank	(3) xx: refer to Classification of I <sub>DSS</sub>
(4)Green Package	(4) G: Halogen Free and Lead Free
(4)Green Package	(4) G: Halogen Free and Lead Free

#### MARKING





## ■ ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Gate-Drain Voltage	V <sub>GDS</sub>	-20	V
Gate-Current	l <sub>G</sub>	10	mA
Drain Power Dissipation	PD	100	mW
Junction Temperature	TJ	125	°C
Storage Temperature Range	T <sub>STG</sub>	-55~125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub> =25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =-15V, V <sub>DS</sub> =0V			-1.0	nA
Gate-Drain Breakdown Voltage	V <sub>(BR)GDS</sub>	V <sub>DS</sub> =0V, I <sub>G</sub> =-100µA	-20			V
Drain Current	I <sub>DSS</sub>	$V_{DS}=5V, V_{GS}=0V$	6		32	mA
Gate-Source Cut-Off Voltage	V <sub>GS</sub> (OFF)	V <sub>DS</sub> =5V, I <sub>D</sub> =1µA			-2.5	V
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> =5V, V <sub>GS</sub> =0V, f=1kHz	15	25		mS
Input Capacitance	Ciss	V <sub>DS</sub> =5V, V <sub>GS</sub> =0V, f=1MHz		7.5	10	рF
Reverse Transfer Capacitance	Crss	V <sub>DG</sub> =5V, I <sub>D</sub> =0V, f=1MHz		2	3	рF

## CLASSIFICATION OF I<sub>DSS</sub>

RANK	GR	BL	V
RANGE	6~12	10~20	16~32



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