# UNISONIC TECHNOLOGIES CO., LTD

BD435

**Preliminary** 

## NPN EPITAXIAL SILICON TRANSISTOR

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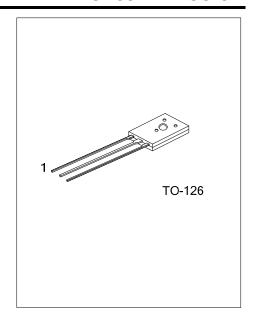
#### DESCRIPTION

The UTC **BD435** is a NPN epitaxial silicon transistor, it uses UTC's advanced technology to provide the customers with high DC current gain, etc.

The UTC **BD435** is suitable for medium power linear and switching applications.

### **■ FEATURES**

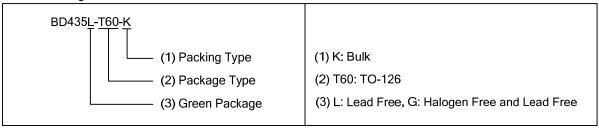
\* High DC current gain



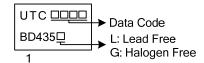
#### ORDERING INFORMATION

Ordering Number		Doolsone	Pin Assignment			Deelsing	
Lead Free	Halogen Free	Package	1	2	3	Packing	
BD435L-T60-K	BD435G-T60-K	TO-126	K	Α	G	Bulk	
BD435L-T60-K	BD435G-T60-K	TO-126	K	Α	G	Bulk	

Note: Pin assignment: G: Gate K: Cathode A: Anode



#### MARKING



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### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> =25°C, unless otherwise noted)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage	$V_{CBO}$	32	V
Collector-Emitter Voltage	$V_{CEO}$	32	V
Collector-Emitter Voltage	V <sub>CES</sub>	32	V
Emitter-Base Voltage	$V_{EBO}$	5	V
Collector Current (DC)	Ic	4	Α
Collector Current (Pulse) (Note 1)	I <sub>CP</sub>	7	Α
Base Current	Ι <sub>Β</sub>	1	Α
Collector Dissipation (T <sub>C</sub> =25°C)	Pc	36	W
Junction Temperature	$T_J$	150	°C
Storage Temperature	T <sub>STG</sub>	-65 ~150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> =25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Sustaining Voltage	$V_{CEO(SUS)}$	I <sub>C</sub> =100mA, I <sub>B</sub> =0A	32			V
Collector Cut-Off Current	$I_{CBO}$	$V_{CB}$ =32V, $I_{E}$ =0			100	μΑ
Collector Cut-Off Current	I <sub>CEO</sub>	$V_{CE}$ =32V, $V_{BE}$ =0			100	μΑ
Emitter Cut-Off Current	$I_{EBO}$	$V_{EB}$ =5 $V$ , $I_{C}$ =0			1	mA
		$V_{CE}$ =5V, $I_{C}$ =10mA	40	130		
DC Current Gain (Note 1)	$h_{FE}$	$V_{CE}$ =1V, $I_{C}$ =500mA	85	140		
		$V_{CE}$ =1V, $I_{C}$ =2A	50			
Collector-Emitter Saturation Voltage (Note 1)	$V_{CE(SAT)}$	I <sub>C</sub> =2A, I <sub>B</sub> =0.2A		0.2	0.5	V
Base-Emitter ON Voltage (Note 1)	$V_{BE(ON)}$	$V_{CE}$ =1V, $I_{C}$ =2A			1.1	V
Current Gain Bandwidth Product	$f_T$	V <sub>CE</sub> =1V, I <sub>C</sub> =250mA	3			MHz

Note: Pulse Test:  $P_W$ =300 $\mu$ s, duty Cycle=1.5% Pulsed

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