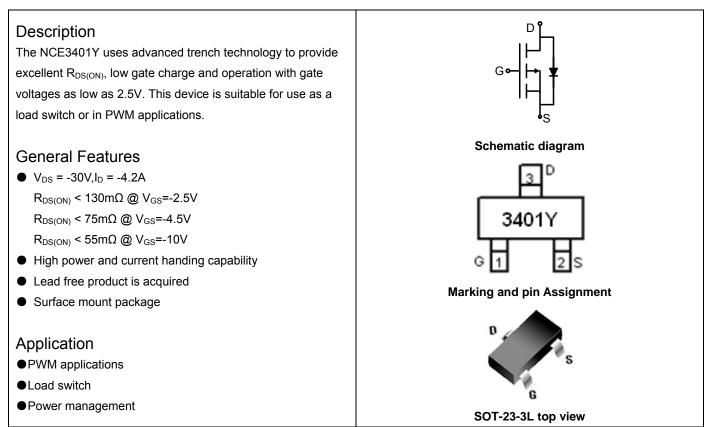


NCE P-Channel Enhancement Mode Power MOSFET



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3401Y	NCE3401Y	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	I _D	-4.2	А
Drain Current-Pulsed (Note 1)	I _{DM}	-30	А
Maximum Power Dissipation	PD	1.2	W
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	104	°C/W
		1	1

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-30		-	V



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NCE3401Y

Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA	-0.7	-1	-1.3	V
		V _{GS} =-10V, I _D =-4.2A	-	47	55	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4A	-	56	75	mΩ
		V _{GS} =-2.5V, I _D =-1A		72	130	mΩ
Forward Transconductance	g _{FS}	V _{DS} =-5V,I _D =-4.2A	-	10	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}		-	880	-	PF
Output Capacitance	C _{oss}	– V _{DS} =-15V,V _{GS} =0V, – F=1.0MHz	-	105	-	PF
Reverse Transfer Capacitance	C _{rss}		-	65	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	7	-	nS
Turn-on Rise Time	tr	V _{DD} =-15V,I _D =-4.2A	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =-10V,R _{GEN} =6Ω	-	30	-	nS
Turn-Off Fall Time	t _f		-	12	-	nS
Total Gate Charge	Qg		-	8.5	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-4.2A,V _{GS} =-4.5V	-	1.8	-	nC
Gate-Drain Charge	Q _{gd}		-	2.7	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-4.2A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t ≤ 10 sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production



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Typical Electrical and Thermal Characteristics

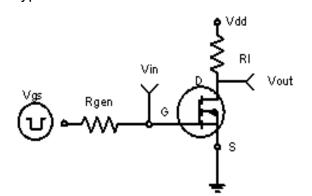
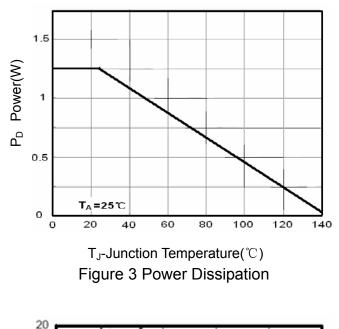
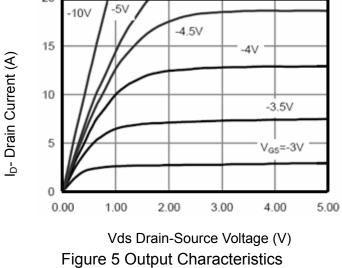


Figure 1:Switching Test Circuit





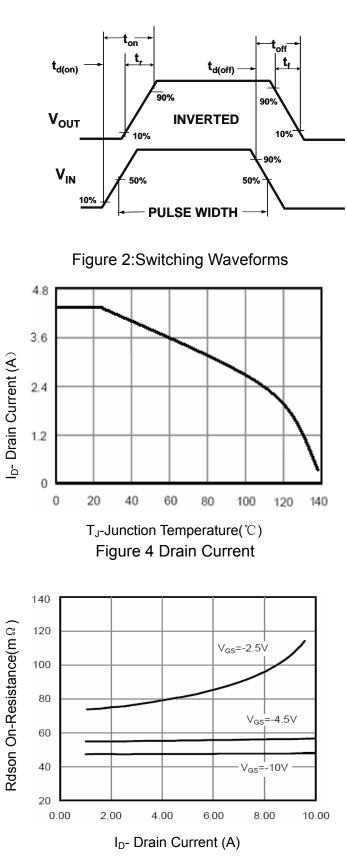


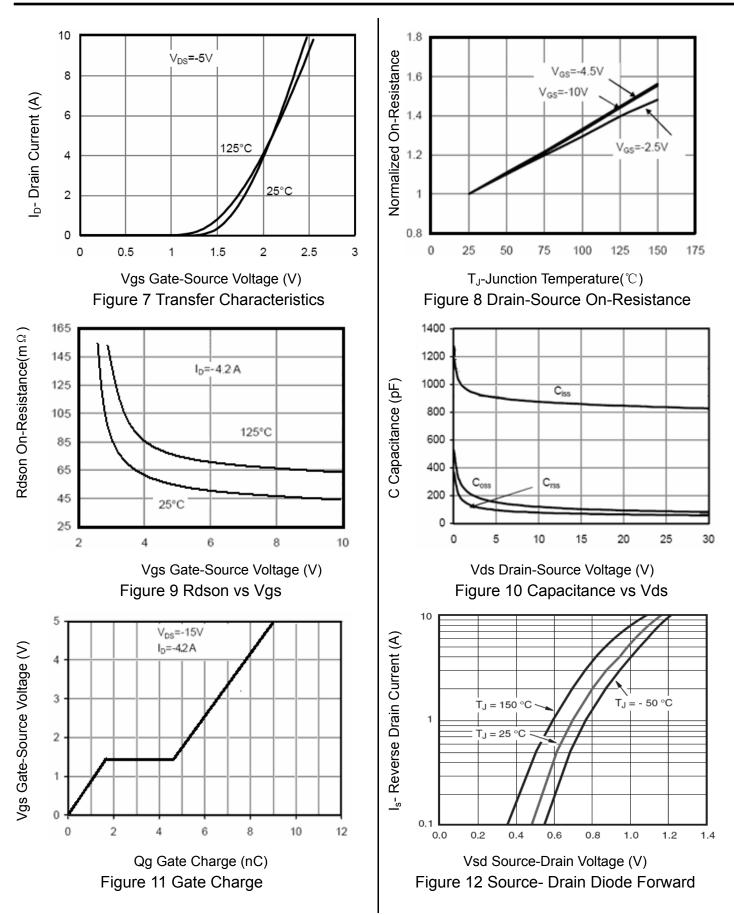
Figure 6 Drain-Source On-Resistance



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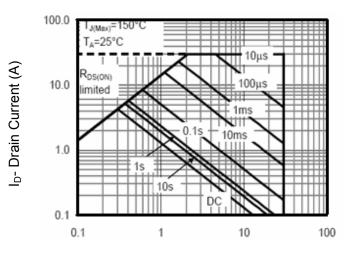


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Vds Drain-Source Voltage (V) Figure 13 Safe Operation Area

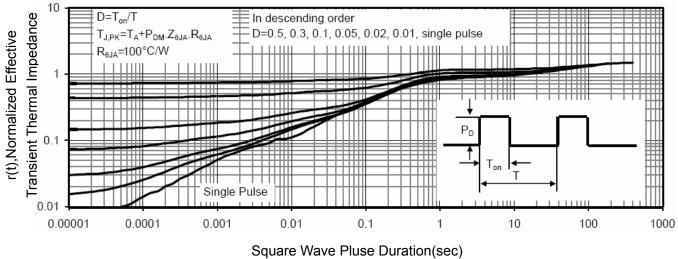
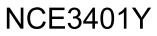


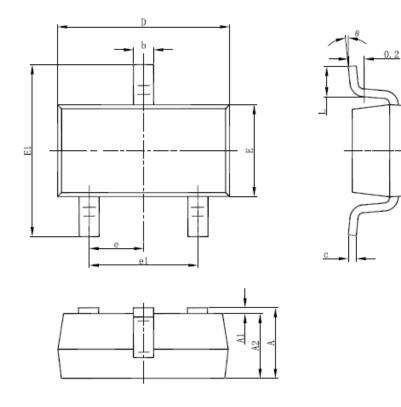
Figure 14 Normalized Maximum Transient Thermal Impedance







SOT-23-3L Package Information



Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
с	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Notes

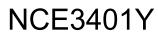
1. All dimensions are in millimeters.

- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.

5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.







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