

# UNISONIC TECHNOLOGIES CO., LTD

MMDT5401 Preliminary DUAL TRANSISTOR

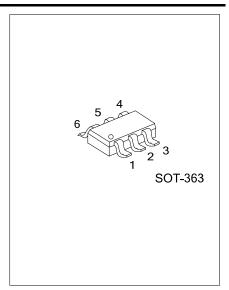
# HIGH VOLTAGE SWITCHING TRANSISTOR

#### **■ DESCRIPTION**

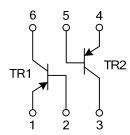
The UTC **MMDT5401** is a high voltage fast-switching dual PNP transistor. It is characterized with high breakdown voltage, high current gain and high switching speed.

#### **■ FEATURES**

- \* High Collector-Emitter Voltage: V<sub>CEO</sub> = -150V
- \* High current gain

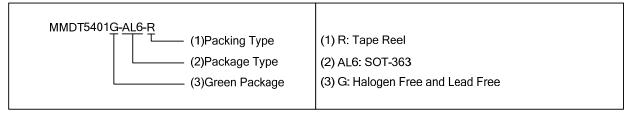


#### **■** EQUIVALENT CIRCUIT

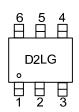


#### ORDERING INFORMATION

Ordering Number	Package	Pin Assignment					Doolsing	
		1	2	3	4	5	6	Packing
MMDT5401G-AL6-R	SOT-363	E1	B1	C2	E2	B2	C1	Tape Reel



## **■** MARKING



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# ■ **ABSOLUATE MAXIUM RATINGS** (T<sub>A</sub>=25°C unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector -Base Voltage	$V_{CBO}$	-160	V
Collector -Emitter Voltage	$V_{CEO}$	-150	V
Emitter -Base Voltage	$V_{EBO}$	-5	V
DC Collector Current	Ic	-600	mA
Power Dissipation	P <sub>D</sub>	200	mW
Junction Temperature	TJ	+150	°C
Storage Temperature	T <sub>STG</sub>	-40 ~ <b>+</b> 150	°C

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ ELECTRICAL CHARACTERISTICS (Ta= 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	V <sub>CBO</sub>	I <sub>C</sub> =-100μA, I <sub>E</sub> =0	-160			V
Collector-Emitter Breakdown Voltage	$V_{CEO}$	$I_C=-1$ mA, $I_B=0$	-150			V
Emitter-Base Breakdown Voltage	$V_{EBO}$	I <sub>E</sub> =-10μA, I <sub>C</sub> =0	-5			V
Collector Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> =-120V, I <sub>E</sub> =0			-50	nA
Emitter Cut-off Current	I <sub>EBO</sub>	$V_{BE}$ =-3 $V$ , $I_{C}$ =0			-50	nA
DC Current Gain(note)	h <sub>FE</sub>	$V_{CE}$ =-5V, $I_C$ =-1mA	80			
		$V_{CE}$ =-5V, $I_{C}$ =-10mA	80	160	400	
		$V_{CE}$ =-5V, $I_C$ =-50mA	80			
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-10mA, I <sub>B</sub> =-1mA			-0.2	V
		I <sub>C</sub> =-50mA, I <sub>B</sub> =-5mA			-0.5	V
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	I <sub>C</sub> =-10mA, I <sub>B</sub> =-1mA			-1	V
		I <sub>C</sub> =-50mA, I <sub>B</sub> =-5mA			-1	V
Current Gain Bandwidth Product	f <sub>T</sub>	$V_{CE}$ =-10V, $I_{C}$ =-10mA, f=100MHz	100		300	MHz
Output Capacitance	C <sub>ob</sub>	$V_{CB}$ =-10V, $I_E$ =0, f=1MHz			6.0	pF
Noise Figure	NF	$I_C$ =-0.25mA, $V_{CE}$ =-5 $V$ R <sub>S</sub> =1kΩ, f=10Hz ~ 15.7kHz			8	dB

Note: Pulse test: PW<300µs, Duty Cycle<2%

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