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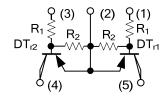
UA11J **Preliminary DUAL TRANSISTOR**

EMITTER COMMON (DUAL DIGITAL TRANSISTORS)

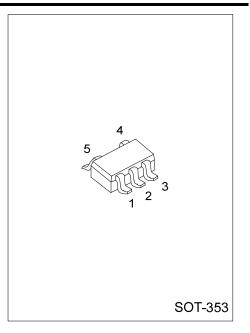
FEATURES

- * Two DTA143Z Chips in a SOT-353 Package.
- * Mounting Cost and Area can be Cut in Half.
- * Epitaxial Planar Type
- * PNP Silicon Transistor(Built-In Resistor Type)

EQUIVALENT CIRCUIT

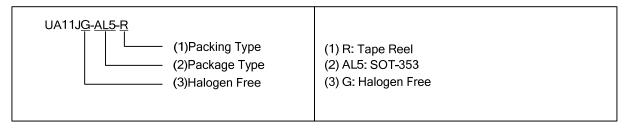


 R_1 =4.7 $k\Omega$ $R_2=47k\Omega$



ORDERING INFORMATION

Ordering Number	Package	Pin Assignment				Dooking	
		1	2	3	4	5	Packing
UA11JG-AL5-R	SOT-353	B1	E1.E2	B2	C2	C1	Tape Reel



MARKING



www.unisonic.com.tw 1 of 2 QW-R222-003.a

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V _{CC}	-50	V
Input Voltage	V_{IN}	-30 ~ +5	V
Output Current	I _{OUT}	-100	mA
Collector Current	I _{C(MAX)}	-100	mA
Total Power Dissipation (Note1)	P _D	150	mW
Junction Temperature	TJ	+150	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Note 1. 120mW per element must not be exceeded.

■ **ELECTRICAL CHARACTERISTICS** (Ta=25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	$V_{IN(OFF)}$	V _{CC} =-5V, I _{OUT} =-100μA			-0.5	V
	$V_{IN(ON)}$	V _{OUT} =-0.3V, I _{OUT} =-5mA	-3			V
Output Voltage	V _{OUT(ON)}	$I_{OUT}/I_{IN}=-5$ mA/ -0.25 mA		-0.1	-0.3	V
Input Current	I _{IN}	V _{IN} =-5V			-1.8	mΑ
Output Current	I _{OUT(OFF)}	V _{CC} =-50V, V _{IN} =0V			-0.5	μΑ
DC Current Gain	h _{FE}	V _{OUT} =-5V, I _{OUT} =-10mA	80			
Transition frequency of the device	f _T	V _{CE} =-10V, I _E =-5mA, f=100MHz		250		MHz
Input Resistance	R ₁		3.29	4.7	6.11	ΚΩ
Resistance Ratio	R ₂ /R ₁		8	10	12	

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^{2.} Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.