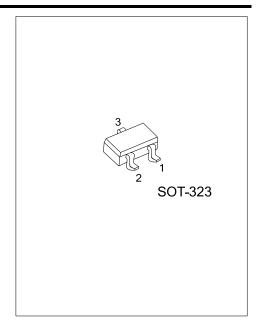
MMBTA05

AMPLIFIER TRANSISTOR

NPN MMBTA05

■ FEATURES

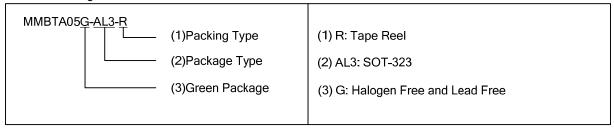
* Collector-Emitter Voltage: V_{CEO}=60V



ORDERING INFORMATION

Ordering Number	Package	Pin Assignment			Da alsia a	
		1	2	3	Packing	
MMBTA05G-AL3-R	SOT-323	В	Е	С	Tape Reel	

Note: Pin Assignment: E: Emitter B: Base C: Collector



■ MARKING



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■ **ABSOLUTE MAXIMUM RATING** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-base voltage	V_{CBO}	60	V
Collector-emitter voltage	V_{CEO}	60	V
Emitter-base voltage	V_{EBO}	4	V
Collector current - Continuous	Ic	500	mA
Power Dissipation, @T _A =25°C	P_{D}	150	mW
Junction Temperature	TJ	125	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
junction to ambient (Note)	θ_{JA}	833	°C/W
junction to case	θ_{JC}	347	°C/W

Note: θ_{JA} is measured with the device soldered into a typical printed circuit board.

■ **ELECTRICAL CHARACTERISTICS** (T_A=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C =1.0mA, I _B =0(Note 1)	60			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	I _E =100μA, Ic=0	4			V
Collector cutoff current	I _{CEO}	V _{CE} =60V, I _B =0			0.1	μΑ
Collector cutoff current	I _{CBO}	V _{CB} =60V, I _E =0			0.1	μΑ
ON CHARACTERISTICS						
DC current gain	h _{FE}	I _C =10mA, V _{CE} =1V	100			
		$I_C=100$ mA, $V_{CE}=1$ V	100			
Collector-emitter saturation voltage	$V_{CE(SAT)}$	I _C =100mA, I _B =10mA			0.25	V
Base-emitter on voltage	$V_{BE(ON)}$	$I_C=100$ mA, $V_{CE}=1$ V			1.2	V
SMALL-SIGNAL CHARACTERISTICS						
Current gain bandwidth product	f _T	I _C =10mA, V _{CE} =2V, f=100MHz(Note 2)	100			MHz

Note: 1. Pulse test: PW<=300μs, Duty Cycle<=2%

^{2.} f_T is defined as the frequency at which Ihfel extrapolates to unity.

■ SWITCHING TIME TEST CIRCUIT

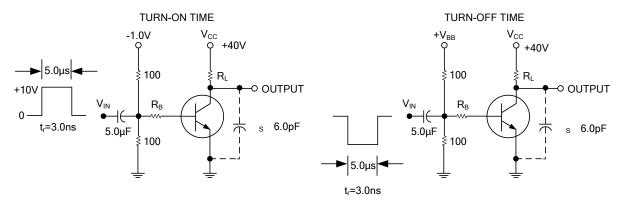


Figure 1. (Note: Total shunt capacitance of test jig and connectors for PNP test circuits, reverse all voltage polarities.)

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