UNISONIC TECHNOLOGIES CO., LTD

DTD114E

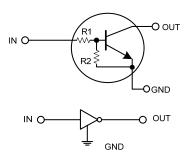
NPN SILICON TRANSISTOR

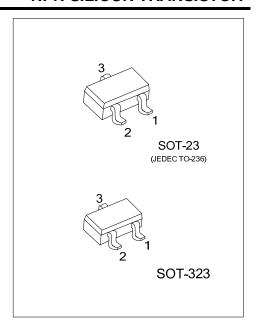
NPN DIGITAL TRANSISTOR (BUILT- IN BIAS RESISTORS)

■ FEATURES

- * Built-in bias resistors that implies easy ON/OFF applications.
- * The bias resistors are thin-film resistors with complete isolation to allow negative input.

■ EQUIVALENT CIRCUIT

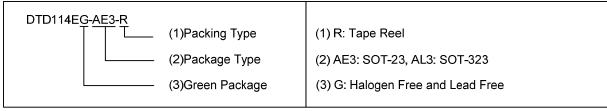




■ ORDERING INFORMATION

Ordering Number	Deekees	Pin Assignment			Danking	
	Package	1	2	3	Packing	
DTD114EG-AE3-R	SOT-23	G	I	0	Tape Reel	
DTD114EG-AL3-R	SOT-323	G	ı	0	Tape Reel	

Note: Pin Assignment: G: GND I: IN O: OUT



■ MARKING



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■ **ABSOLUTE MAXIMUM RATINGS** (T_A=25°C, unless others specified)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-10~+40	V
Output Current	I _{OUT}	500	mA
Power Dissipation	P_{D}	200	mW
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-40 ~ +150	°C

Note Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

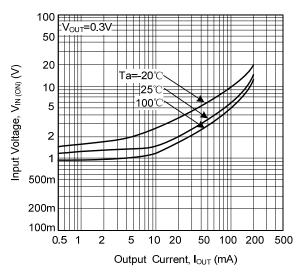
■ **ELECTRICAL SPECIFICATIONS** (T_A=25°C, unless others specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V _{IN(OFF)}	V _{CC} =5V, I _{OUT} =100μA			0.5	
	V _{IN(ON)}	V _{OUT} =0.3V, I _{OUT} =10mA	3			V
Output Voltage	V _{OUT(ON)}	I _{OUT} /I _{IN} =50mA/2.5mA		0.1	0.3	V
Input Current	I _{IN}	V _{IN} =5V			0.88	mA
Output Current	I _{OUT(OFF)}	V _{CC} =50V, V _{IN} =0V			0.5	μA
DC Current Gain	h _{FE}	V _{OUT} =5V, I _{OUT} =50mA	56			
Input Resistance	R ₁		7	10	13	kΩ
Resistance Ratio	R ₂ /R ₁	_	8.0	1	1.2	
Transition Frequency	f _T	$V_{CE} = 10V$, $I_{E} = -50$ mA, $f = 100$ MHz		200		MHz

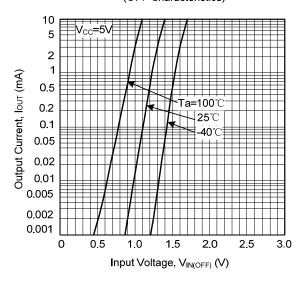
^{*}Transition frequency of the device

■ TYPICAL CHARACTERISTICS

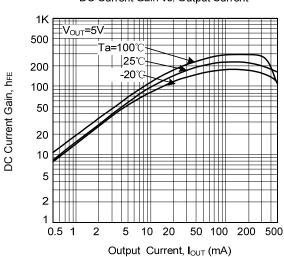
Input Voltage vs. Output Current (ON Characteristics)



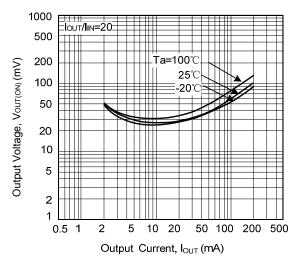
Output Current vs. Input Voltage (OFF Characteristics)



DC Current Gain vs. Output Current



Output Voltage vs. Output Current



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