

UNISONIC TECHNOLOGIES CO., LTD

DTB114E

PNP SILICON TRANSISTOR

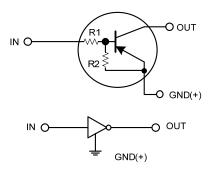
DIGITAL TRANSISTORS (BUILT-IN BIAS RESISTORS)

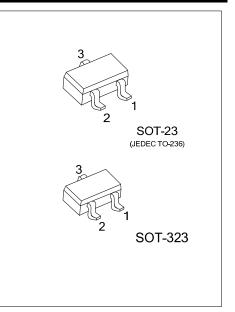
FEATURES

* Built-in bias resistors that implies easy ON/OFF applications.

* The bias resistors are thin-film resistors with complete isolation to allow positive input.

EQUIVALENT CIRCUIT





ORDERING INFORMATION

Ordering Number	Package	Pin Assignment			Decking	
		1	2	3	Packing	
DTB114EG-AE3-R	SOT-23	G	I	0	Tape Reel	
DTB114EG-AL3-R	SOT-323	G	I	0	Tape Reel	
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Note: Pin Assignment: G: GND I: IN O: OUT

DTB114EG- <u>AE3-</u> R	1)Packing Type	(1) R: Tape Reel
	2)Package Type	(2) AE3: SOT-23, AL3: SOT-323
(3	3)Green Package	(3) G: Halogen Free and Lead Free

MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_A = 25°C, unless otherwise specified)

PARAMETER	SYMBOL RATING		UNIT
Supply Voltage	V _{cc}	-50	V
Input Voltage	V _{IN}	-40~+10	V
Output Current	I _{OUT}	-500	mA
Power Dissipation	PD	200	
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A = 25°C, unless otherwise specified.)

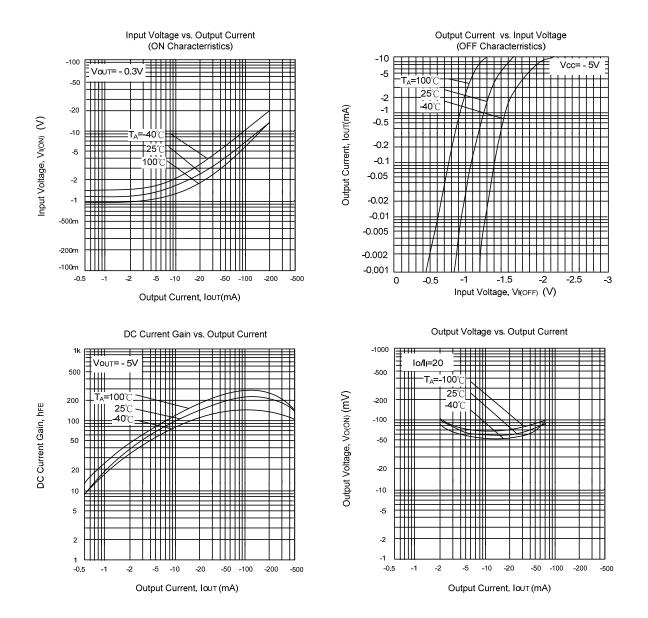
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Voltage	V _{IN(OFF)}	V _{CC} = -5V, Ι _{ΟUT} = -100μΑ			-0.5	V
	V _{IN(ON)}	V _{OUT} = -0.3V, I _{OUT} = -10mA	-3			V
Output Voltage	V _{OUT(ON)}	I _{OUT} /I _{IN} = -50mA/-2.5 mA		-0.1	-0.3	V
Input Current	I _{IN}	V _{IN} = -5V			-0.88	mA
Output Current	I _{OUT(OFF)}	V _{CC} = -50V , V _{IN} =0V			-0.5	μA
DC Current Gain	h _{FE}	V _{OUT} = -5V, I _{OUT} = -50mA	56			
Input Resistance	R ₁		7	10	13	kΩ
Resistance Ratio	R_2/R_1		0.8	1	1.2	
Transition Frequency	f _T	V _{CE} = -10 V, I _E =5mA, f=100MHz(Note)		200		MHz

Note: Transition frequency of the device



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TYPICAL CHARACTERISTICS



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