

UNISONIC TECHNOLOGIES CO., LTD

2SB1182

PNP SILICON TRANSISTOR

MEDIUM POWER LOW VOLTAGE TRANSISTOR

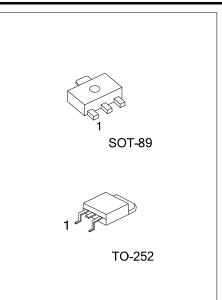
DESCRIPTION

The UTC 2SB1182 is a medium power low voltage transistor, designed for audio power amplifier, DC-DC converter and voltage regulator.

FEATURES

* High current output up to 3A

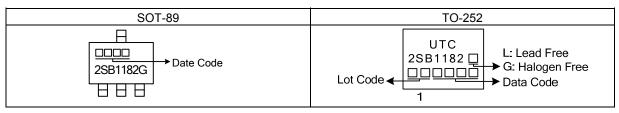
* Low saturation voltage



ORDERING INFORMATION

Ordering Number		Dookago	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	2SB1182G-x-AB3-R	SOT-89	В	С	E	Tape Reel	
2SB1182L-x-TN3-R	2SB1182G-x-TN3-R	TO-252	В	С	E	Tape Reel	
Note: Pin Assignment: B: Base C: Collector E: Emitter							
2SB1182G-x-AB3-R (1)Packing Type (2)Package Type (3)Rank (4)Green Package		(1) R: Tape Reel (2) AB3: SOT-89, TN3: TO-252					
		(3) refer to Classification of hFE2					
		(4) G: Halogen Free and Lead Free, L: Lead Free					

MARKING



■ **ABSOLUTE MAXIMUM RATINGS** (T_A = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		V _{CBO}	-40	V
Collector-Emitter Voltage		V _{CEO}	-32	V
Emitter-Base Voltage		V _{EBO}	-5	V
Collector Current	DC	lc	-2	А
	Pulse	I _{CP}	-3	A
Base Current	<u>.</u>	IB	-0.6	A
Collector Discinction (T -25°C)	SOT-89	D	0.5	W
Collector Dissipation (T _A =25°C)	TO-252	– Pc –	10	W
Junction Temperature	•	TJ	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A= 25°C, unless otherwise specified)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
BV_{CBO}	I _C =-50μΑ	-40			V
BV_{CEO}	I _C =-1mA	-32			V
BV_{EBO}	I _E =-50μΑ	-5			V
I _{CBO}	V _{CB} =-20V			-1	μA
I _{CEO}	V _{CE} =-20V			-1	μA
I _{EBO}	V _{EB} =-4V			-1	μA
h _{FE}	V _{CE} =-3V, I _C =-0.5A	120		390	
V _{CE(SAT)}	I _C =-2A, I _B =-0.2A		-0.5	-0.8	V
V _{BE(SAT)}	I _C =-2A, I _B =-0.2A		-1.0	-2.0	V
f⊤	V _{CE} =-5V, I _E =0.5 A, ,f=100MHz		100		MHz
C _{OB}	V _{CB} =-10V, I _E =0 A,f=1MHz		50		pF
	$\begin{array}{c} BV_{CBO}\\ BV_{CEO}\\ BV_{EBO}\\ \hline \\ I_{CBO}\\ \hline \\ I_{CEO}\\ \hline \\ I_{EBO}\\ \hline \\ h_{FE}\\ \hline \\ V_{CE(SAT)}\\ \hline \\ V_{BE(SAT)}\\ \hline \\ f_T \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

Note 1: Pulse test: P_W < 300µs, Duty Cycle < 2%

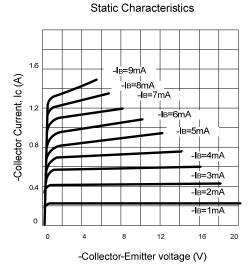
■ CLASSIFICATION OF h_{FE2}

RANK	Q	R
RANGE	120 ~ 270	180 ~ 390

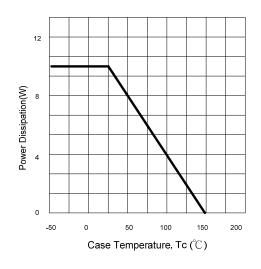


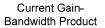
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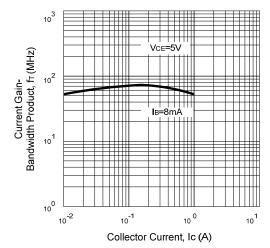
■ TYPICAL CHARACTERICS



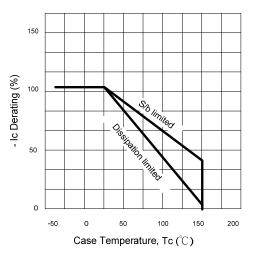
Power Derating



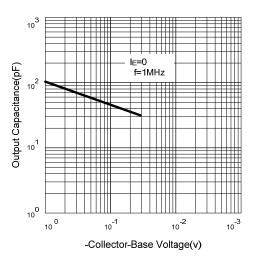


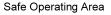


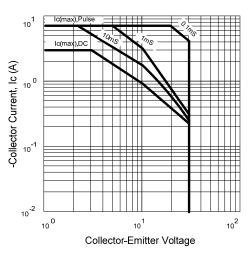
Derating Curve of Safe Operating Areas



Collector Output Capacitance



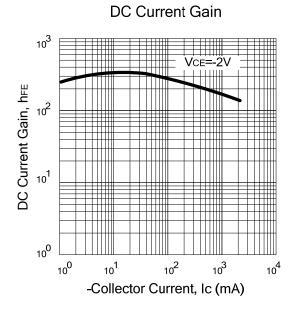




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