

**UTC** UNISONIC TECHNOLOGIES CO., LTD

## 2N6027

# PROGRAMMABLE UNIJUNCTION TRANSISTOR

#### DESCRIPTION

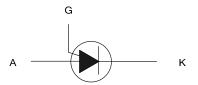
The UTC 2N6027 is a programmable unijunction transistor, it uses UTC's advanced technology to provide customers with low forward voltage, low gate to anode leakage current, low offset voltage and high peak output voltage, etc.

The UTC 2N6027 is suitable for timing, thyristor-trigger, oscillator and pulse circuits, etc.

#### **FEATURES**

- \* Low Forward Voltage
- \* Low Offset Voltage
- \* Low Gate to Anode Leakage Current
- \* High Peak Output Voltage

#### **SYMBOL**



#### **ORDERING INFORMATION**

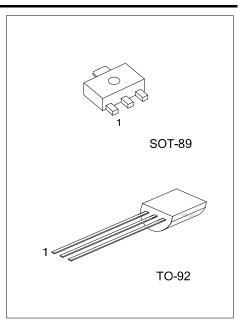
Ordering Number		Daakaga	Pin Assignment			Deaking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
_	2N6027G-AB3-R	SOT-89	А	G	К	Tape Reel	
2N6027L-T92-B	2N6027G-T92-B	TO-92	А	G	К	Tape Box	
2N6027L-T92-K	2N6027G-T92-K	TO-92	Α	G	К	Bulk	

Note: Pin Assignment: A: Anode G: Gate K: Cathode

(2) AB3: SOT-89, T92: TO- (3)Green Package (3) G: Halogen Free and Lo
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### MARKING

SOT-89	TO-92
Data Code 2N6027G → Data Code	UTC 2N6027 P: Halogen Free Data Code



SCR

### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>J</sub>=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Gate to Cathode Forward Volt	age	V <sub>GKF</sub>	40	V
Gate to Cathode Reverse Volt	age	V <sub>GKR</sub>	-5.0	V
Gate to Anode Reverse Voltag	ge	V <sub>GAR</sub>	40	V
DC Forward Anode Current	TJ=25°C		150	mA
DC Forward Anode Current	Derate Above 25°C		2.67	mA/°C
DC Gate Current		l <sub>G</sub>	±50	mA
Repetitive Peak Forward	Pulse Width=100µs		1.0	А
Current (Note 2)	Pulse Width=20 µs	I <sub>TRM</sub>	2.0	А
Non-Repetitive Peak Forward	l Current 10 μs Pulse Width	Jps      2.0        Ise Width      I <sub>TSM</sub> 5.0		А
Anode to Cathode Voltage		V <sub>AK</sub>	± 40	V
Device Dissignation	SOT-89		280	
Power Dissipation	TO-92	P <sub>D</sub>	300	mW
Power Dissipation Derate	SOT-89	1/0	4.45	
Above 25°C	TO-92	1/θ <sub>JA</sub>	4.0	mW/°C
Operating Junction Temperatu	ure Range	T」 —50 ~ +100		°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Duty Cycle ≤ 1%

3. Anode positive, R<sub>GA</sub>=1000 ohms

Anode negative, R<sub>GA</sub>=Open

## THERMAL CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT	
lunction to Ambient	SOT-89	0	220	°C 1.11	
Junction to Ambient	TO-92	$\theta_{JA}$		°C/W	
lunation to Case	SOT-89	$\theta_{\text{JC}}$	80	°C/M	
Junction to Case	TO-92		75	°C/W	

#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub>=25°C unless otherwise noted)

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Peak Current		$V_{S}$ =10V, $R_{G}$ =1M $\Omega$		1.25	25  2.0    .0  5.0    70  1.6    8  50    50	μA
Peak Current	IP	V <sub>S</sub> =10V, R <sub>G</sub> =10kΩ	1.25      2.0      4.0        4.0      5.0      4.0        0.2      0.70      1.6        18      50      4.0        70      150      4.0        1.5      1.0      1.0        1.5      1.0      10        1.6      3.0      10        1      5.0      50	μA		
Offset Voltage	VT	$V_{S}$ =10V, $R_{G}$ =1M $\Omega$	0.2	0.70	1.6	V
		$V_{S}$ =10V, $R_{G}$ =1M $\Omega$		18	50	μA
Valley Current	Iv	V <sub>S</sub> =10V, R <sub>G</sub> =10kΩ	70	150		μA
		V <sub>S</sub> =10V, R <sub>G</sub> =200Ω	1.5			mA
Gate to Anode Leakage		V <sub>S</sub> =40V, T <sub>A</sub> =25°C, Cathode Open		1.0	10	nA
Current	I <sub>GAO</sub>	$\label{eq:V_s} \begin{split} &V_{\rm S} = 10 V, \ R_{\rm G} = 10 k \Omega \\ &V_{\rm S} = 10 V, \ R_{\rm G} = 10 M \Omega \\ &V_{\rm S} = 10 V, \ R_{\rm G} = 10 M \Omega \\ &V_{\rm S} = 10 V, \ R_{\rm G} = 10 k \Omega \\ &V_{\rm S} = 10 V, \ R_{\rm G} = 200 \Omega \end{split}$		3.0		nA
Gate to Cathode Leakage Current	I <sub>GKS</sub>	$V_S$ = 40V, Anode to Cathode Shorted		5.0	50	nA
Forward Voltage (Note)	V <sub>F</sub>	I <sub>F</sub> =50mA Peak		0.8	1.5	V
Peak Output Voltage	Vo	V <sub>G</sub> =20V, C <sub>C</sub> =0.2µF	60	11		V
Pulse Voltage Rise Time	tr	V <sub>B</sub> =20V, C <sub>C</sub> =0.2µF		40	80	ns

Note: Pulse Test: Pulse Width≤300µsec, Duty Cycle ≤ 2%



## ■ TEST CIRCUITS AND TYPICAL CHARACTERISTICS

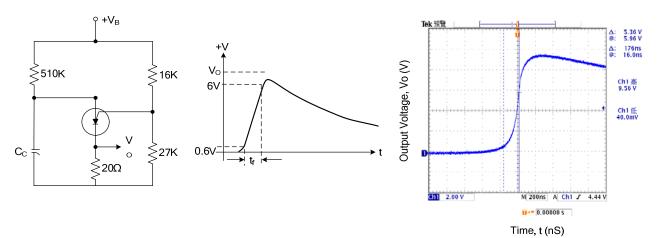
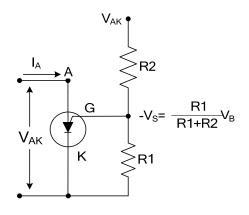
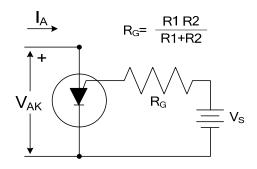
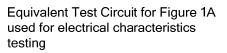


Fig 1. Output Voltage and Rise Time Test Circuit

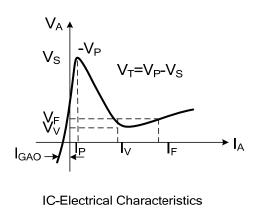


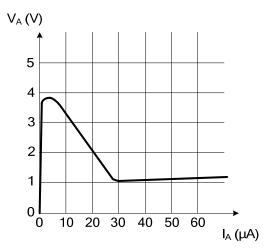
Programmable Unijunction with "Program"Resistors R1 and R2





**Electrical Characteristics** 









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