Dual P-Channel Enhancement Mode Power MOSFET

DESCRIPTION

The HM6801 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

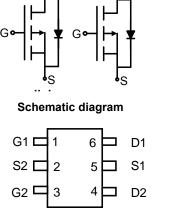
• V_{DS} = -30V,I_D = -4.2A

 $R_{DS(ON)} < 130 m\Omega @ V_{GS} = -2.5 V$

- $R_{DS(ON)} < 75m\Omega @ V_{GS}=-4.5V$
- $R_{DS(ON)} < 65m\Omega @ V_{GS}=-10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- ●PWM applications
- Load switch
- Power management



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Marking and pin Assignment



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
6801	HM6801	SOT-23-6L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

U .			1
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	-4.2	А
Drain Current-Pulsed (Note 1)	I _{DM}	-30	А
Maximum Power Dissipation	PD	1.2	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)Reja104°C/W

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-30		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA

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Gate-Body Leakage Current	I _{GSS}	I _{GSS} V _{GS} =±20V,V _{DS} =0V		-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA		-1	-1.3	V
	R _{DS(ON)}	V _{GS} =-10V, I _D =-4.2A	-	50	55	mΩ
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-4A	-	64	72	mΩ
		V _{GS} =-2.5V, I _D =-1A		95	120	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-4.2A	-	10	-	S
Dynamic Characteristics (Note4)	·					
Input Capacitance	C _{lss}	(-15)()(-0)(-	950	-	PF
Output Capacitance	C _{oss}	- V _{DS} =-15V,V _{GS} =0V, - F=1.0MHz	-	115	-	PF
Reverse Transfer Capacitance	C _{rss}	F = 1.0101112	-	75	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	7	-	nS
Turn-on Rise Time	tr	V _{DD} =-15V,I _D =-3.2A	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V,R _{GEN} =6 Ω	-	30	-	nS
Turn-Off Fall Time	t _f		-	12	-	nS
Total Gate Charge	Qg		-	9.5	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-4A,V _{GS} =-4.5V	-	2	-	nC
Gate-Drain Charge	Q _{gd}		-	3	-	nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

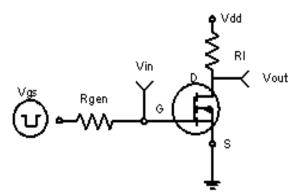
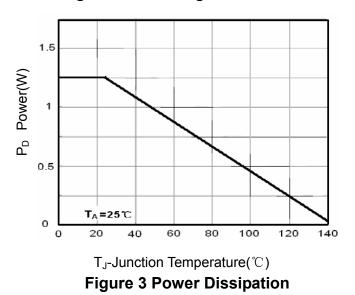
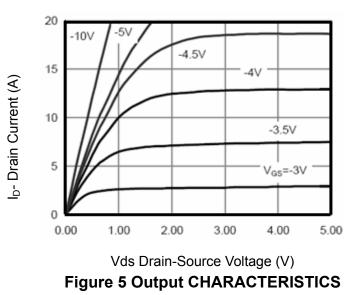
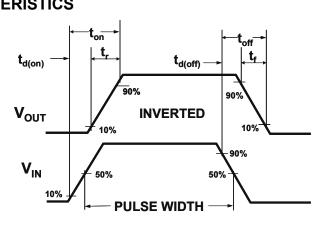


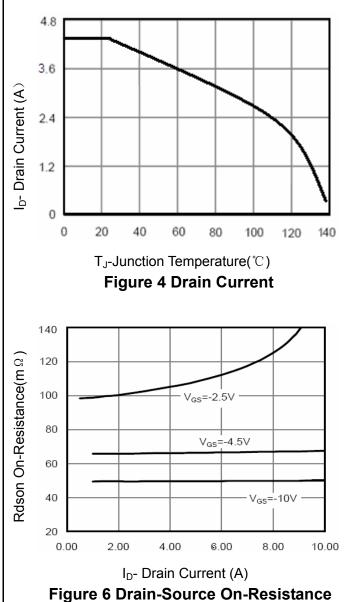
Figure 1:Switching Test Circuit





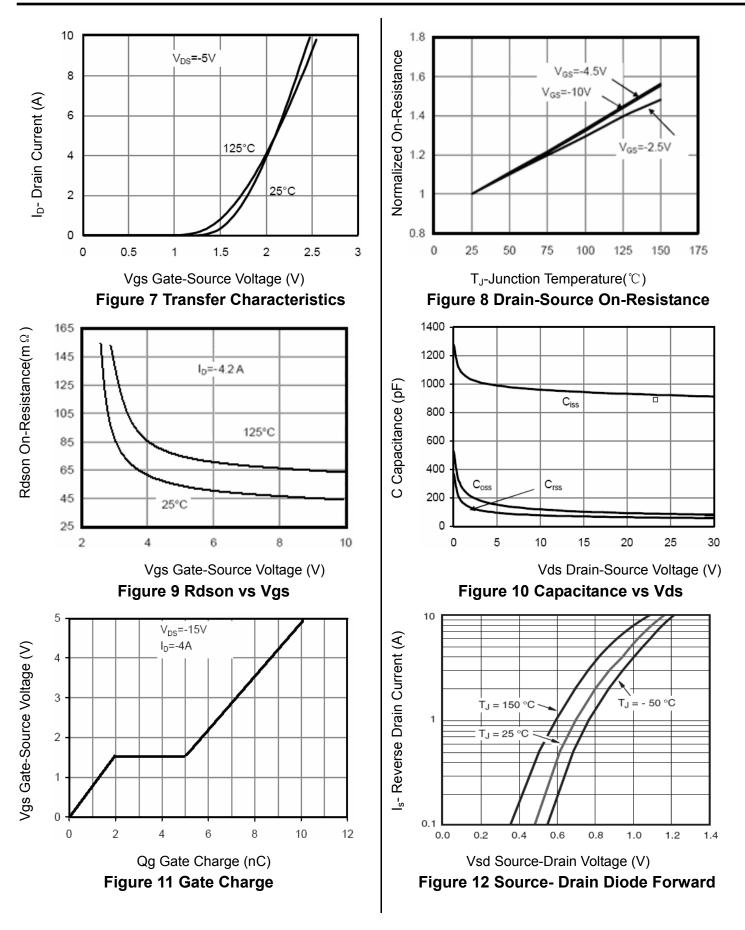






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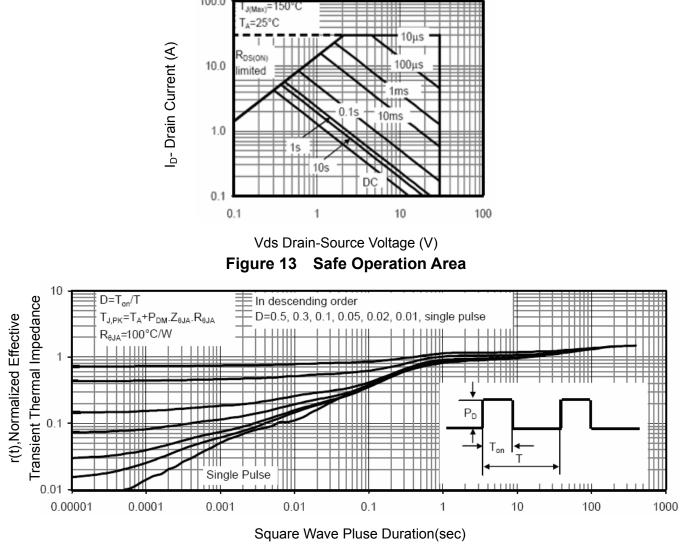
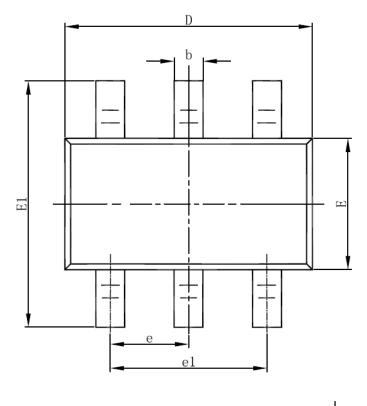
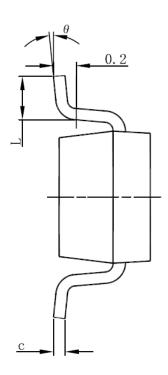
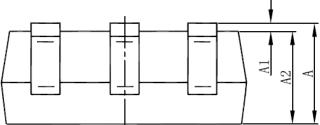


Figure 14 Normalized Maximum Transient Thermal Impedance







Symbol	Dimensions Ir	n Millimeters	Dimensions In Inches		
	Min	Max	Min	Max	
A	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

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