Dual P-Channel Enhancement Mode Power MOSFET

DESCRIPTION

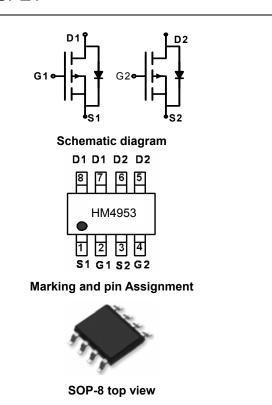
The HM4953 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a load switch or in PWM applications.

GENERAL FEATURES

- $V_{DS} = -30V, I_D = -5.1A$ $R_{DS(ON)} < 105m\Omega @ V_{GS} = -4.5V$ $R_{DS(ON)} < 55m\Omega @ V_{GS} = -10V$
- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

Application

- ●PWM applications
- Load switch
- Power management



Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4953	HM4953	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	-5.1	А
Drain Current-Pulsed (Note 1)	I _{DM}	-20	А
Maximum Power Dissipation	PD	2.5	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	50	°C /W
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Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA

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HM4953

Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	·					•
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250µA		-1.6	-3	V
	R _{DS(ON)}	V _{GS} =-10V, I _D =-5.1A	-	48	55	mΩ
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-4.2A	-	73	105	mΩ
Forward Transconductance	g fs	V _{DS} =-15V,I _D =-4.5A	4	7	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	(-15)()(-0)(-	1040	-	PF
Output Capacitance	C _{oss}	- V _{DS} =-15V,V _{GS} =0V, - F=1.0MHz	-	420	-	PF
Reverse Transfer Capacitance	C _{rss}	r = 1.0ivii 12	-	150	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	15	-	nS
Turn-on Rise Time	tr	V _{DD} =-15V, ID=-1A,	-	13	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	58	-	nS
Turn-Off Fall Time	t _f		-	21	-	nS
Total Gate Charge	Qg		-	12	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-5.1A,V _{GS} =-10V	-	2.2	-	nC
Gate-Drain Charge	Q _{gd}		-	3	-	nC
Drain-Source Diode Characteristics	·					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-1.7A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

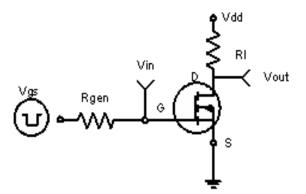
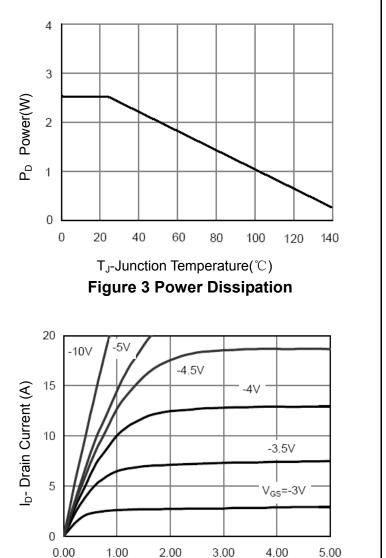
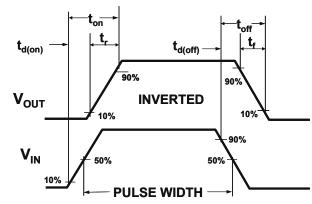


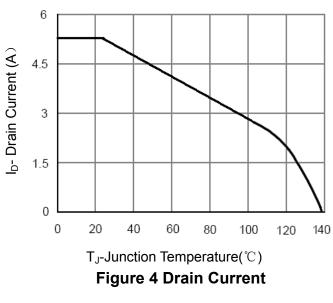
Figure 1:Switching Test Circuit

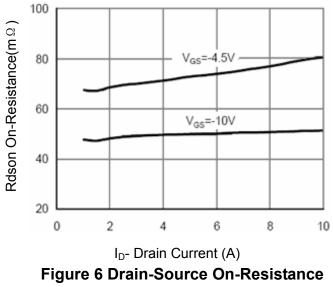


Vds Drain-Source Voltage (V) Figure 5 Output CHARACTERISTICS

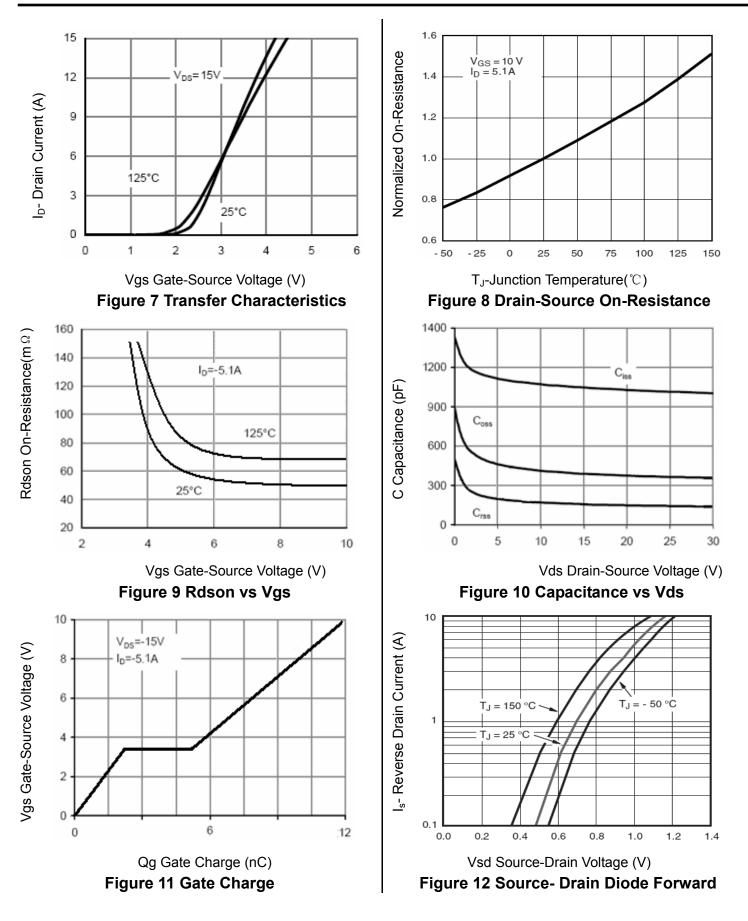












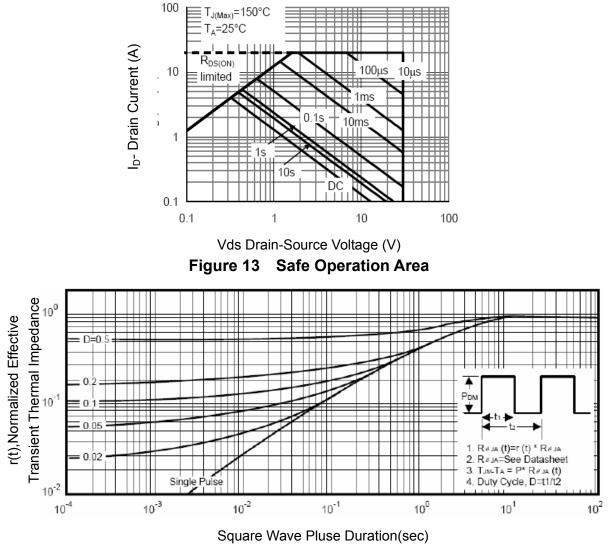
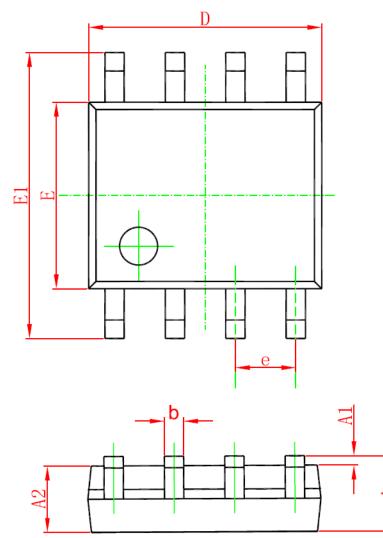
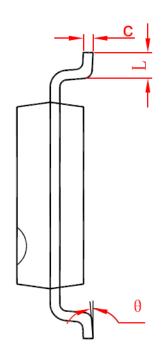


Figure 14 Normalized Maximum Transient Thermal Impedance

SOP-8 PACKAGE IN FORMATION





C. m.h. a l	Dimensions Ir	n Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
A	1. 350	1. 750	0. 053	0. 069	
A1	0. 100	0. 250	0.004	0.010	
A2	1. 350	1.550	0.053	0. 061	
b	0. 330	0. 510	0.013	0. 020	
с	0. 170	0. 250	0.006	0.010	
D	4. 700	5. 100	0. 185	0. 200	
E	3.800	4.000	0. 150	0. 157	
E1	5. 800	6. 200	0. 228	0. 244	
е	1. 270	(BSC)	0. 050 (BSC)		
L	0. 400	1. 270	0.016	0. 050	
θ	0°	8°	0°	8°	

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